



NEX90015BDG-Q100 150 mA, 40 V low Iq (5.3 μ A) LDO evaluation board



Abstract: This user manual describes the NEX90015BDG-Q100 evaluation board (EVB). The NEX90015BDG-Q100 is a 40 V, 150 mA low-dropout (LDO) linear regulator with only 5.3 μ A typical quiescent current at light load for automotive always on/stand by applications. This document contains the EVB schematic and configuration, bill of materials (BOM) and board layouts.

Keywords: NEX90015BDG-Q100, LDO linear regulator, ultra-low operating quiescent current, evaluation board

1. Introduction

This evaluation board is designed for NEX90015BDG-Q100. The NEX90015BDG-Q100 device is an LDO designed for up to 40 V input voltage with maximum 150 mA output current. The EVB helps engineers to evaluate the operation and performance of NEX90015BDG-Q100.

1.1. Features

The following features are available on this EVB:

- Input voltage range: 3 V to 40 V
- Output voltage and current:
 - 3.3 V/150 mA
 - 5 V/150 mA

1.2. Applications

NEX90015BDG-Q100 is used in the following applications:

- Body control modules (BCMs)
- Automotive lighting
- Automotive head units & clusters
- Telematics control units
- Powertrain of electric vehicles (EV) and hybrid electric vehicles (HEV)

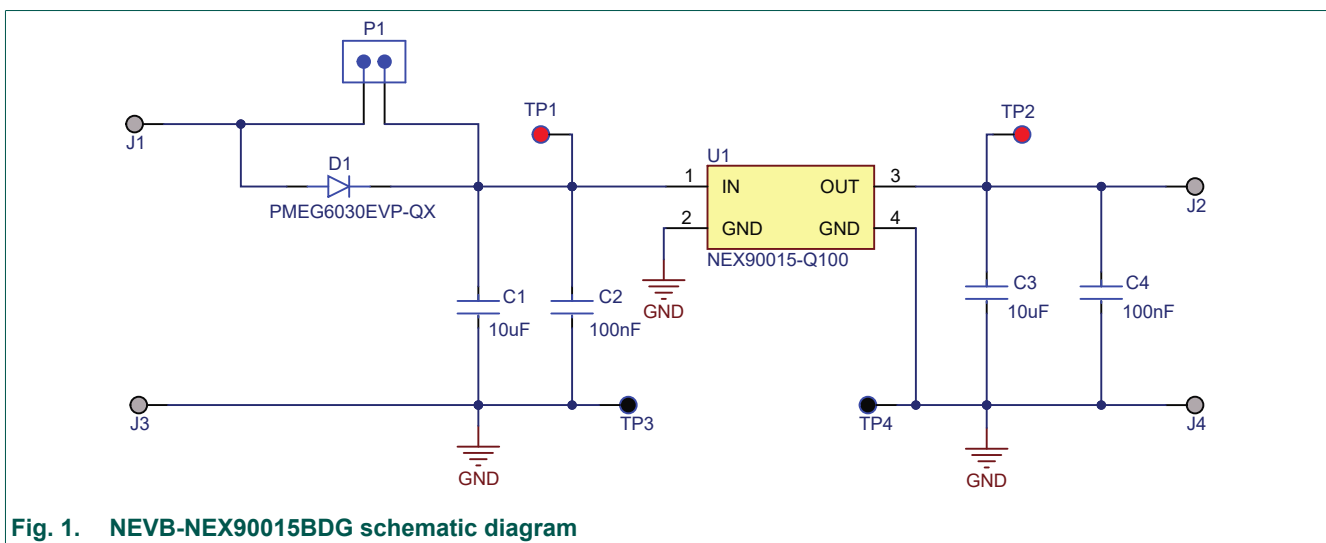
1.3. Target EVBs for this document

This user manual is applicable to the EVBs listed in [Table 1](#).

Table 1. Target EVBs and type numbers

Target EVBs	Type number
NEVB-NEX90015BDG	NEX90015BDG-Q100
NEVB-NEX90015ADG	NEX90015ADG-Q100

2. Schematic



3. General configuration and description

This section describes the connectors and test points on the EVB and how to properly connect, set up and use the NEVB-NEX90015BDG.

3.1. Physical access

[Table 2](#) lists the NEVB-NEX90015BDG connectors and test point functionality.

Table 2. Connectors and test points

Connector	Label	Descriptions
J1	VBAT	This connector is the input of the EVB
J2	OUT	This connector is the output of the EVB
J3, J4	GND	These connectors are the ground connector of the EVB
P1	N/A	This jumper is for bypass of reverse diode D1
TP1	IN	Device input test point
TP2	OUT	Output test point
TP3, TP4	GND	Ground test points

3.2. Test setup

The following steps show how to set up this EVB.

1. P1 is floating to connect the reverse diode or short together to bypass the reverse diode.
2. Connect a power supply with positive voltage between J1 (VBAT) and J3 (GND) connectors, and ensure that the input range is 3 V to 40 V.
3. Connect a load from 0 mA to 150 mA between the J2 (OUT) and J4 (GND) connectors.
4. Turn on the input power supply.
5. Measure the respective parameters by using test points (TP1 to TP4).

4. PCB layout

Figure 2 and Figure 3 show the PCB layouts for the NEVB-NEX90015BDG.

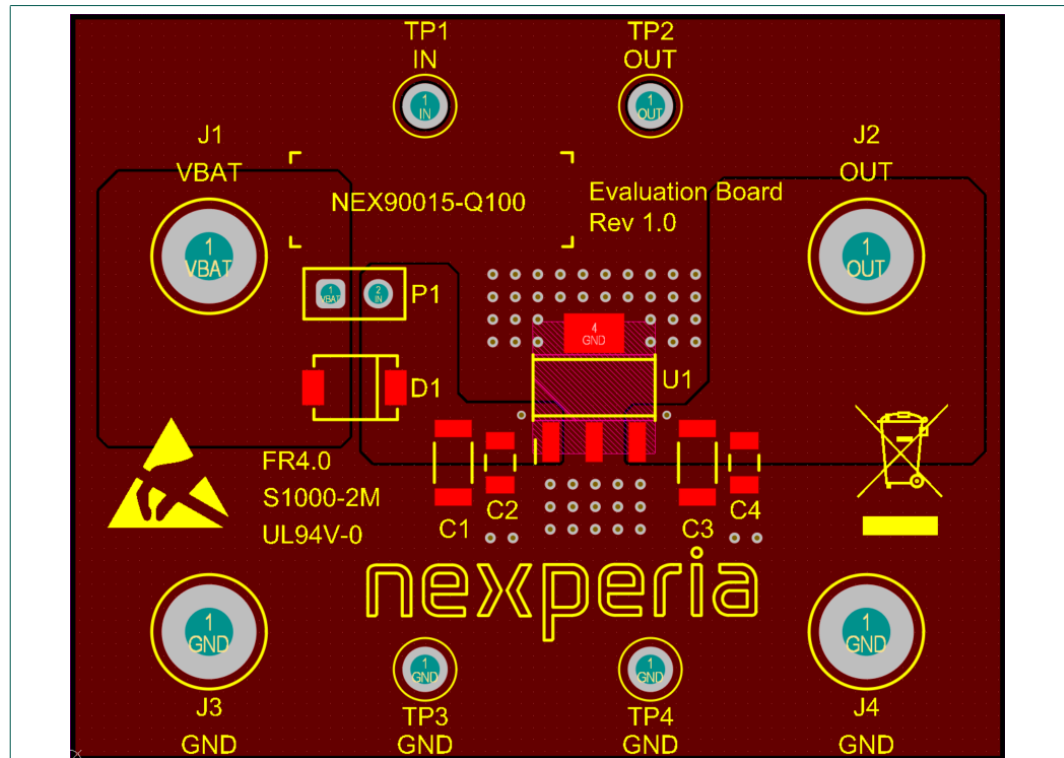


Fig. 2. NEVB-NEX90015BDG top layer routing

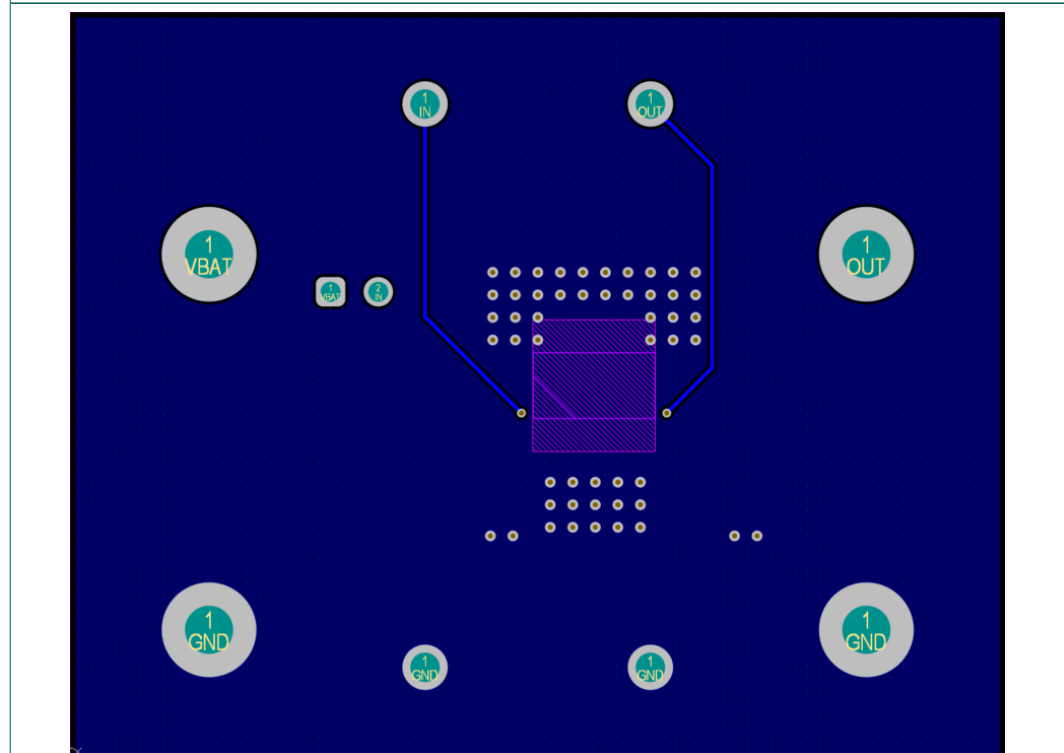


Fig. 3. NEVB-NEX90015BDG bottom layer routing

5. Bill of materials

[Table 2](#) details the bill of materials of NEVB-NEX90015BDG.

Table 3. Bill of materials (BOM)

Designator	Value	Description	Quantity	Part number	Manufacturer
C1	10 μ F	Cap Ceramic 10 μ F 50 V X7R 10% Pad SMD 1206 125 °C Automotive T/R	1	CGA5L1X7R1H106KT0Y0N	TDK
C2, C4	100 nF	Cap Ceramic 100 nF 100V X7R 10% Pad SMD 0805 125 °C Automotive T/R	2	CGA4J2X7R2A104K125AA	TDK
C3	10 μ F	Cap Ceramic 10 μ F 16 V X7R 10% Pad SMD 1206 125 °C Automotive T/R	1	CGA5L1X7R1C106K160AC	TDK
D1	SMA	PMEG6030EVP-Q - High-temperature 60 V, 3 A Schottky barrier rectifier	1	PMEG6030EVP-QX	Nexperia
J1, J2, J3, J4	TH	Terminal DBL Turret, Through Hole, RoHS	4	1502-2	Keystone Electronics
P1	TH	CONN HEADER VERT 2POS 2.54 MM	1	61150211121	We-online
R1	10 k Ω	RES Thick Film, 10 k Ω , 5%, 0.1 W, 200 ppm/°C, 0603	1	RC0603FR-0710KL	YAGEO
TP1, TP2	TH	PC TEST POINT COMPACT BLACK	2	5005	Keystone Electronics
TP3, TP4	TH	PC TEST POINT COMPACT BLACK	2	5006	Keystone Electronics
U1	-	150 mA High Voltage Ultra-Low Iq Low Dropout Regulator	1	NEX90015-Q100	Nexperia

6. Revision history

Table 4. Revision history

Revision number	Release date	Description
UM90088 v. 1	20260128	Initial version

7. Legal information

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