ne<mark>x</mark>peria

Important notice

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On 7 February 2017 the former NXP Standard Product business became a new company with the tradename **Nexperia**. Nexperia is an industry leading supplier of Discrete, Logic and PowerMOS semiconductors with its focus on the automotive, industrial, computing, consumer and wearable application markets

In data sheets and application notes which still contain NXP or Philips Semiconductors references, use the references to Nexperia, as shown below.

Instead of <u>http://www.nxp.com</u>, <u>http://www.philips.com/</u> or <u>http://www.semiconductors.philips.com/</u>, use <u>http://www.nexperia.com</u>

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If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia



SPICE thermal model

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base		-	-	0.74	K/W
	Cth ₁	2.480E-01 F		\bigtriangleup		
	Cth ₂	2.300E-02 F			<u>tj</u>	
	Cth ₃	3.726E-03 F				
	Cth ₄	6.145E-04 F				_
	Cth ₅	1.013E-04 F			$\int_{R^{th_1}} \overset{\bullet}{=} \operatorname{Cth}_1$	I
	Cth ₆	1.671E-05 F				
	Cth ₇	2.756E-06 F			ᡗᢩ᠆᠊᠆᠋	
	Cth ₈	4.546E-07 F			$\int Rth_2 + Cth_2$	2
	Rth₁	4.030E-01 Ω				
	Rth ₂	2.260Ε-01 Ω				
	Rth ₃	7.000Ε-02 Ω				3
	Rth ₄	2.200Ε-02 Ω			᠊ᡗ᠋	
	Rth₅	6.687E-03 Ω			┎─╺╋──┐	
	Rth ₆	2.069Ε-03 Ω				4
	Rth ₇	6.400Ε-04 Ω				÷
	Rth ₈	1.980Ε-04 Ω		(Р)		
	U U			\bigvee	∽┓	
					$\int Rth_5 = Cth_{f}$	5
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					Rth6 + Cthe	3
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Part: /ersion:	BUK754R0-40C 1					3
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Date:	17-9-2010			· · · · •] •	
Rth	0.735337816			\checkmark	t _{amb}	_
					001aal768	3

www.nxp.com

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