



PESD1V2Y1BSF

Extremely low clamping bidirectional ESD protection diode

6 April 2020

Short data sheet

1. General description

Extremely low clamping, extremely low capacitance bidirectional ElectroStatic Discharge (ESD) protection diode, which is part of the TrEOS protection family. The device is housed in a DSN0603-2 (SOD962-2) leadless ultra small Surface-Mounted Device (SMD) package designed to protect one signal line from damage caused by ESD and other transients.

2. Features and benefits

- Bidirectional ESD protection of one line
- Extremely low diode capacitance $C_d = 0.26$ pF typ.
- Extremely low clamping voltage to protect sensitive transceivers
- Extremely low leakage current $I_{RM} < 1$ nA typ. at 1.2 V
- Extremely low inductance protection path to ground
- Low trigger voltage $V_{t1} = 3.4$ V TLP
- Ultra small SMD package

3. Applications

- Cellular handsets and accessories
- Portable electronics
- Communication systems
- Computers and peripherals

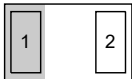
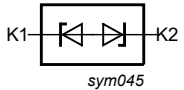
4. Quick reference data

Table 1. Quick reference data

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|-----------|--------------------------|---|------|------|------|------|
| V_{RWM} | reverse standoff voltage | | -1.2 | - | 1.2 | V |
| C_d | diode capacitance | $f = 1$ MHz; $V_R = 0$ V; $T_{amb} = 25$ °C | - | 0.26 | 0.31 | pF |

5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-------------|--|---|
| 1 | K1 | cathode |  <p>Transparent top view</p> <p>DSN0603-2 (SOD962-2)</p> |  <p>sym045</p> |
| 2 | K2 | cathode | | |

6. Ordering information

Table 3. Ordering information

| Type number | Package | | |
|--------------|-----------|---|----------|
| | Name | Description | Version |
| PESD1V2Y1BSF | DSN0603-2 | silicon, leadless ultra small package; 2 terminals; 0.4 mm pitch; 0.6 mm x 0.3 mm x 0.3 mm body | SOD962-2 |

7. Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.

The device uses an advanced clamping structure showing a negative dynamic resistance. This snap-back behavior strongly reduces the clamping voltage to the system behind the ESD protection during an ESD event. Do not connect unlimited DC current sources to the data lines to avoid keeping the ESD protection device in snap-back state after exceeding breakdown voltage (due to an ESD pulse for instance).

Due to the very low trigger voltage V_{t1} , it needs to be ensured that V_{RWM} is not exceeded under normal application conditions. If the device is used in an USB 3.2 environment, place the device between the AC coupling capacitor and the transceiver. Refer to the USB 3.2 specification for maximum working voltages.

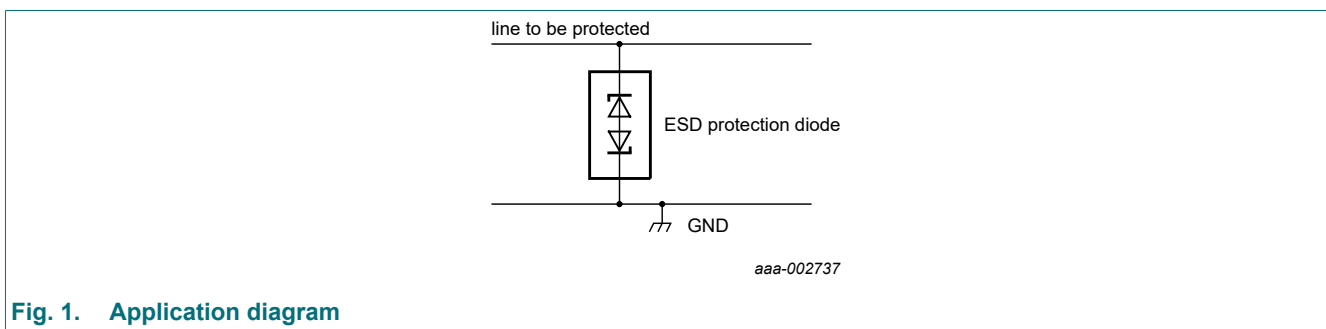


Fig. 1. Application diagram

Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

8. Package outline

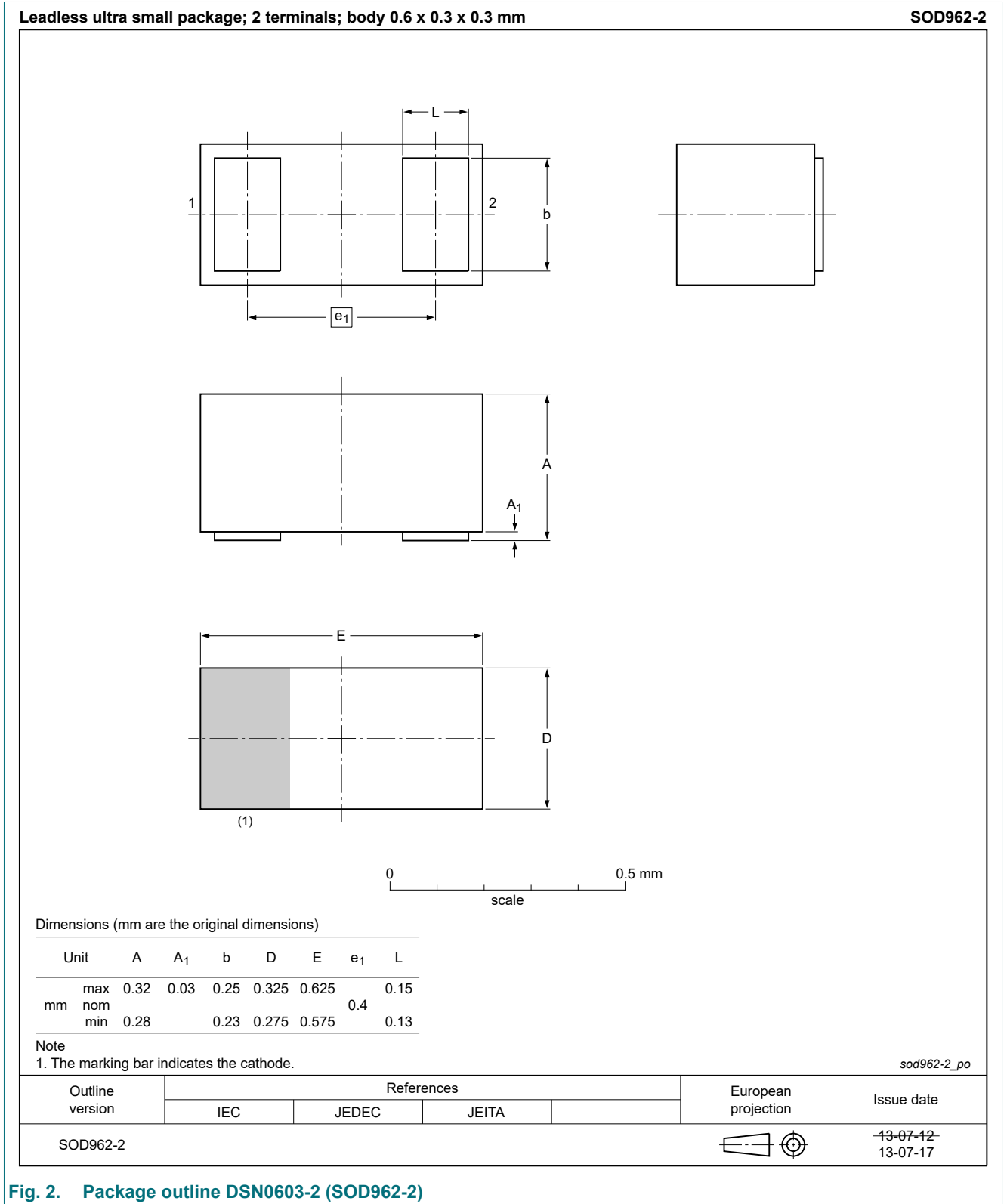


Fig. 2. Package outline DSN0603-2 (SOD962-2)

9. Soldering

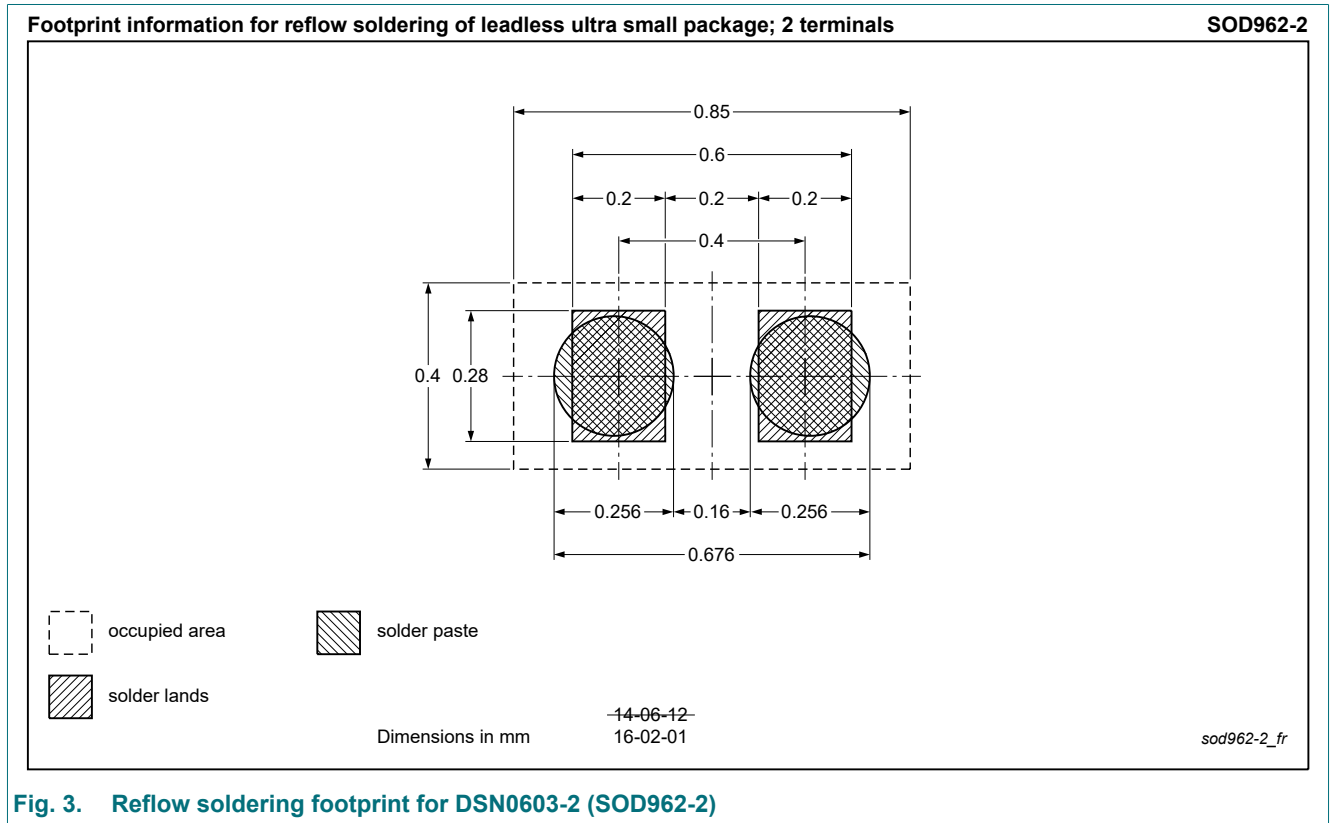


Fig. 3. Reflow soldering footprint for DSN0603-2 (SOD962-2)

10. Revision history

Table 4. Revision history

| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes |
|----------------------|--------------|-------------------|---------------|------------|
| PESD1V2Y1BSF_SDS v.1 | 20200406 | Short data sheet | - | - |

11. Legal information

Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------|--------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

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- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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