

Quarterly Reliability Monitoring Results

Quarters: Q1/2022 to Q4/2023

Based on structural similarity

Supplier Nexperia B.V. Name of Laboratory Assembly reliability labs Based on AEC-Q101 Test		User Part Number						
		PESD2USB5UVT-Q						
		Part Description						
		NXP ICN8 Protection INDI						
		SMD package						
		Test Conditions	Duration	# Lots	# Quantity	# Rejects		
	TEST							
	Pre- and Post-Stress							
# E1	Electrical Test	Tamb = 25 °C	N/A	see below	all parts	see below		
		JESD22-A113						
		Bake Tamb = 125 °C	24 hours					
	PC	Soak Tamb = 85 °C, RH = 85%	168 hours					
# A1	Preconditioning	Reflow soldering	3 cycles	524	22940	0		
		MIL-STD-750-1						
	HTRB	M1038 Method A						
		Tj = Tjmax, Vr = 100% of max. datasheet						
# B1	Bias	reverse voltage	1000 hours	30	1280	0		
		JECD22 A104						
	TC Temperature Cycling	JESD22-A104 -65 °C to Timax, not to exceed 150°C	1000	456	7000	•		
# A4	remperature Cycling	-03 C to Tjillax, flot to exceed 130 C	1000 cycles	156	7080	0		
	UHAST	JESD22-A118						
# A3 or	Unbiased HAST	Tamb = 130 °C, RH = 85 %	— 96 hours	156	7080	0		
# A3 01	011010000 11710 1	JESD22-A102						
	AC	Tamb = 121 °C, RH = 100 %						
# A3 alt	Autoclave	Pressure = 205 kPa (29.7 psia)						
" NO dic								
	H3TRB	JESD22-A101						
	High Humidity High	Tamb = 85 °C, RH = 85%, VR = 80 % of						
# A2 alt		rated reverse voltage ^[1]	1000 hours	156	7080	0		
		MIL-STD-750 Method 1037						
	IOL	ton = toff, devices powered to insure ΔTj =						
# A5	Intermittent Operating Life	100 °C for 15000 cycles	1000 hours	n.a.	n.a.	n.a.		
	RSH	JESD22-A111						
# C8	Resistance to Solder Heat	260 °C ± 5 °C	10 s	56	1700	0		
	SD							
# C10	Solderability	J-STD-002		56	1700	0		

^[1] The maximum applied voltage is limited by test chamber set up and does not exceed 115V.

Calculation of FIT and MTTF

Test considered for FIT calculation: High Temperature Reverse Bias (HTRB, Test #B1) Confidence level 60%, derated to 55 °C, activation energy 0.7 eV, test time 168 to 1000 hours

Wafer Fab	Technology	Quantity	Rejects	Failure Rate (FIT)	MTTF (hrs)
NXP ICN8	Protection INDI	1280	0	3,32	3,01E+08

© 2024 Nexperia B.V.

All information hereunder is per Nexperia's best knowledge. This document does not provide for any representation or warranty express or implied by Nexperia. In case Nexperia has tested the product, this documentation reflects the outcome of the analysis of the actually tested parts only.

nexperia.com