

## **Quarterly Reliability Monitoring Results**

## Quarters: Q1/2022 to Q4/2023

Based on structural similarity

| Supplier Nexperia B.V. Name of Laboratory Assembly reliability labs Based on AEC-Q101 Test |                                                                | User Part Number<br>BC51-10PA-Q                                                               |                                   |           |            |           |  |  |
|--------------------------------------------------------------------------------------------|----------------------------------------------------------------|-----------------------------------------------------------------------------------------------|-----------------------------------|-----------|------------|-----------|--|--|
|                                                                                            |                                                                |                                                                                               |                                   |           |            |           |  |  |
|                                                                                            |                                                                | Nexperia DHAM Small Signal Bipolar Transistor                                                 |                                   |           |            |           |  |  |
|                                                                                            |                                                                | MCD package                                                                                   |                                   |           |            |           |  |  |
|                                                                                            |                                                                | Test Conditions                                                                               | Duration                          | # Lots    | # Quantity | # Rejects |  |  |
|                                                                                            |                                                                |                                                                                               | TEST                              |           |            |           |  |  |
|                                                                                            | Pre- and Post-Stress                                           |                                                                                               |                                   |           |            |           |  |  |
| # E1                                                                                       | Electrical Test                                                | Tamb = 25 °C                                                                                  | N/A                               | see below | all parts  | see below |  |  |
| # A1                                                                                       | PC Preconditioning                                             | JESD22-A113 Bake Tamb = 125 °C Soak Tamb = 85 °C, RH = 85% Reflow soldering                   | 24 hours<br>168 hours<br>3 cycles | 464       | 20960      | 0         |  |  |
| # A1                                                                                       | Treconditioning                                                |                                                                                               | 5 cycles                          | 404       | 20960      | U         |  |  |
| # B1                                                                                       | <b>HTRB</b> High Temperature Reverse Bias                      | MIL-STD-750-1<br>M1039 Method A<br>Tj = Tjmax, Vr = 100% of max. datasheet<br>reverse voltage | 1000 hours                        | 415       | 18680      | 0         |  |  |
|                                                                                            |                                                                |                                                                                               |                                   |           |            |           |  |  |
| # A4                                                                                       | <b>TC</b><br>Temperature Cycling                               | JESD22-A104<br>-65 °C to Tjmax, not to exceed 150°C                                           | 1000 cycles                       | 116       | 5240       | 0         |  |  |
| # A3 <b>or</b>                                                                             | <b>UHAST</b><br>Unbiased HAST                                  | JESD22-A118<br>Tamb = 130 °C, RH = 85 %                                                       | — 96 hours                        | 116       | 5240       | 0         |  |  |
| # A3 alt                                                                                   | <b>AC</b><br>Autoclave                                         | JESD22-A102<br>Tamb = 121 °C, RH = 100 %<br>Pressure = 205 kPa (29.7 psia)                    |                                   |           |            |           |  |  |
| # A2 alt                                                                                   | <b>H3TRB</b><br>High Humidity High<br>Temperature Reverse Bias | JESD22-A101<br>Tamb = 85 °C, RH = 85%, VR = 80 % of rated reverse voltage <sup>[1]</sup>      | 1000 hours                        | 116       | 5240       | 0         |  |  |
|                                                                                            | IOL                                                            | MIL-STD-750 Method 1037<br>ton = toff, devices powered to insure $\Delta Tj$ =                |                                   |           |            |           |  |  |
| # A5                                                                                       | Intermittent Operating Life                                    | 100 °C 101' 15000 cycles                                                                      | 1000 hours                        | 116       | 5240       | 0         |  |  |
| # C8                                                                                       | <b>RSH</b><br>Resistance to Solder Heat                        | JESD22-A111<br>260 °C ± 5 °C                                                                  | 10 s                              | n.a.      | n.a.       | n.a.      |  |  |
| # C10                                                                                      | <b>SD</b><br>Solderability                                     | J-STD-002                                                                                     |                                   | 86        | 2580       | 0         |  |  |

<sup>[1]</sup> The maximum applied voltage is limited by test chamber set up and does not exceed 115V.

## **Calculation of FIT and MTTF**

Test considered for FIT calculation: High Temperature Reverse Bias (HTRB, Test #B1) Confidence level 60%, derated to 55 °C, activation energy 0.7 eV, test time 168 to 1000 hours

| Wafer Fab | Technology           | Quantity | Rejects | Failure Rate (FIT) | MTTF (hrs) |
|-----------|----------------------|----------|---------|--------------------|------------|
| Nexperia  | Small Signal Bipolar |          |         |                    |            |
| DHAM      | Transistor           | 18680    | 0       | 0,23               | 4,40E+09   |

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