

Quarterly Reliability Monitoring Results

Quarters: Q3/2021 to Q4/2022

Based on structural similarity

Supplier Nexperia B.V.		User Part Number						
		2PD601ARW						
Name of Laboratory Assembly reliability labs		Part Description						
		Nexperia DHAM Small Signal Bipolar Transistor						
		SMD package						
Test		Test Conditions	Duration	# Lots	# Quantity	# Rejects		
	TEST Pre- and Post-Stress							
# 1	Electrical Test	Tamb = 25 °C	N/A	see below	all parts	see below		
# 2	PC Preconditioning	JESD22-A113 Bake Tamb = 125 °C Soak Tamb = 85 °C, RH = 85% Reflow soldering	24 hours 168 hours 3 cycles	1265	69890	0		
# 5	HTRB High Temperature Reverse Bias	MIL-STD-750-1 M1039 Method A Tj = Tjmax, Vr = 100% of max. datasheet reverse voltage	1000 hours	316	18920	0		
# 7	TC Temperature Cycling	JESD22-A104 -65 °C to Tjmax, not to exceed 150°C	200 cycles	260	15680	0		
# 8 or	UHAST Unbiased HAST	JESD22-A118 Tamb = 130 °C, RH = 85 %	— 96 hours	270	16360	0		
# 8a	AC Autoclave	JESD22-A102 Tamb = 121 °C, RH = 100 % Pressure = 205 kPa (29.7 psia)						
# 9	H3TRB High Humidity High Temperature Reverse Bias	JESD22-A101 Tamb = 85 °C, RH = 85%, VR = 80 % of rated reverse voltage $^{[1]}$	1000 hours	262	15760	0		
# 10	IOL Intermittent Operating Life	MIL-STD-750 Method 1037 ton = toff, devices powered to insure $\Delta Tj = 100$ °C for 15000 cycles	333 hours	262	15760	0		
# 20	RSH Resistance to Solder Heat	JESD22-A111 260 °C ± 5 °C	10 s	211	6330	0		
# 21	SD Solderability	J-STD-002		468	4680	0		

^[1]The maximum applied voltage is limited by test chamber set up and does not exceed 115V.

Calculation of FIT and MTTF

Test considered for FIT calculation: High Temperature Reverse Bias (HTRB, Test # 5) Confidence level 60%, derated to 55 °C, activation energy 0.7 eV, test time 168 to 1000 hours

Wafer Fab	Technology	Quantity	Rejects	Failure Rate (FIT)	MTTF (hrs)
Nexperia	Small Signal Bipolar				
DHAM	Transistor	18920	0	0,22	4,46E+09

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