

# WLCSP8\_SOT8068

wafer level chip-scale package; 8 bumps; 1.42 × 0.72 × 0.465 mm body

25 January 2024

Package information

## 1. Package summary

<b>Terminal position code</b>	B (bottom)
<b>Package type descriptive code</b>	WLCSP8_SOT8068
<b>Package type industry code</b>	WLCSP8_SOT8068
<b>Package style descriptive code</b>	WLCSP (wafer level chip-size package)
<b>Package body material type</b>	X (other)
<b>Mounting method type</b>	S (surface mount)
<b>Number of package outline detail graphic references</b>	0
<b>Issue date</b>	22-1-2024
<b>Manufacturer package code</b>	WLCSP8_SOT8068
<b>Footprint dimensions (mm)</b>	1.6 x 0.9
<b>Footprint area (mm<sup>2</sup>)</b>	1.44
<b>Package dimensions including leads (l x w) (mm)</b>	1.42 x 0.72
<b>Package area (mm<sup>2</sup>)</b>	1.022

Table 1. Package summary

Symbol	Parameter	Min	Typ	Nom	Max	Unit
D	package length	-	-	1.42	-	mm
E	package width	-	-	0.72	-	mm
A	seated height	-	-	-	0.465	mm
A <sub>2</sub>	package height	0.24	-	-	0.29	mm
e	nominal pitch	-	-	0.35	-	mm
n <sub>2</sub>	actual quantity of termination	-	-	8	-	

## 2. Package outline

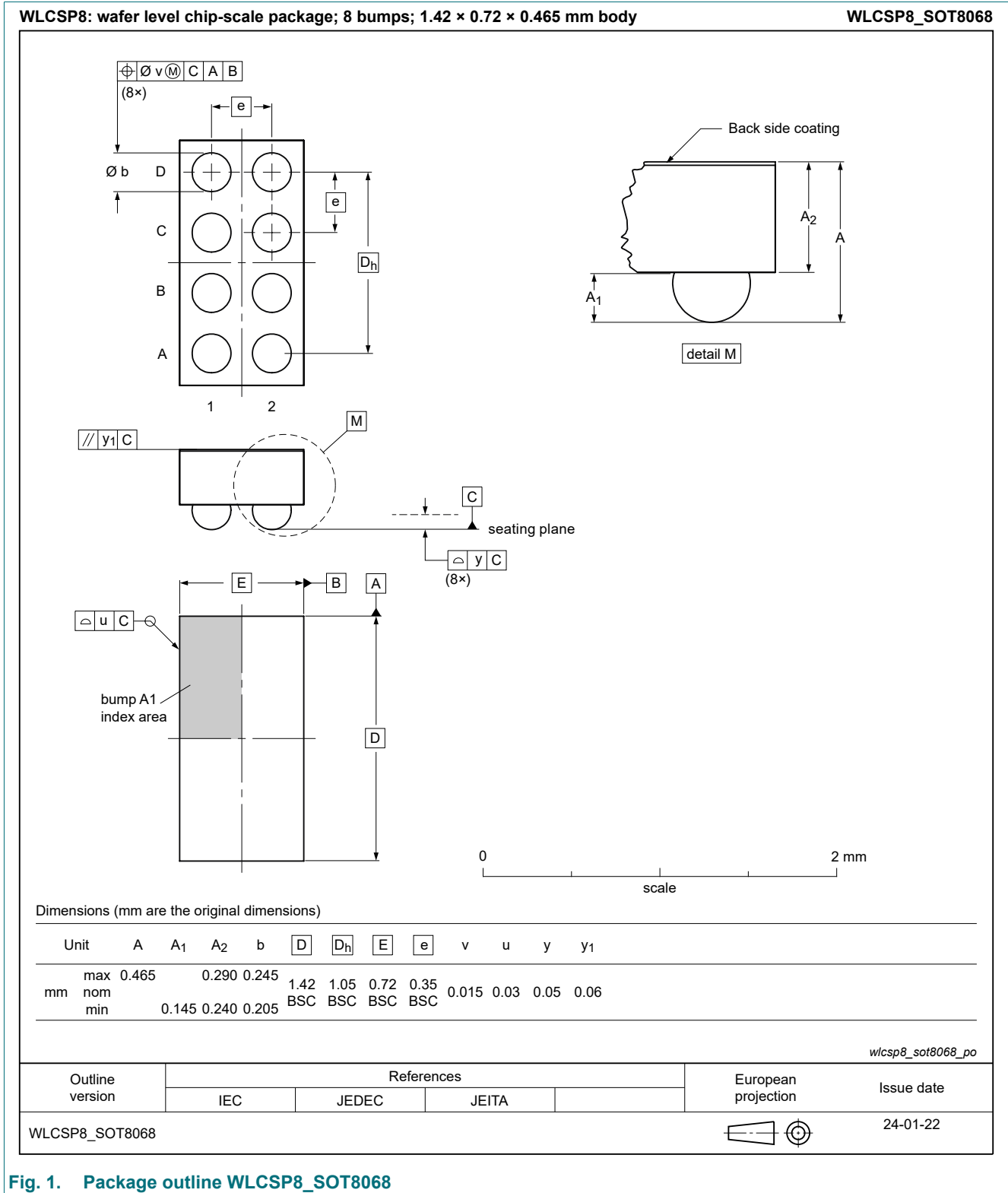
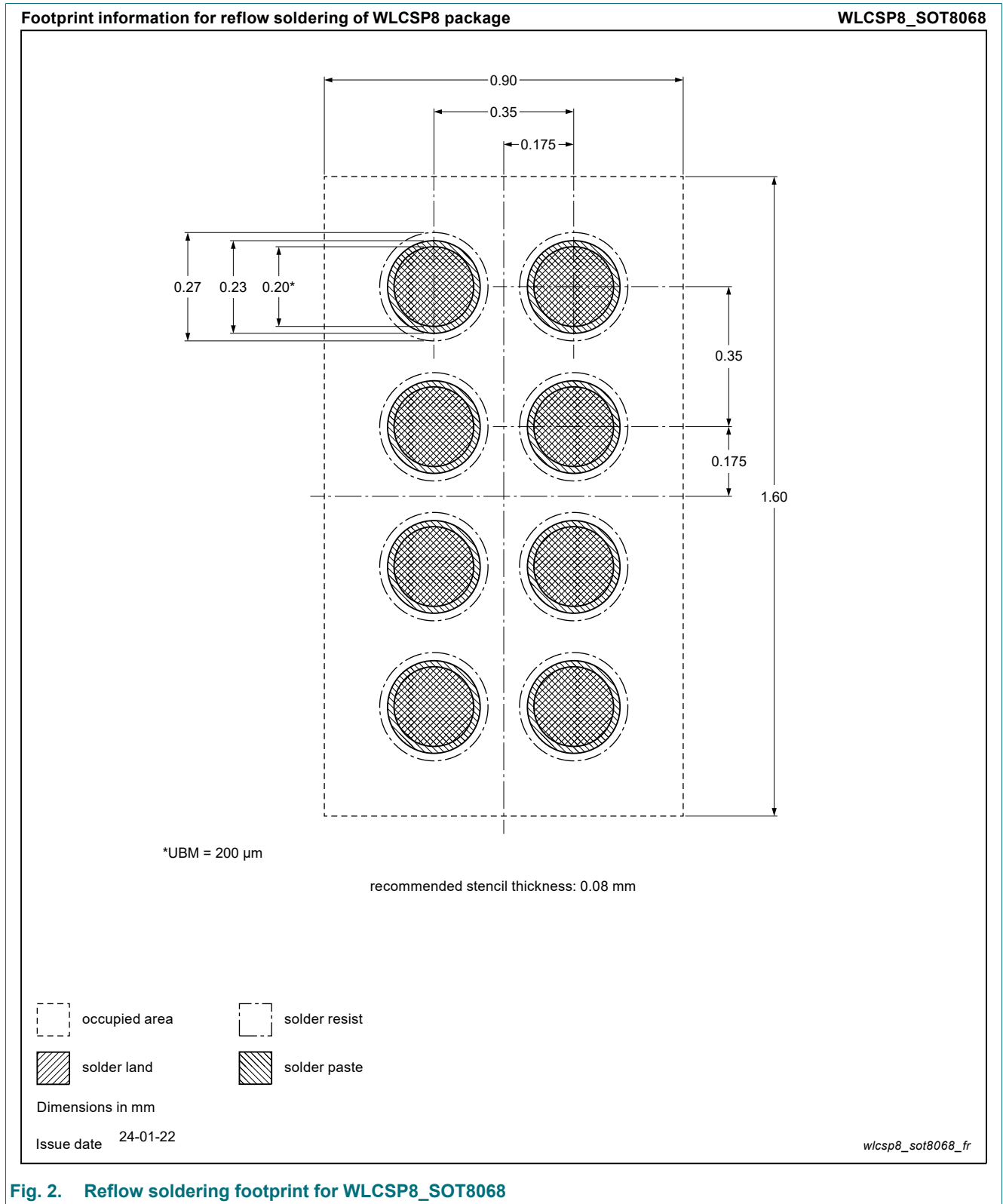


Fig. 1. Package outline WLCSP8\_SOT8068

### 3. Soldering



**Fig. 2. Reflow soldering footprint for WLCSP8\_SOT8068**

## 4. Legal information

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## Contents

1. Package summary.....	1
2. Package outline.....	2
3. Soldering.....	3
4. Legal information.....	4

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