



WLCSP8-SOT8072

wafer level chip-scale package; 8 solder bars; body: 3.5 x 2.13 x 0.429 mm

21 March 2023

Package information

1. Package summary

Terminal position code	B (bottom)
Package type descriptive code	WLCSP8
Package type industry code	WLCSP8
Package style descriptive code	WLCSP (wafer level chip-size package)
Package body material type	X (other)
JEDEC package outline code	MO-211 compatible
Mounting method type	S (surface mount)
Number of package outline detail graphic references	0
Issue date	3-3-2023
Manufacturer package code	WLCSP8-SOT8072
Footprint dimensions (mm)	3.7 x 2.35
Footprint area (mm ²)	8.70

Table 1. Package summary

Symbol	Parameter	Min	Typ	Nom	Max	Unit
D	package length	-	-	2.13	-	mm
E	package width	-	-	3.5	-	mm
A	seated height	0.394	-	0.429	0.464	mm
e	nominal pitch	-	-	0.5	-	mm
n ₂	actual quantity of termination	-	-	8	-	

2. Package outline

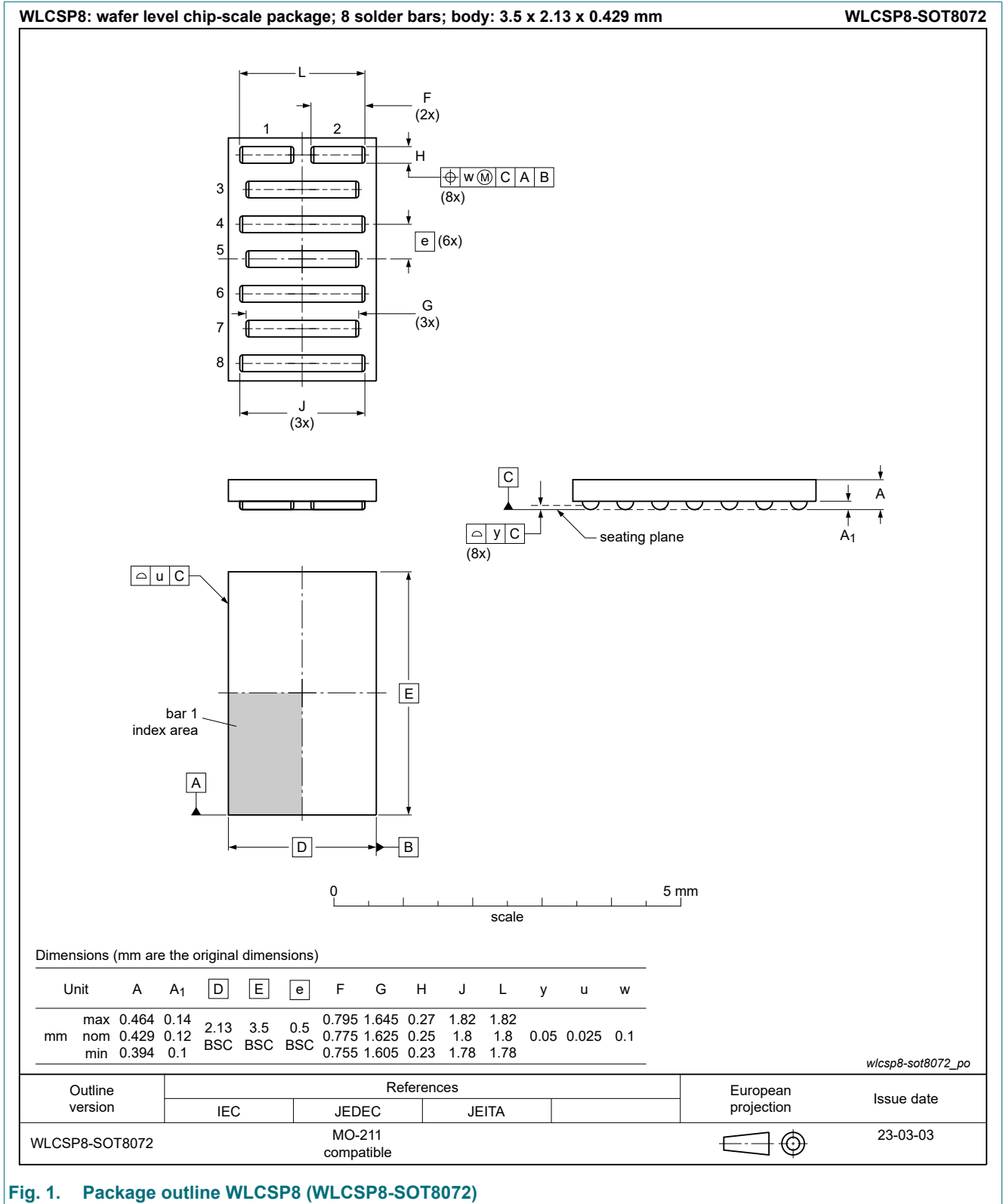


Fig. 1. Package outline WLCSP8 (WLCSP8-SOT8072)

3. Soldering

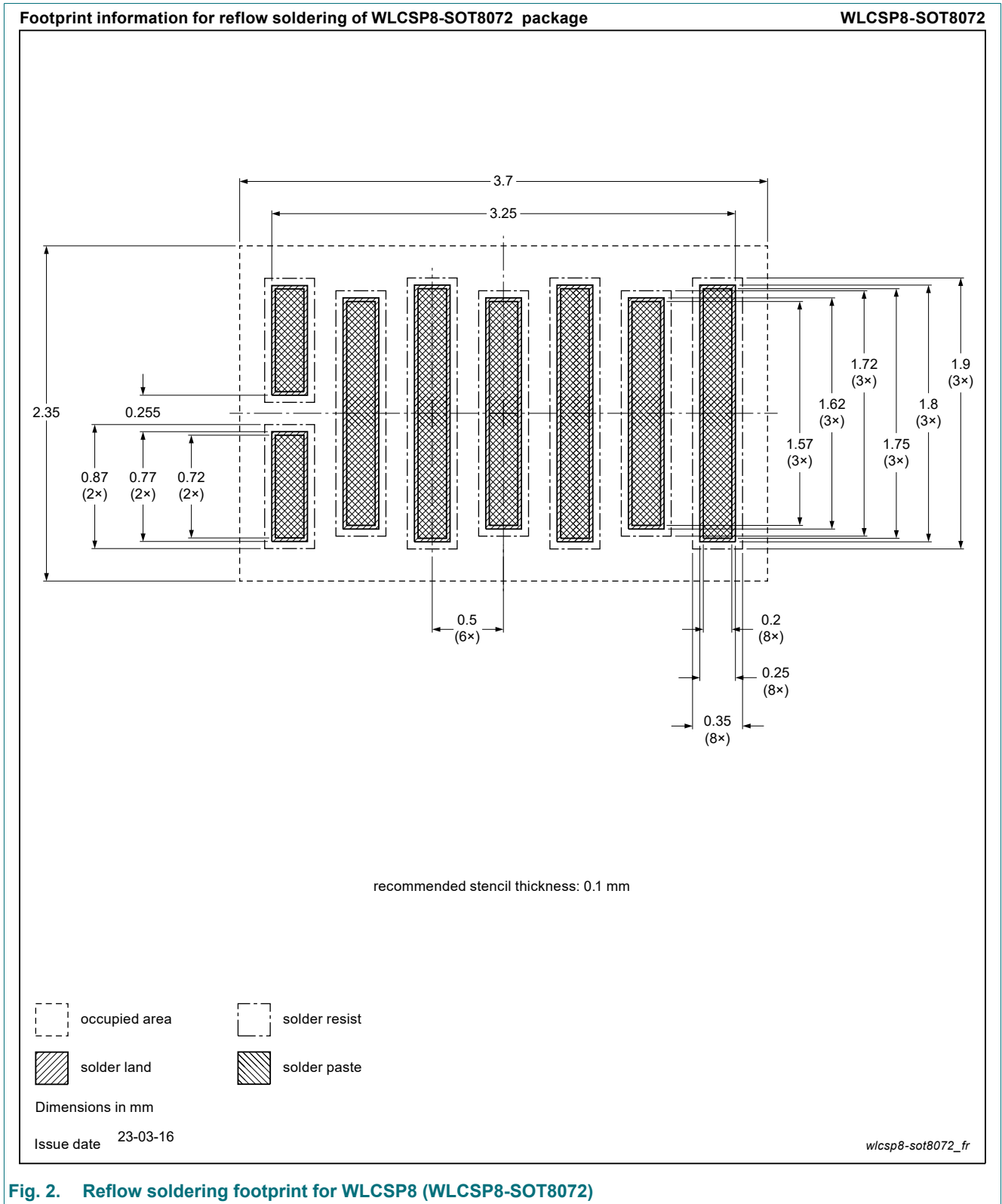


Fig. 2. Reflow soldering footprint for WLCSP8 (WLCSP8-SOT8072)

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