

WLCSP6_SOT8090

WLCSP6: wafer level chip-size package; 6 bumps

16 January 2025

Package information

1. Package summary

Terminal position code	B (bottom)
Package type descriptive code	WLCSP6
Package type industry code	WLCSP6
Package style descriptive code	WLCSP (wafer level chip-size package)
Package body material type	X (other)
Mounting method type	S (surface mount)
Number of package outline detail graphic references	0
Issue date	8-1-2025
Manufacturer package code	WLCSP6_SOT8090
Footprint dimensions (mm)	2.9 x 1.9
Footprint area (mm ²)	5.51
Package dimensions including leads (l x w) (mm)	1.5 x 2.5
Package area (mm ²)	3.75

Table 1. Package summary

Symbol	Parameter		Min	Typ	Nom	Max	Unit
D	package length		1.475	-	1.5	1.525	mm
E	package width		2.475	-	2.5	2.525	mm
A	seated height		-	-	-	0.465	mm
A ₂	package height		-	-	-	0.365	mm
e	nominal pitch		-	-	0.5	-	mm
n ₂	actual quantity of termination		-	-	6	-	

2. Package outline

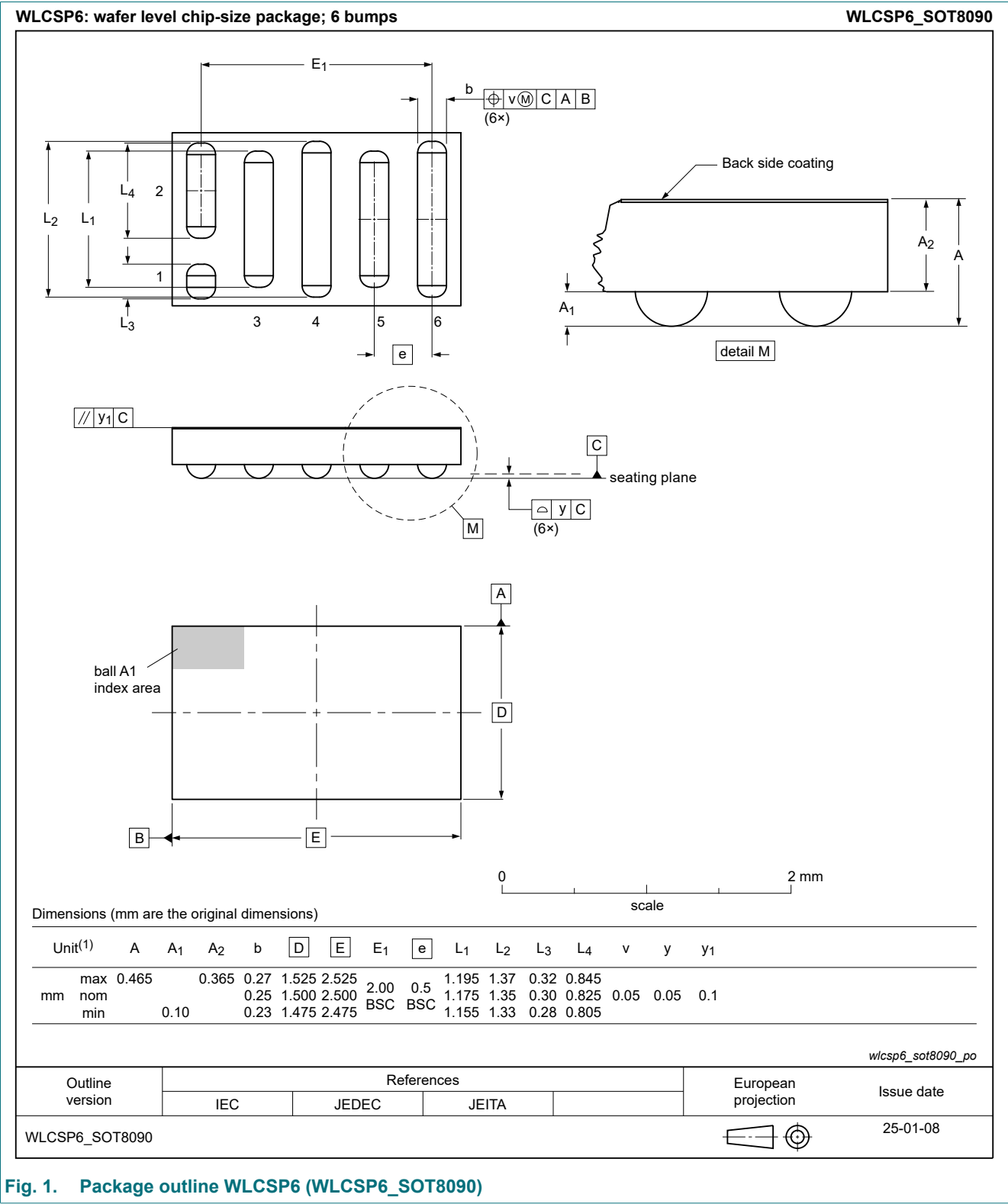


Fig. 1. Package outline WLCSP6 (WLCSP6_SOT8090)

3. Soldering

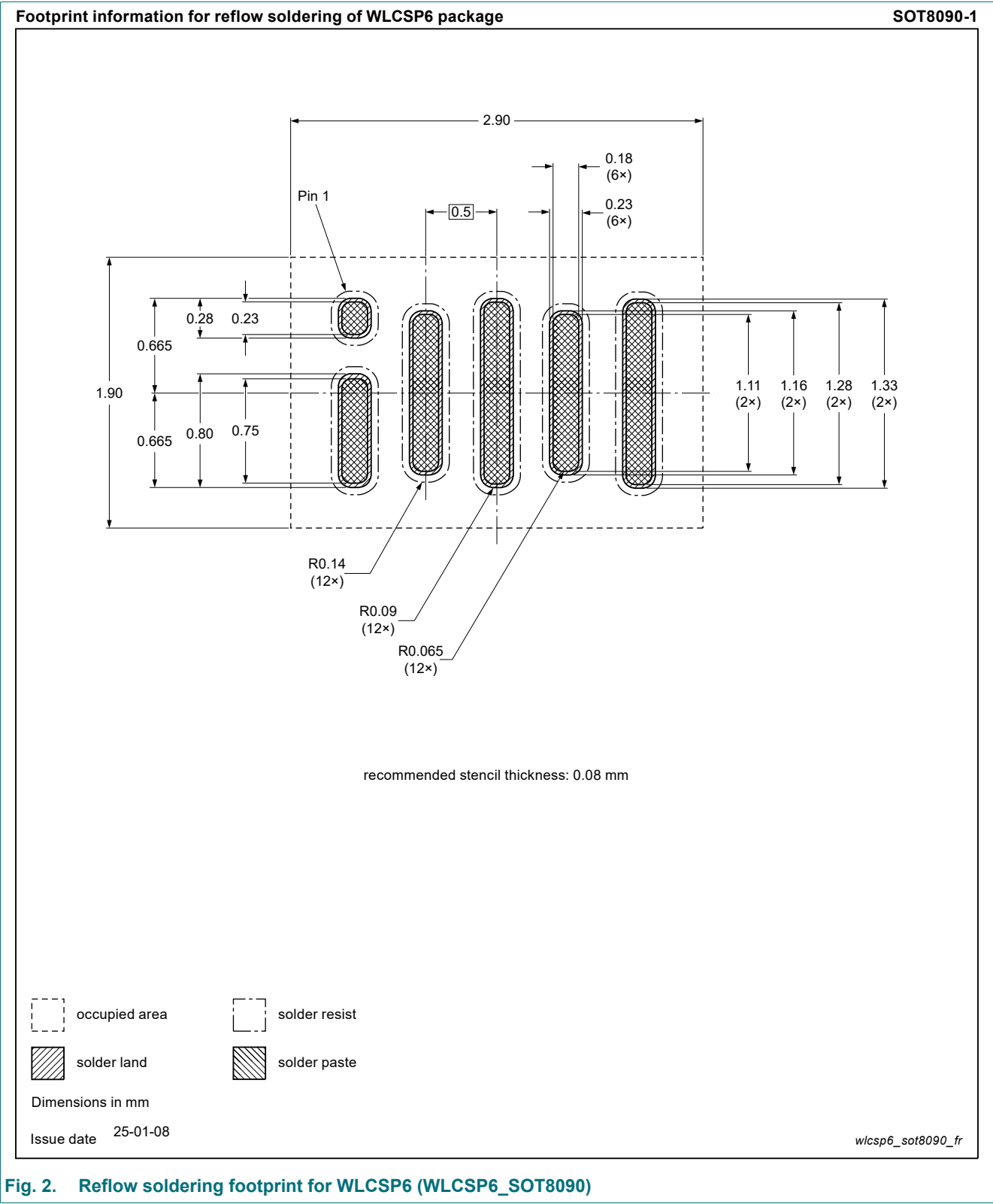


Fig. 2. Reflow soldering footprint for WLCSP6 (WLCSP6_SOT8090)

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