

WLCSP22_SOT8086

wafer level chip-size package

25 April 2024

Package information

1. Package summary

Terminal position code	B (bottom)
Package type descriptive code	WLCSP22
Package type industry code	WLCSP22
Package style descriptive code	WLCSP (wafer level chip-size package)
Package body material type	X (other)
JEDEC package outline code	MO-211
Mounting method type	S (surface mount)
Number of package outline detail graphic references	0
Issue date	19-2-2024
Manufacturer package code	WLCSP22_SOT8086
Footprint dimensions (mm)	4.7 x 2.7
Footprint area (mm ²)	12.69
Package dimensions including leads (l x w) (mm)	2.1 x 2.1
Package area (mm ²)	4.41

Table 1. Package summary

Symbol	Parameter		Min	Typ	Nom	Max	Unit
D	package length		-	-	2.1	-	mm
E	package width		-	-	2.1	-	mm
A	seated height		0.517	-	0.564	0.611	mm
A ₂	package height		0.33	-	0.344	0.358	mm
e	nominal pitch		-	-	0.4	-	mm
n ₂	actual quantity of termination		-	-	22	-	

2. Package outline

Package description line 1
Package description line 2

WLCSP22_SOT8086

Package under development

Nexperia reserves the right to make changes without notice.

detail M

Dimensions (mm are the original dimensions)

Unit ⁽¹⁾	A	A ₁	A ₂	b	D	D ₁	E	E ₁	e	u	v	y	y ₁
max	0.611	0.225	0.358	0.295									
nom	0.564	0.195	0.344	0.268	2.1	1.6	2.1	1.6	0.4	0.025	0.05	0.05	0.1
min	0.517	0.165	0.330	0.241	BSC	BSC	BSC	BSC	BSC				

wlcsp22_sot8086_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			

WLCSP22_SOT8086

MO-211

24-02-19

Fig. 1. Package outline WLCSP22 (WLCSP22_SOT8086)

3. Soldering

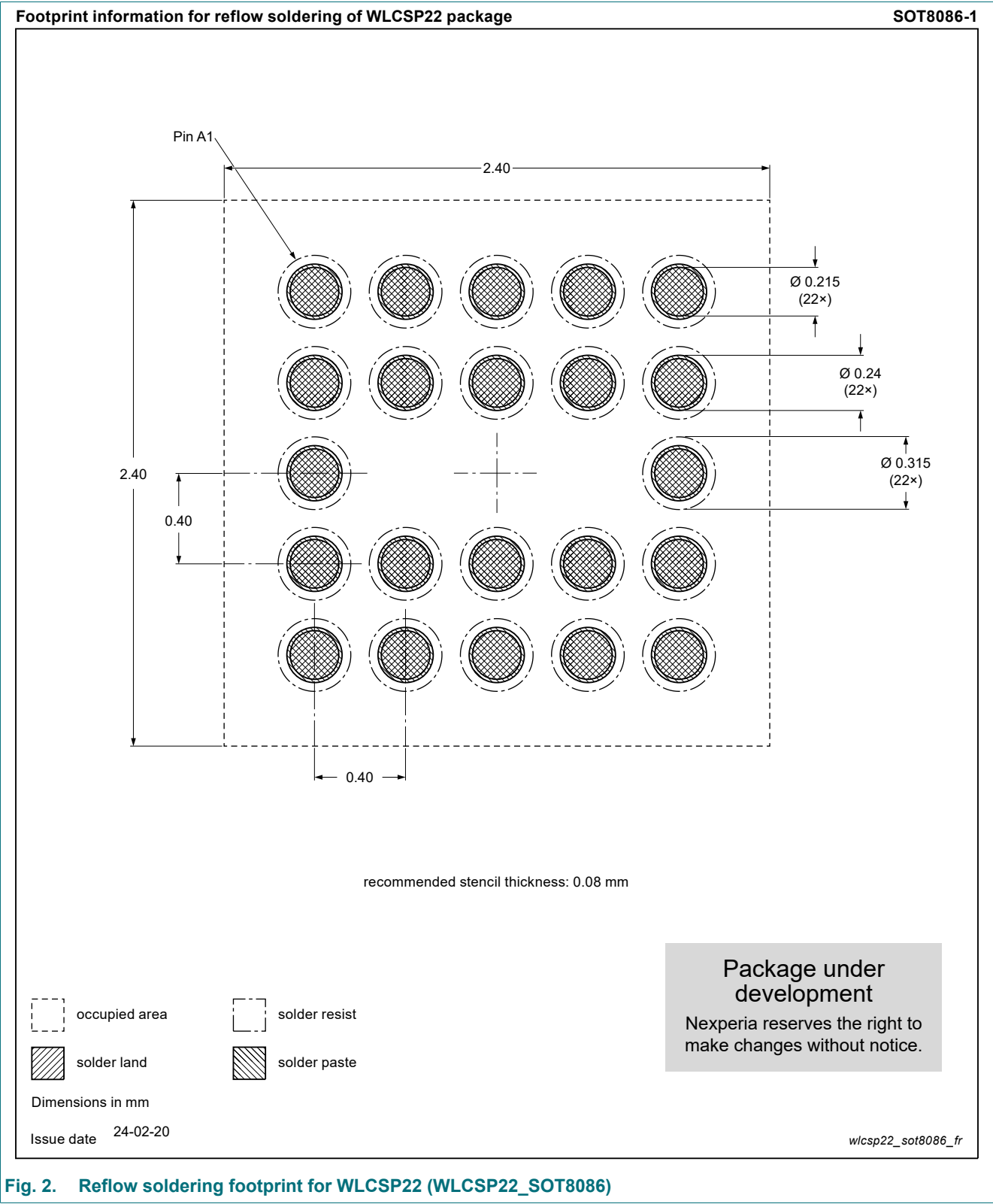


Fig. 2. Reflow soldering footprint for WLCSP22 (WLCSP22_SOT8086)

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Contents

1. Package summary..... 1

2. Package outline..... 2

3. Soldering..... 3

4. Legal information..... 4

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