



SOT8055-1

WLCSP6: wafer level chip-size package; 6 bumps (3 x 2)

25 November 2024

Package information

1. Package summary

Terminal position code	B (bottom)
Package type descriptive code	WLCSP6
Package type industry code	WLCSP6
Package style descriptive code	WLCSP (wafer level chip-size package)
Package body material type	X (other)
Mounting method type	S (surface mount)
Number of package outline detail graphic references	0
Issue date	27-9-2024
Manufacturer package code	SOT8055
Footprint dimensions (mm)	2.9 x 1.9
Footprint area (mm ²)	5.51
Package dimensions including leads (l x w) (mm)	1.09 x 0.74
Package area (mm ²)	0.81

Table 1. Package summary

Symbol	Parameter		Min	Typ	Nom	Max	Unit
D	package length		-	-	1.09	-	mm
E	package width		-	-	0.74	-	mm
A	seated height		0.32	-	0.36	0.4	mm
A ₂	package height		0.19	-	0.21	0.23	mm
e	nominal pitch		-	-	0.35	-	mm
n ₂	actual quantity of termination		-	-	6	-	

2. Package outline

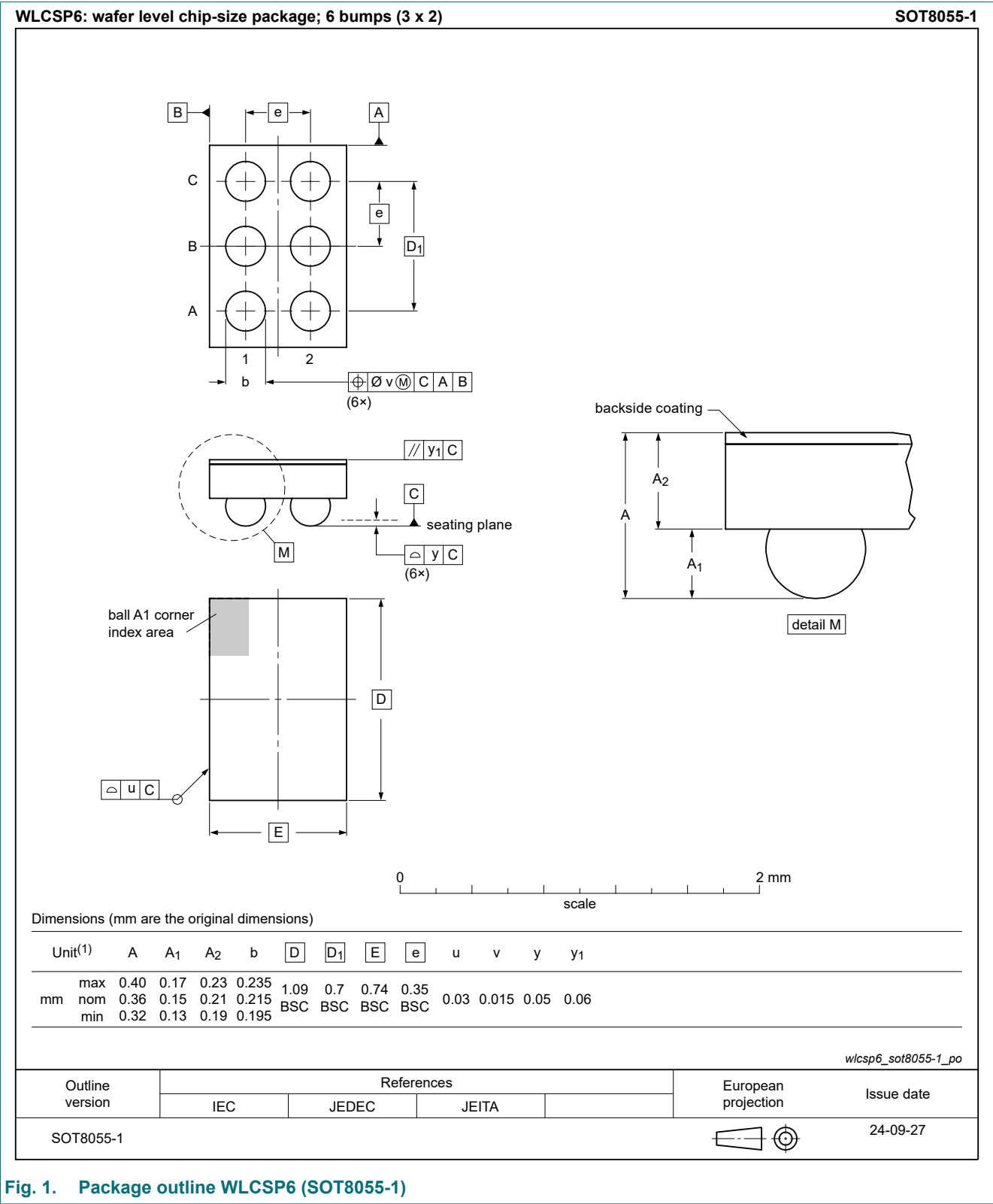


Fig. 1. Package outline WLCSP6 (SOT8055-1)

3. Soldering

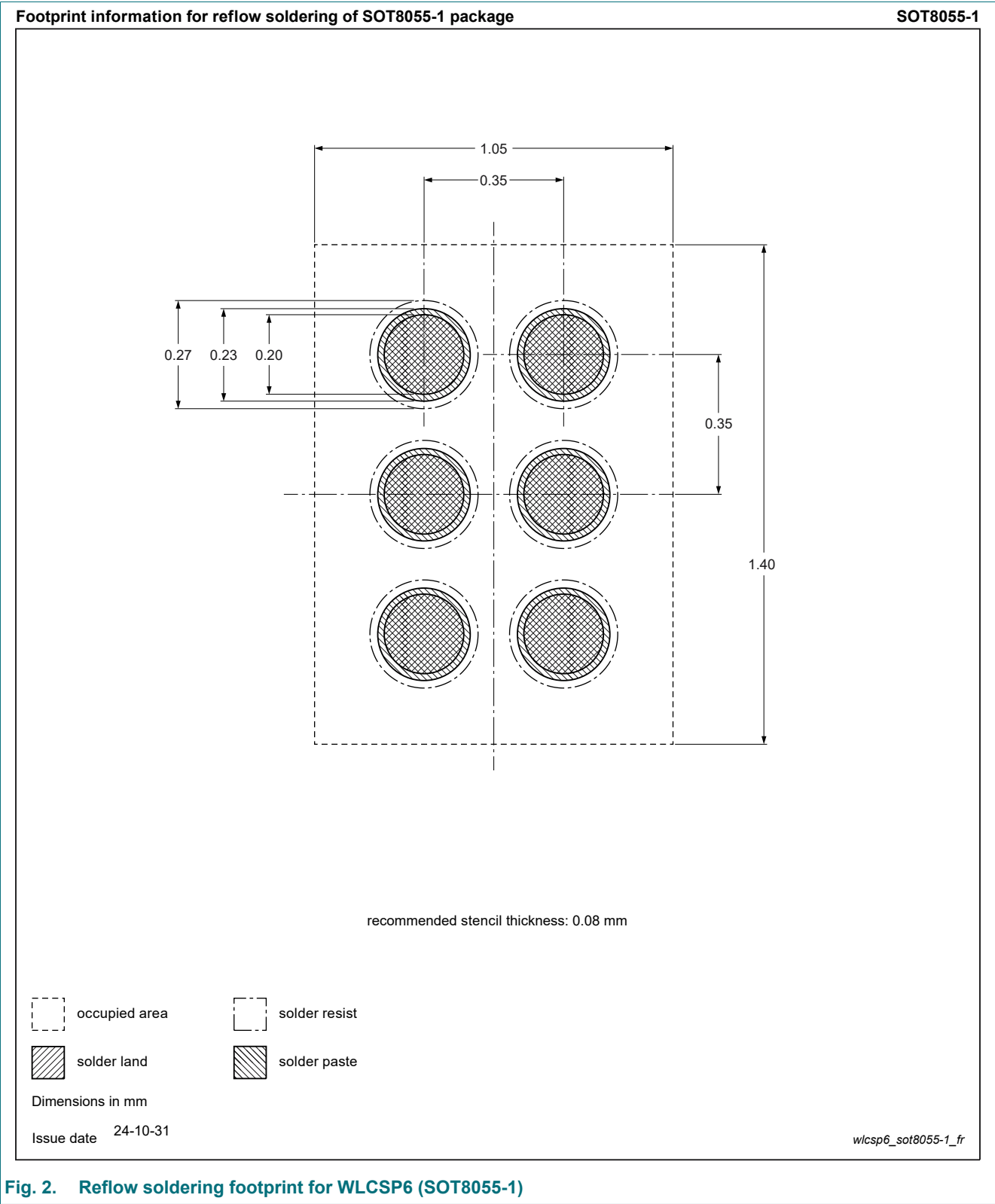


Fig. 2. Reflow soldering footprint for WLCSP6 (SOT8055-1)

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