



SOT1454-1

wafer level chip-scale package, 6 bumps

18 October 2018

Package information

1. Package summary

Terminal position code	B (bottom)
Package type descriptive code	WLCSP6
Package type industry code	WLCSP6
Package style descriptive code	WLCSP (wafer level chip-size package)
Package body material type	X (other)
Mounting method type	S (surface mount)
Number of package outline detail graphic references	0
Issue date	2-3-2016
Manufacturer package code	SOT1454
Footprint dimensions (mm)	0.65 x 0.44
Footprint area (mm²)	0.29

Table 1. Package summary

Symbol	Parameter	Min	Typ	Nom	Max	Unit
D	package length	0.62	-	0.65	0.68	mm
E	package width	0.41	-	0.44	0.47	mm
A	seated height	0.24	-	0.27	0.3	mm
A ₂	package height	0.18	-	0.2	0.22	mm
e	nominal pitch	-	-	0.44	-	mm
n ₂	actual quantity of termination	-	-	6	-	

2. Package outline

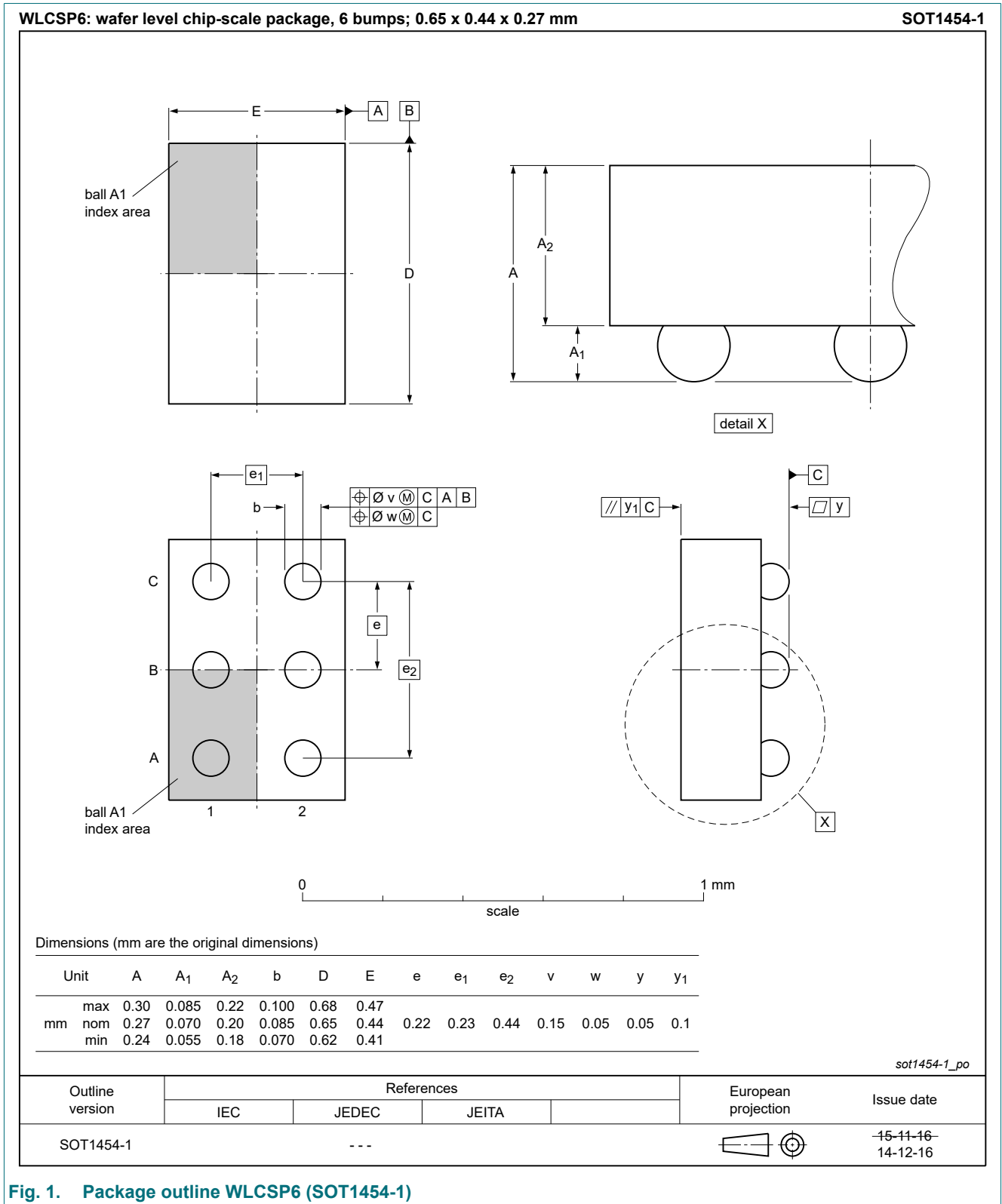


Fig. 1. Package outline WLCSP6 (SOT1454-1)

3. Soldering

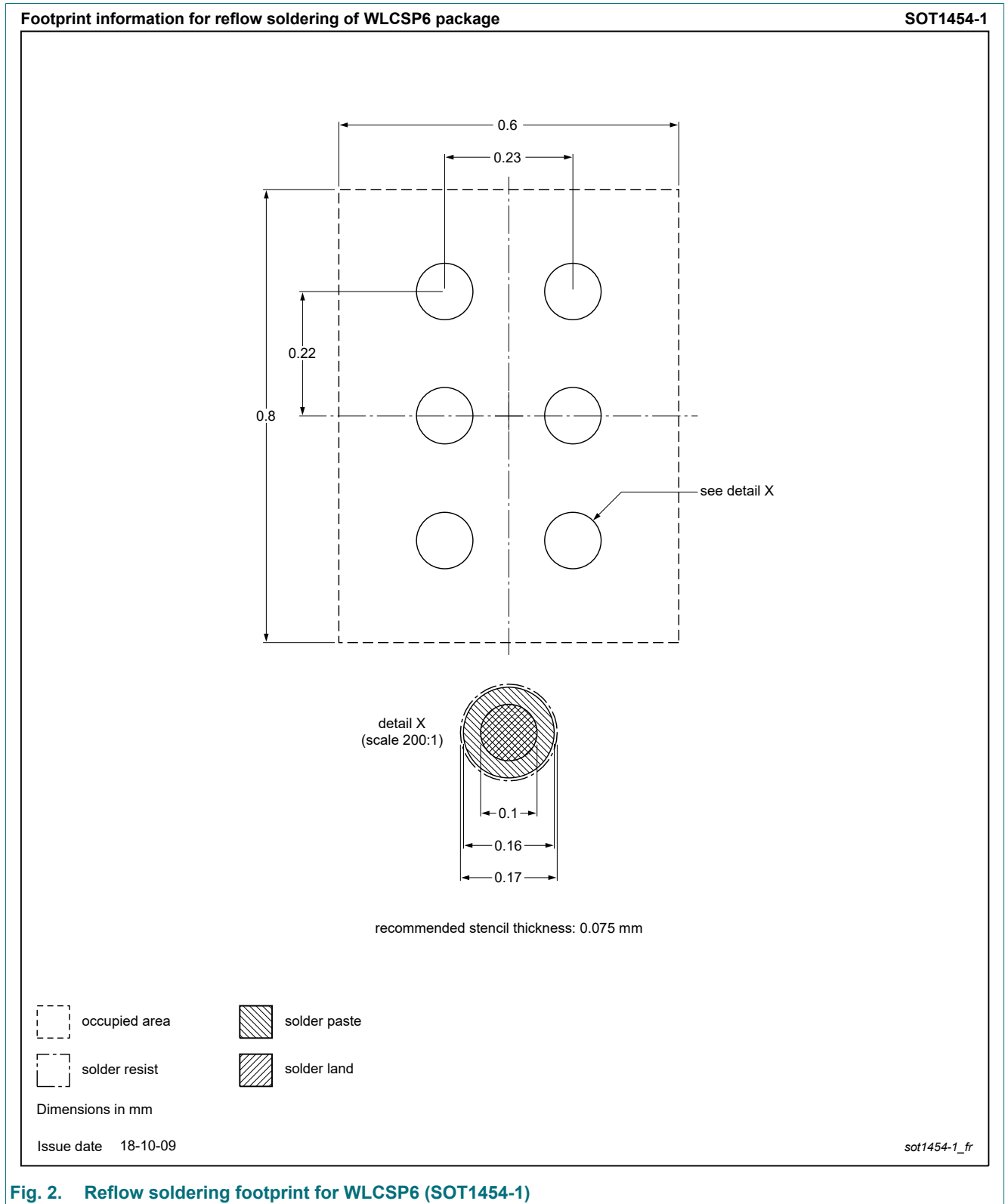


Fig. 2. Reflow soldering footprint for WLCSP6 (SOT1454-1)

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