Nexperia NFC antenna protection diodes PESD18VF1Bx and PESD24VF1Bx

High-performance, small form-factor devices protect the NFC antenna

The best ESD protection for NFC systems - from the global leader in ESD protection and Near-Field Communication (NFC).

**Features**
- Bidirectional configuration, allowing operating voltages up to 18 V and 24 V
- Very low capacitance, enabling easy design of the antenna matching circuit
- Very small voltage dependency of the diode capacitance, avoiding intermodulation distortion
- Small form-factor packages of 1006 (0402 inch) and 0603 (0201 inch) standard size

**General description**
NFC is the breakthrough technology that allows tags in posters, check-in signs, and contactless payment terminals to interact with mobile phones. The NFC antenna is often integrated into the battery cover or the battery itself and is connected to the NFC ICs via small contacts on the phone – an entry point for ESD strikes which are potentially hazardous to the NFC IC. These new Nexperia devices are optimized for the requirements of the NFC system and ensure the best-possible protection of the NFC IC.

**Selection guide**

<table>
<thead>
<tr>
<th>Type number</th>
<th>Protected lines</th>
<th>( V_{\text{max}} ) (V)</th>
<th>( C_{\text{lead ( , \text{typ} )}} ) (pF)</th>
<th>( C_{\text{lead ( , \text{max} )}} ) (pF)</th>
<th>ESD rating max (kV)</th>
<th>Configuration</th>
<th>Package</th>
<th>Size (mm)</th>
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</thead>
<tbody>
<tr>
<td>PESD18VF1BSP</td>
<td>1</td>
<td>18</td>
<td>0.28</td>
<td>0.45</td>
<td>10</td>
<td>Bidirectional</td>
<td>DSN60603-2</td>
<td>0.6 x 0.3 x 0.3</td>
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<tr>
<td>PESD1NFC-SF</td>
<td></td>
<td></td>
<td>0.35</td>
<td>0.5</td>
<td>10</td>
<td>Bidirectional</td>
<td>DFN1006-2</td>
<td>1 x 0.6 x 0.48</td>
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<td></td>
<td></td>
<td>0.25</td>
<td>0.4</td>
<td>10</td>
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<td>DSN60603-2</td>
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<tr>
<td>PESD24VF1BSP</td>
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<td>24</td>
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<td>0.4</td>
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<td>DFN1006-2</td>
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<tr>
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<td>0.3</td>
<td>0.45</td>
<td>10</td>
<td>Bidirectional</td>
<td>DFN1006-2</td>
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<tr>
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<td>10</td>
<td>Bidirectional</td>
<td>DFN1006-2</td>
<td>1 x 0.6 x 0.48</td>
</tr>
</tbody>
</table>
Using tiny packages makes PCB design more flexible. The highly linear diode capacitance, with very small variation, minimizes signal degradation.

Circuit diagram

Diode capacitance versus bias voltage diagram

**DFN1006 and DSN0603 packages**
These small packages are ideally suited for use in slim, compact mobile devices.

**DFN1006-2 Outline**

**DSN0603-2 Outline**

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