# 2-input EXCLUSIVE-OR gate

Rev. 3 — 3 January 2024

**Product data sheet** 

### 1. General description

XC7SH86 is a high-speed Si-gate CMOS device. It provides a 2-input EXCLUSIVE-OR function.

### 2. Features

- · Symmetrical output impedance
- High noise immunity
- · Low power dissipation
- CMOS input levels
- Balanced propagation delays
- ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Specified from -40 °C to +125 °C

### 3. Ordering information

**Table 1. Ordering information** 

Turns number   Deckers									
Type number	Package								
	Temperature range	Name	Description	Version					
XC7SH86GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1					
XC7SH86GV	-40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753					

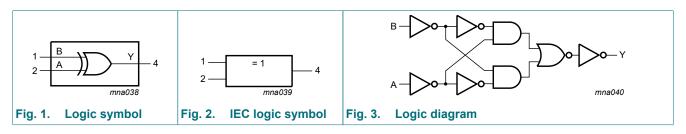
## 4. Marking

### Table 2. Marking codes

Type number	Marking code [1]
XC7SH86GW	fH
XC7SH86GV	f86

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

# 5. Functional diagram

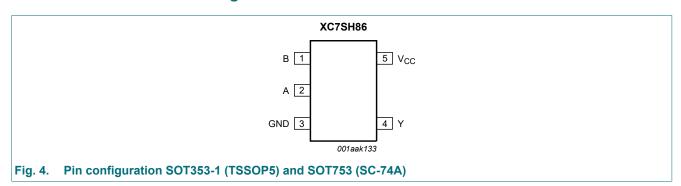




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# 6. Pinning information

### 6.1. Pinning



### 6.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
В	1	data input
Α	2	data input
GND	3	ground (0 V)
Υ	4	data output
V <sub>CC</sub>	5	supply voltage

## 7. Functional description

#### **Table 4. Function table**

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level.$ 

Inputs	Output	
A	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L

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## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		-0.5	+7.0	V
VI	input voltage		-0.5	+7.0	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < -0.5 V	-20	-	mA
I <sub>OK</sub>	output clamping current	$V_O < -0.5 \text{ V or } V_O > V_{CC} + 0.5 \text{ V}$ [1]	-	±20	mA
Io	output current	$-0.5 \text{ V} < \text{V}_{\text{O}} < \text{V}_{\text{CC}} + 0.5 \text{ V}$	-	±25	mA
I <sub>CC</sub>	supply current		-	75	mA
$I_{GND}$	ground current		-75	-	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40  ^{\circ}\text{C} \text{ to } +125  ^{\circ}\text{C}$ [2]	-	250	mW

<sup>[1]</sup> The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

## 9. Recommended operating conditions

### Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CC</sub>	supply voltage		2.0	5.0	5.5	V
VI	input voltage		0	-	5.5	V
Vo	output voltage		0	-	V <sub>CC</sub>	V
T <sub>amb</sub>	ambient temperature		-40	+25	+125	°C
Δt/ΔV	input transition rise and fall rate	$V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V}$	-	-	100	ns/V
		$V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$	-	-	20	ns/V

<sup>[2]</sup> For SOT353-1 (TSSOP5) package: P<sub>tot</sub> derates linearly with 3.3 mW/K above 74 °C. For SOT753 (SC-74A) package: P<sub>tot</sub> derates linearly with 3.8 mW/K above 85 °C.

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## 10. Static characteristics

#### **Table 7. Static characteristics**

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		-40 °C	to +85 °C	-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	1
V <sub>IH</sub>	HIGH-level	V <sub>CC</sub> = 2.0 V	1.5	-	-	1.5	-	1.5	-	V
	input voltage	V <sub>CC</sub> = 3.0 V	2.1	-	-	2.1	-	2.1	-	V
		V <sub>CC</sub> = 5.5 V	3.85	-	-	3.85	-	3.85	-	V
V <sub>IL</sub>	LOW-level	V <sub>CC</sub> = 2.0 V	-	-	0.5	-	0.5	-	0.5	V
	input voltage	V <sub>CC</sub> = 3.0 V	-	-	0.9	-	0.9	-	0.9	V
		V <sub>CC</sub> = 5.5 V	-	-	1.65	-	1.65	-	1.65	V
V <sub>OH</sub>	HIGH-level	$V_I = V_{IH}$ or $V_{IL}$								
	output voltage	I <sub>O</sub> = -50 μA; V <sub>CC</sub> = 2.0 V	1.9	2.0	-	1.9	-	1.9	-	V
		I <sub>O</sub> = -50 μA; V <sub>CC</sub> = 3.0 V	2.9	3.0	-	2.9	-	2.9	-	V
		I <sub>O</sub> = -50 μA; V <sub>CC</sub> = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.58	-	-	2.48	-	2.40	-	V
		I <sub>O</sub> = -8.0 mA; V <sub>CC</sub> = 4.5 V	3.94	-	-	3.8	-	3.70	-	V
V <sub>OL</sub>	LOW-level	$V_I = V_{IH}$ or $V_{IL}$								
	output voltage	I <sub>O</sub> = 50 μA; V <sub>CC</sub> = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 50 μA; V <sub>CC</sub> = 3.0 V	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 50 μA; V <sub>CC</sub> = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.36	-	0.44	-	0.55	V
		I <sub>O</sub> = 8.0 mA; V <sub>CC</sub> = 4.5 V	-	-	0.36	-	0.44	-	0.55	V
l <sub>l</sub>	input leakage current	V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μΑ
I <sub>CC</sub>	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	1.0	-	10	-	40	μΑ
Cı	input capacitance		-	1.5	10	-	10	-	10	pF

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# 11. Dynamic characteristics

#### **Table 8. Dynamic characteristics**

GND = 0 V. For waveform see Fig. 5. For test circuit see Fig. 6.

Symbol	Parameter	Conditions		neter Conditions 25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit	
				Min	Тур	Max	Min	Max	Min	Max	
t <sub>pd</sub>	propagation	A and B to Y	[1]								
	delay	V <sub>CC</sub> = 3.0 V to 3.6 V	[2]								
		C <sub>L</sub> = 15 pF		-	4.0	11.0	1.0	13.0	1.0	14.0	ns
		C <sub>L</sub> = 50 pF		-	5.8	14.5	1.0	16.5	1.0	18.5	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V	[3]								
		C <sub>L</sub> = 15 pF		-	3.4	6.8	1.0	8.0	1.0	8.5	ns
		C <sub>L</sub> = 50 pF		-	4.9	8.8	1.0	10.0	1.0	11.5	ns
C <sub>PD</sub>	power dissipation capacitance	per buffer; C <sub>L</sub> = 50 pF; f = 1 MHz; V <sub>I</sub> = GND to V <sub>CC</sub>	[4]	-	9	-	-	-	-	-	pF

- $t_{\text{pd}}$  is the same as  $t_{\text{PLH}}$  and  $t_{\text{PHL}}.$
- Typical values are measured at  $V_{CC}$  = 3.3 V.
- [3] Typical values are measured at  $V_{CC} = 5.0 \text{ V}$ . [4]  $C_{PD}$  is used to determine the dynamic power dissipation  $P_D$  ( $\mu$ W).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o)$  where:

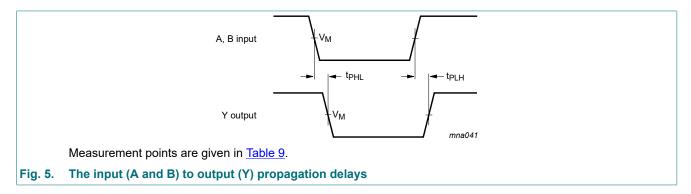
f<sub>i</sub> = input frequency in MHz;

f<sub>o</sub> = output frequency in MHz;

C<sub>L</sub> = output load capacitance in pF;

 $V_{CC}$  = supply voltage in V.

### 11.1. Waveform and test circuit

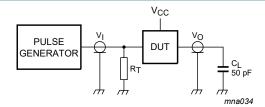


**Table 9. Measurement points** 

Input	Output	
V <sub>I</sub>	V <sub>M</sub>	V <sub>M</sub>
GND to V <sub>CC</sub>	0.5 × V <sub>CC</sub>	0.5 × V <sub>CC</sub>

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Test data is given in Table 10.

Definitions for test circuit:

C<sub>L</sub> = load capacitance including jig and probe capacitance;

 $R_{T}$  = termination resistance should be equal to the output impedance  $Z_{o}$  of the pulse generator.

Fig. 6. Test circuit for measuring switching times

### Table 10. Test data

Input		Load	Test
V <sub>I</sub>	t <sub>r</sub> , t <sub>f</sub>	CL	
V <sub>CC</sub>	≤ 3.0 ns	15 pF, 50 pF	t <sub>PLH</sub> , t <sub>PHL</sub>

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# 12. Package outline

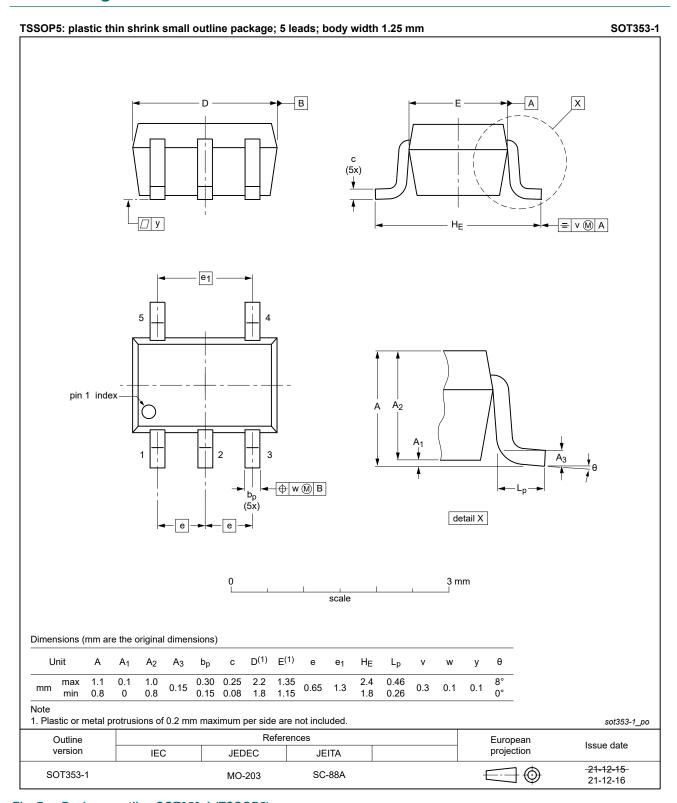
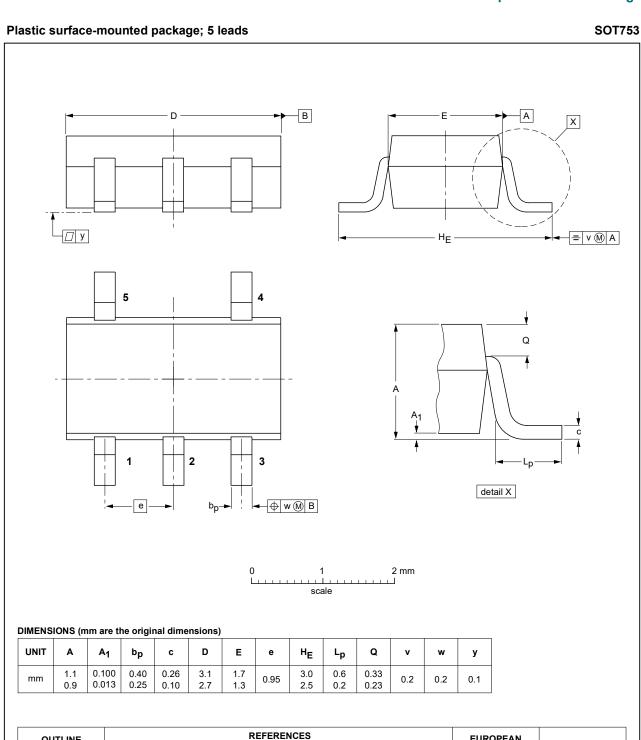


Fig. 7. Package outline SOT353-1 (TSSOP5)

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SOT753 SC-74A 

EUROPEAN PROJECTION OCCUPANTION OC

Fig. 8. Package outline SOT753 (SC-74A)

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### 13. Abbreviations

#### **Table 11. Abbreviations**

Acronym	Description			
CDM	Charged Device Model			
CMOS	mplementary Metal-Oxide Semiconductor			
DUT	Device Under Test			
ESD	ElectroStatic Discharge			
НВМ	Human Body Model			

# 14. Revision history

#### **Table 12. Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes				
XC7SH86 v.3	20240103	Product data sheet	-	XC7SH86 v.2				
Modifications:	Section 2: ES	<u>Section 2</u> : ESD specification updated according to the latest JEDEC standard.						
XC7SH86 v.2	20220111	Product data sheet	-	XC7SH86 v.1				
Modifications:	Nexperia. • Legal texts ha • Section 8: De	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Section 8: Derating values for P<sub>tot</sub> total power dissipation updated.</li> <li>Fig. 7: Package outline drawing SOT353-1 (TSSOP5) has changed.</li> </ul>						
XC7SH86 v.1	20090907	Product data sheet	-	-				

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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