1. General description

Transient voltage supressor in a DFN1006-2 (SOD882) ultra small and leadless Surface-Mounted Device (SMD) package designed to protect one line against high surge currents and other transients.

2. Features and benefits

- Bidirectional ESD protection of one line
- Very high surge robustness; \( I_{PP} = 40 \text{ A} \) for 8/20 \( \mu \text{s} \) pulse (average measured)
- Very low clamping voltage: \( V_{CL} = 10.5 \text{ V} \) typ. for 34 A, 8/20 \( \mu \text{s} \) pulse
- ESD protection up to 30 kV
- Very low dynamical resistance \( R_{dyn} = 0.07 \text{ Ohm} \) (TLP)
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

Surge protection for:

- supply and battery lines
- audio interfaces

in portable communication, consumer and computing devices.

4. Quick reference data

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_{RWM} )</td>
<td>reverse standoff voltage</td>
<td>( T_{amb} = 25 , ^{\circ} \text{C} )</td>
<td>-</td>
<td>-</td>
<td>5.5</td>
<td>V</td>
</tr>
<tr>
<td>( V_{CL} )</td>
<td>clamping voltage</td>
<td>( I_{PPM} = 35 , \text{A}; \ t_p = 8/20 , \mu\text{s}; \ T_{amb} = 25 , ^{\circ} \text{C} )</td>
<td>[1]</td>
<td>10.3</td>
<td>12.2</td>
<td>V</td>
</tr>
</tbody>
</table>

[1] In accordance with IEC 61000-4-5 (8/20 \( \mu \text{s} \) current waveform).
5. Pinning information

Table 2. Pinning information

<table>
<thead>
<tr>
<th>Pin</th>
<th>Symbol</th>
<th>Description</th>
<th>Simplified outline</th>
<th>Graphic symbol</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>K1</td>
<td>cathode (diode 1)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>K2</td>
<td>cathode (diode 2)</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

6. Ordering information

Table 3. Ordering information

<table>
<thead>
<tr>
<th>Type number</th>
<th>Package</th>
<th>Name</th>
<th>Description</th>
<th>Version</th>
</tr>
</thead>
<tbody>
<tr>
<td>PTVS5V5D1BL-Q</td>
<td>DFN1006-2</td>
<td>plastic, leadless ultra small package; 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.48 mm body</td>
<td>SOD882</td>
<td></td>
</tr>
</tbody>
</table>

7. Marking

Table 4. Marking codes

<table>
<thead>
<tr>
<th>Type number</th>
<th>Marking code</th>
</tr>
</thead>
<tbody>
<tr>
<td>PTVS5V5D1BL-Q</td>
<td>J8</td>
</tr>
</tbody>
</table>
# 8. Limiting values

### Table 5. Limiting values

*In accordance with the Absolute Maximum Rating System (IEC 60134).*

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$I_{PPM}$</td>
<td>rated peak pulse current</td>
<td>$t_p = 8/20 \mu s$</td>
<td>[1]</td>
<td>-</td>
<td>35 A</td>
</tr>
<tr>
<td>$T_j$</td>
<td>junction temperature</td>
<td></td>
<td>-</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>$T_{amb}$</td>
<td>ambient temperature</td>
<td></td>
<td>-55</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>$T_{stg}$</td>
<td>storage temperature</td>
<td></td>
<td>-65</td>
<td>150</td>
<td>°C</td>
</tr>
</tbody>
</table>

**ESD maximum ratings**

| $V_{ESD}$ | electrostatic discharge voltage | IEC 61000-4-2; contact discharge | [2] | -    | 30 kV |
|           |                                | IEC 61000-4-2; air discharge     | [2] | -    | 30 kV |

[1] In accordance with IEC 61000-4-5 (8/20 µs current waveform).


---

**Fig. 1.** 8/20 µs pulse waveform according to IEC 61000-4-5

**Fig. 2.** ESD pulse waveform according to IEC 61000-4-2
9. Characteristics

Table 6. Characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_{RWM} )</td>
<td>reverse standoff voltage</td>
<td>( T_{amb} = 25 , ^\circ C )</td>
<td>-</td>
<td>-</td>
<td>5.5</td>
<td>V</td>
</tr>
<tr>
<td>( V_{BR} )</td>
<td>breakdown voltage</td>
<td>( I_R = 5 , mA; , T_{amb} = 25 , ^\circ C )</td>
<td>5.6</td>
<td>6.4</td>
<td>7.6</td>
<td>V</td>
</tr>
<tr>
<td>( I_{RM} )</td>
<td>reverse leakage current</td>
<td>( V_R = 5.5 , V; , T_{amb} = 25 , ^\circ C )</td>
<td>-</td>
<td>10</td>
<td>100</td>
<td>nA</td>
</tr>
<tr>
<td>( C_d )</td>
<td>diode capacitance</td>
<td>( f = 1 , MHz; , V_R = 0 , V; , T_{amb} = 25 , ^\circ C )</td>
<td>-</td>
<td>70</td>
<td>84</td>
<td>pF</td>
</tr>
<tr>
<td>( V_{CL} )</td>
<td>clamping voltage</td>
<td>( I_{pp} = 1 , A; , t_p = 8/20 , \mu s; , T_{amb} = 25 , ^\circ C )</td>
<td>[1]</td>
<td>5.7</td>
<td>6.9</td>
<td>V</td>
</tr>
<tr>
<td>( R_{dyn} )</td>
<td>dynamic resistance</td>
<td>( I_R = 10 , A; , t_p = 100 , ns; , T_{amb} = 25 , ^\circ C )</td>
<td>[2]</td>
<td>0.1</td>
<td>-</td>
<td>Ω</td>
</tr>
</tbody>
</table>

[1] In accordance with IEC 61000-4-5 (8/20 µs current waveform).
[2] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI/ESD STM5.5.1-2008

Fig. 3. V-I characteristics for a bidirectional ESD protection diode

Fig. 4. Diode capacitance as a function of reverse voltage; typical values
Transmission Line Pulse (TLP);  
\( t_p = 100 \, \text{ns}; \, tr = 1 \, \text{ns} \)

**Fig. 5.** Positive clamping voltage (TLP); typical values

Transmission Line Pulse (TLP);  
\( t_p = 100 \, \text{ns}; \, tr = 1 \, \text{ns} \)

**Fig. 6.** Negative clamping voltage (TLP); typical values

\( t_p = \frac{8}{20} \, \mu\text{s}; \) according to IEC 61000-4-5

**Fig. 7.** Positive clamping voltage (8/20 μs pulse); typical values

\( t_p = \frac{8}{20} \, \mu\text{s}; \) according to IEC 61000-4-5

**Fig. 8.** Negative clamping voltage (8/20 μs pulse); typical values

**Fig. 9.** Rated peak pulse power as a function of square pulse duration; typical values
PTVS5V5D1BL-Q
Ultra compact transient voltage supressor

IEC 61000-4-2 ed.2
C_s = 150 pF; R_d = 330 Ω

RG 223/U
50 Ω coax

DUT (DEVICE UNDER TEST)

40 dB ATTENUATOR
50 Ω

4 GHz DIGITAL OSCILLOSCOPE

V (kV)

V (kV)

-8
0
4
6
8
-10 0 10 20 30 40 50 60 70 t (ns)

-2
0
2
4
6
8
-10 0 10 20 30 40 50 60 70 t (ns)

undamped +8 kV ESD pulse waveform
(IEC 61000-4-2 network)

unclamped -8 kV ESD pulse waveform
(IEC 61000-4-2 network)

Fig. 10. ESD clamping test setup and waveforms

Fig. 11. Clamped +8 kV pulse waveform (IEC 61000-4-2 network)

Fig. 12. Clamped -8 kV pulse waveform (IEC 61000-4-2 network)
10. Application information

The device is designed for the protection of one bidirectional line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.

![Application diagram](aaa-026975)

**Fig. 13. Application diagram**

**Circuit board layout and protection device placement**

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Test information

**Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.
12. Package outline

DFN1006-2: Plastic, leadless ultra small package; 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.48 mm body

Fig. 14. Package outline DFN1006-2 (SOD882)
13. Soldering

Fig. 15. Reflow soldering footprint for DFN1006-2 (SOD882)
## 14. Revision history

<table>
<thead>
<tr>
<th>Data sheet ID</th>
<th>Release date</th>
<th>Data sheet status</th>
<th>Change notice</th>
<th>Supersedes</th>
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<tr>
<td>PTVS5V5D1BL-Q v.1</td>
<td>20230605</td>
<td>Product data sheet</td>
<td>-</td>
<td>-</td>
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15. Legal information

Data sheet status

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<thead>
<tr>
<th>Document status</th>
<th>Product status</th>
<th>Definition</th>
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<tr>
<td>Objective [short] data sheet</td>
<td>Development</td>
<td>This document contains data from the objective specification for product development.</td>
</tr>
<tr>
<td>Preliminary [short] data sheet</td>
<td>Qualification</td>
<td>This document contains data from the preliminary specification.</td>
</tr>
<tr>
<td>Product [short] data sheet</td>
<td>Production</td>
<td>This document contains the product specification.</td>
</tr>
</tbody>
</table>

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term "short data sheet" is explained in section "Definitions".
[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com.

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PTVS5V5D1BL-Q
Ultra compact transient voltage supressor

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