1. General description

300 W unidirectional Transient Voltage Suppressor (TVS) in a DFN2020-3 (SOT1061) leadless medium power Surface-Mounted Device (SMD) plastic package, designed for transient overvoltage protection.

2. Features and benefits

- Unidirectional protection of one line
- Reverse standoff voltage: \( V_{RWM} = 20 \) V
- Surge robustness: \( I_{PPM} = 98.5 \) A (8/20 µs) / \( I_{PPM} = 9.2 \) A (10/1000 µs)
- Reverse current: \( I_{RM} = 1 \) nA
- Very low package height: 0.65 mm
- AEC-Q101 qualified

3. Applications

- Power supply protection
- Industrial applications
- Power management

4. Quick reference data

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_{RWM} )</td>
<td>reverse standoff voltage</td>
<td>( T_j = 25 ) °C</td>
<td>-</td>
<td>-</td>
<td>20</td>
<td>V</td>
</tr>
<tr>
<td>( I_{PPM} )</td>
<td>rated peak pulse current</td>
<td>( t_p = 8/20 ) µs</td>
<td>[1] [2]</td>
<td>-</td>
<td>98.5</td>
<td>A</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( t_p = 10/1000 ) µs</td>
<td>[3] [2]</td>
<td>-</td>
<td>9.2</td>
<td>A</td>
</tr>
</tbody>
</table>

[1] In accordance with IEC 61000-4-5 (8/20 µs current waveform).
5. Pinning information

Table 2. Pinning information

<table>
<thead>
<tr>
<th>Pin</th>
<th>Symbol</th>
<th>Description</th>
<th>Simplified outline</th>
<th>Graphic symbol</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>A</td>
<td>anode</td>
<td></td>
<td>3 1, 2</td>
</tr>
<tr>
<td>2</td>
<td>A</td>
<td>anode</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>K</td>
<td>cathode</td>
<td></td>
<td>005aab838</td>
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</tbody>
</table>

6. Ordering information

Table 3. Ordering information

<table>
<thead>
<tr>
<th>Type number</th>
<th>Package</th>
<th>Name</th>
<th>Description</th>
<th>Version</th>
</tr>
</thead>
<tbody>
<tr>
<td>PTVS20VU1UPA</td>
<td>DFN2020-3</td>
<td>plastic, thermal enhanced ultra thin small outline package; 3 terminals; 1.3 mm pitch; 2 mm x 2 mm x 0.65 mm body</td>
<td>SOT1061</td>
<td></td>
</tr>
</tbody>
</table>

7. Marking

Table 4. Marking codes

<table>
<thead>
<tr>
<th>Type number</th>
<th>Marking code</th>
</tr>
</thead>
<tbody>
<tr>
<td>PTVS20VU1UPA</td>
<td>D4</td>
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</table>
8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$P_{PPM}$</td>
<td>rated peak pulse power</td>
<td>$t_p = 8/20 \mu s$</td>
<td>[1]</td>
<td>-</td>
<td>3800 W</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$t_p = 10/1000 \mu s$</td>
<td>[3]</td>
<td>-</td>
<td>300 W</td>
</tr>
<tr>
<td>$I_{PPM}$</td>
<td>rated peak pulse current</td>
<td>$t_p = 8/20 \mu s$</td>
<td>[1]</td>
<td>-</td>
<td>98.5 A</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$t_p = 10/1000 \mu s$</td>
<td>[3]</td>
<td>-</td>
<td>9.2 A</td>
</tr>
<tr>
<td>$T_j$</td>
<td>junction temperature</td>
<td>-</td>
<td>150 °C</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$T_{amb}$</td>
<td>ambient temperature</td>
<td>-55</td>
<td>150 °C</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$T_{stg}$</td>
<td>storage temperature</td>
<td>-65</td>
<td>150 °C</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

ESD maximum ratings

| $V_{ESD}$ | electrostatic discharge voltage | IEC 61000-4-2; contact discharge | [4] | - | 30 kV |
|           |                                  | IEC 61000-4-2; air discharge     | [4] | - | 30 kV |

[1] In accordance with IEC 61000-4-5 (8/20 µs current waveform).

Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

Fig. 2. ESD pulse waveform according to IEC 61000-4-2
9. Characteristics

Fig. 3. 10/1000 µs pulse waveform according to IEC 61643-321

Table 6. Characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>V_RWM</td>
<td>reverse standoff voltage</td>
<td>T_J = 25 °C</td>
<td>-</td>
<td>-</td>
<td>20</td>
<td>V</td>
</tr>
<tr>
<td>V_BR</td>
<td>breakdown voltage</td>
<td>I_R = 1 mA</td>
<td>22.2</td>
<td>23.4</td>
<td>24.5</td>
<td>V</td>
</tr>
<tr>
<td>I_RM</td>
<td>reverse leakage current</td>
<td>V_R = 20 V</td>
<td>-</td>
<td>1</td>
<td>50</td>
<td>nA</td>
</tr>
<tr>
<td>C_d</td>
<td>diode capacitance</td>
<td>f = 1 MHz; V_R = 0 V</td>
<td>-</td>
<td>800</td>
<td>-</td>
<td>pF</td>
</tr>
<tr>
<td>V_CL</td>
<td>clamping voltage</td>
<td>I_PPM = 98.5 A; t_p = 8/20 µs</td>
<td>[1]</td>
<td>33</td>
<td>38.7</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>I_PPM = 9.2 A; t_p = 10/1000 µs</td>
<td>[3]</td>
<td>-</td>
<td>32.5</td>
<td>V</td>
</tr>
</tbody>
</table>

[1] In accordance with IEC 61000-4-5 (8/20 µs current waveform).
Fig. 4. V-I characteristics for a unidirectional TVS protection diode

Fig. 5. Relative variation of rated peak pulse power as a function of junction temperature; typical values

f = 1 MHz; $T_J = 25 \, ^\circ\text{C}$

Fig. 6. Diode capacitance as a function of reverse voltage; typical values
Fig. 7. Rated peak pulse power as a function of a pulse duration; typical values

Fig. 8. Positive clamping voltage (8/20 μs pulse); typical values

Fig. 9. Negative clamping voltage (8/20 μs pulse); typical values
**Fig. 10.** Dynamic resistance with positive clamping voltage; typical values

Dynamic resistance with positive clamping voltage; typical values.

**Fig. 11.** Dynamic resistance with negative clamping voltage; typical values

Dynamic resistance with negative clamping voltage; typical values.

---

**Fig. 12.** ESD clamping test setup and waveforms

ESD clamping test setup and waveforms.
Fig. 13. Clamped +8 kV pulse waveform (IEC 61000-4-2 network)

Fig. 14. Clamped -8 kV pulse waveform (IEC 61000-4-2 network)
10. Application information

The device is designed for the protection of one unidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are either positive or negative with respect to ground.

---

Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.
11. Package outline

HUSON3: plastic thermal enhanced ultra thin small outline package; no leads;
3 terminals; body 2 x 2 x 0.65 mm

Fig. 16. Package outline DFN2020-3 (SOT1061)
12. Soldering

Fig. 17. Reflow soldering footprint for DFN2020-3 (SOT1061)
## 13. Revision history

<table>
<thead>
<tr>
<th>Data sheet ID</th>
<th>Release date</th>
<th>Data sheet status</th>
<th>Change notice</th>
<th>Supersedes</th>
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<tr>
<td>PTVS20VU1UPA v.1</td>
<td>20170612</td>
<td>Product data sheet</td>
<td>-</td>
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Data sheet status

<table>
<thead>
<tr>
<th>Document status</th>
<th>Product status</th>
<th>Definition</th>
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</thead>
<tbody>
<tr>
<td>[short] data sheet</td>
<td>Development</td>
<td>This document contains data from the objective specification for product development.</td>
</tr>
<tr>
<td>[short] data sheet</td>
<td>Qualification</td>
<td>This document contains data from the preliminary specification.</td>
</tr>
<tr>
<td>[short] data sheet</td>
<td>Production</td>
<td>This document contains the product specification.</td>
</tr>
</tbody>
</table>

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[2] The term "short data sheet" is explained in section "Definitions".

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