

650 V, 30 A ultrafast recovery rectifier 3 May 2024

Product data sheet

#### 1. General description

High power density, ultrafast switching time recovery rectifier with high-efficiency planar technology, encapsulated in D2PAK Real-2-Pin (SOT8018).

#### 2. Features and benefits

- Reverse voltage  $V_R \le 650 \text{ V}$
- Forward current I<sub>F</sub> ≤ 30 A •
- Typical switching time t<sub>rr</sub> of 32 ns •
- Pt doped life time control •
- Low inductance •
- Planar die design

#### 3. Applications

- AC/DC converter
- DC/DC converter •
- SMPS/UPS •
- Battery charger
- Inverter
- Freewheeling applications

#### 4. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
I <sub>F(AV)</sub>	average forward current	δ = 0.5; f = 20 kHz; square wave; T <sub>c</sub> ≤ 98 °C		-	-	30	A
V <sub>RRM</sub>	repetitive peak reverse voltage	T <sub>j</sub> = 25 °C		-	-	650	V
V <sub>R</sub>	reverse voltage			-	-	650	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 30 A; pulsed; T <sub>j</sub> = 25 °C	[1]	-	1.38	1.8	V
		I <sub>F</sub> = 30 A; pulsed; T <sub>j</sub> = 125 °C	[1]	-	1.26	1.6	V
		I <sub>F</sub> = 30 A; pulsed; T <sub>j</sub> = 175 °C	[1]	-	1.19	-	V
I <sub>R</sub>	reverse current	V <sub>R</sub> = 650 V; pulsed; T <sub>j</sub> = 25 °C	[1]	-	-	5	μA
		V <sub>R</sub> = 650 V; pulsed; T <sub>j</sub> = 125 °C	[1]	-	2.7	50	μA
		V <sub>R</sub> = 650 V; pulsed; T <sub>j</sub> = 175 °C	[1]	-	47	-	μA

[1] Very short pulse, in order to maintain a stable junction temperature.

# nexperia

# 5. Pinning information

Pin	Pinning info		Simulified outline	Cranhia aumhal
PIN	Symbol	Description	Simplified outline	Graphic symbol
1	к	cathode	mb	
2	A	anode		
mb	К	mounting base; connected to cathode, also referred to as the case	D2PAK R2P (SOT8018)	K K; mb A Aaa-037872

# 6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PNU650300AEJ	D2PAK R2P	Plastic, single-ended surface-mounted package (D2PAK R2P); Real-2-Pin configuration; 5.08 mm pitch; 8.8 mm x 10.35 mm x 4.46 mm body	<u>SOT8018</u>			

#### 7. Marking

Table 4. Marking codes						
Type number	Marking code					
PNU650300AEJ	U65030A					

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 601134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>RRM</sub>	repetitive peak reverse voltage	T <sub>j</sub> = 25 °C		-	650	V
V <sub>R</sub>	reverse voltage			-	650	V
V <sub>RMS</sub>	RMS voltage			-	460	V
l <sub>F</sub>	forward current	$\delta = 1; T_c \le 80 \degree C$		-	42	А
I <sub>F(AV)</sub>	average forward current	δ = 0.5; f = 20 kHz; square wave; T <sub>c</sub> ≤ 98 °C		-	30	A
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8.3 ms; single half sine wave (applied at rated load condition); $T_{j(init)}$ = 25 °C		-	209	A
		t <sub>p</sub> = 10 ms; square wave; T <sub>j(init)</sub> = 25 °C		-	158	Α
P <sub>tot</sub>	total power dissipation	T <sub>c</sub> ≤ 25 °C	[1]	-	2.4	W
			[2]	-	4.2	W
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 6 cm<sup>2</sup>.

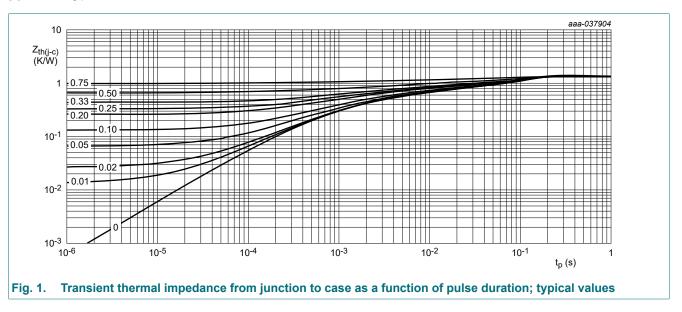
#### 9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R <sub>th(j-a)</sub>	thermal resistance from	m in free air	[1]	-	-	61	K/W
	junction to ambient		[2]	-	-	36	K/W
R <sub>th(j-c)</sub>	thermal resistance from junction to case		[3]	-	-	1.5	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 6 cm<sup>2</sup>.

[2] Device mounted on an FR4 PC[3] Soldering point of cathode tab.



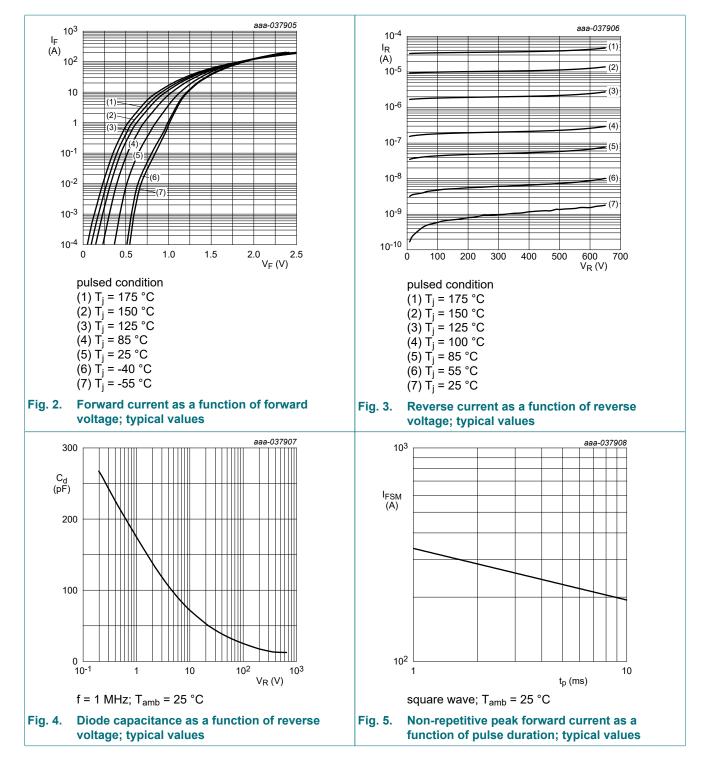
PNU650300AEJ

### **10. Characteristics**

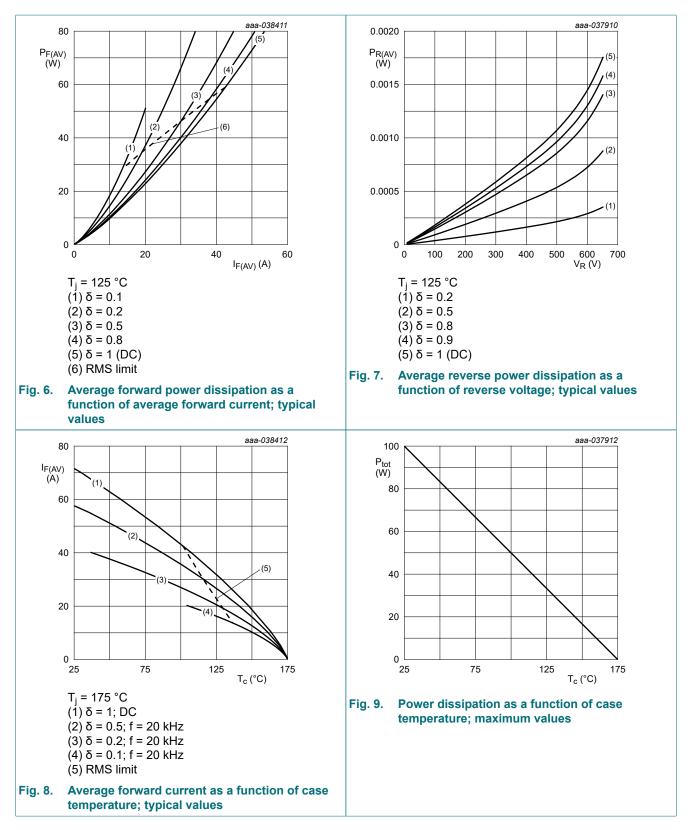
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V <sub>(BR)R</sub>	reverse breakdown voltage	I <sub>R</sub> = 100 μA; pulsed; T <sub>j</sub> = 25 °C	[1]	650	-	-	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 30 A; pulsed; T <sub>j</sub> = 25 °C	[1]	-	1.38	1.8	V
		I <sub>F</sub> = 30 A; pulsed; T <sub>j</sub> = 125 °C	[1]	-	1.26	1.6	V
		I <sub>F</sub> = 30 A; pulsed; T <sub>j</sub> = 175 °C	[1]	-	1.19	-	V
R	reverse current	V <sub>R</sub> = 650 V; pulsed; T <sub>j</sub> = 25 °C	[1]	-	-	5	μA
		V <sub>R</sub> = 650 V; pulsed; T <sub>j</sub> = 125 °C	[1]	-	2.7	50	μA
		V <sub>R</sub> = 650 V; pulsed; T <sub>j</sub> = 175 °C	[1]	-	47	-	μA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 400 V; f = 1 MHz; T <sub>j</sub> = 25 °C		-	13	-	pF
t <sub>rr</sub>	reverse recovery time ; step recovery	$I_F = 0.5 \text{ A}; I_R = 1 \text{ A}; I_{R(meas)} = 0.25 \text{ A};$ $T_j = 25 \text{ °C}$		-	32	60	ns
	reverse recovery time ; ramp recovery	I <sub>F</sub> = 30 A; dI <sub>F</sub> /dt = -200 A/µs; V <sub>R</sub> = 400 V; T <sub>j</sub> = 25 °C		-	133	-	ns
		I <sub>F</sub> = 30 A; dI <sub>F</sub> /dt = -1000 A/µs; V <sub>R</sub> = 400 V; T <sub>j</sub> = 25 °C		-	83	-	ns
		I <sub>F</sub> = 30 A; dI <sub>F</sub> /dt = -200 A/μs; V <sub>R</sub> = 400 V; T <sub>j</sub> = 125 °C		-	211	-	ns
		I <sub>F</sub> = 30 A; dI <sub>F</sub> /dt = -1000 A/µs; V <sub>R</sub> = 400 V; T <sub>j</sub> = 125 °C		-	127	-	ns
I <sub>RM</sub>	peak reverse recovery current	I <sub>F</sub> = 30 A; dI <sub>F</sub> /dt = -200 A/μs; V <sub>R</sub> = 400 V; T <sub>j</sub> = 25 °C		-	9	-	A
		I <sub>F</sub> = 30 A; dI <sub>F</sub> /dt = -1000 A/µs; V <sub>R</sub> = 400 V; T <sub>j</sub> = 25 °C		-	27	-	A
		I <sub>F</sub> = 30 A; dI <sub>F</sub> /dt = -200 A/μs; V <sub>R</sub> = 400 V; T <sub>j</sub> = 125 °C		-	14	-	A
		I <sub>F</sub> = 30 A; dI <sub>F</sub> /dt = -1000 A/µs; V <sub>R</sub> = 400 V; T <sub>j</sub> = 125 °C		-	38	-	A
Q <sub>rr</sub>	reverse recovery charge	I <sub>F</sub> = 30 A; dI <sub>F</sub> /dt = -200 A/µs; V <sub>R</sub> = 400 V; T <sub>j</sub> = 25 °C		-	690	-	nC
		$I_F = 30 \text{ A}; \text{ d}I_F/\text{d}t = -1000 \text{ A}/\mu\text{s};$ $V_R = 400 \text{ V}; \text{ T}_j = 25 ^\circ\text{C}$		-	1349	-	nC
		$I_F$ = 30 A; dI <sub>F</sub> /dt = -200 A/µs; V <sub>R</sub> = 400 V; T <sub>j</sub> = 125 °C		-	1788	-	nC
		I <sub>F</sub> = 30 A; dI <sub>F</sub> /dt = -1000 A/µs; V <sub>R</sub> = 400 V; T <sub>i</sub> = 125 °C		-	2897	-	nC

[1] Very short pulse, in order to maintain a stable junction temperature.

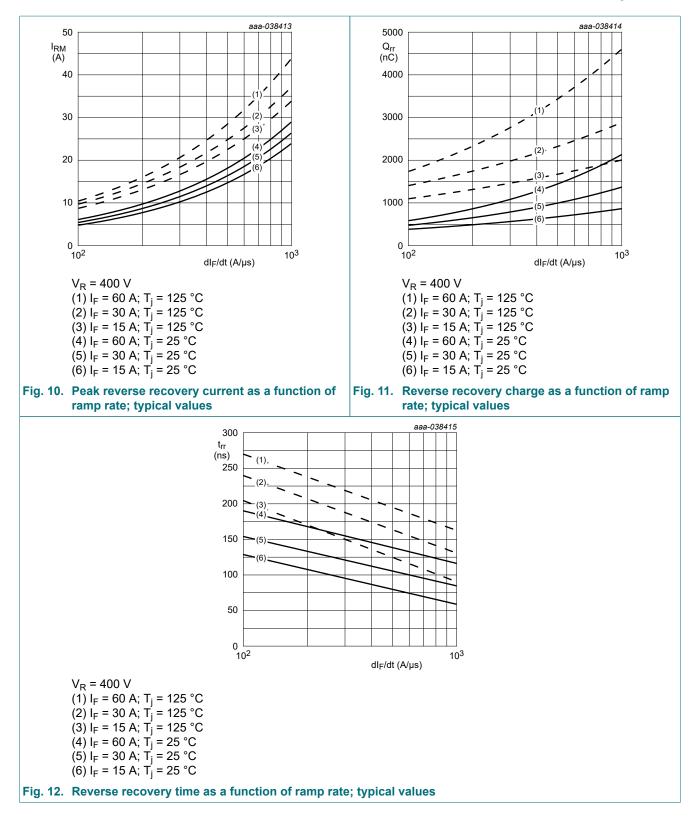
#### 650 V, 30 A ultrafast recovery rectifier



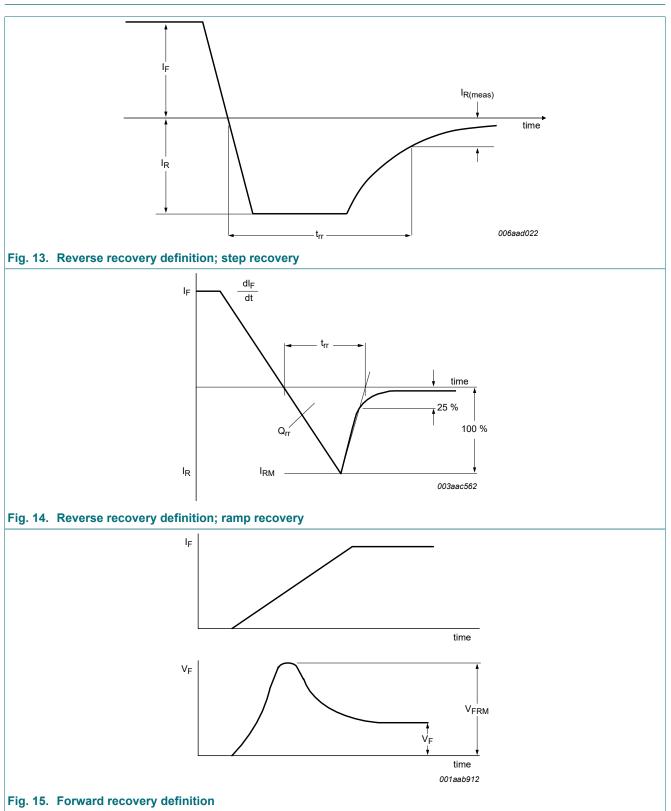
#### 650 V, 30 A ultrafast recovery rectifier



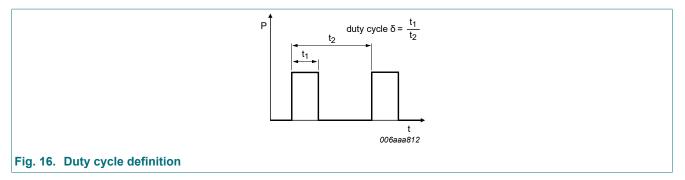
#### 650 V, 30 A ultrafast recovery rectifier



### **11. Test information**



#### 650 V, 30 A ultrafast recovery rectifier



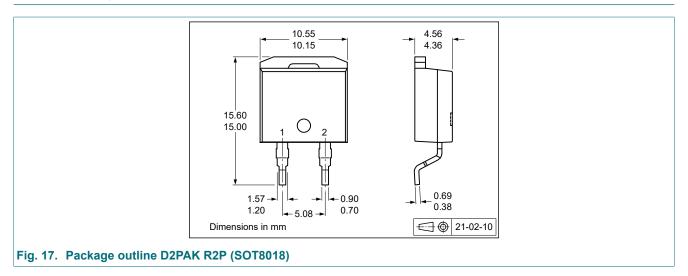
The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)}{=}I_M{\times}\delta$  with  $I_M$  defined as peak current

 $I_{RMS}=I_{F(AV)}$  at DC, and  $I_{RMS}=I_M \times \sqrt{\delta}$ 

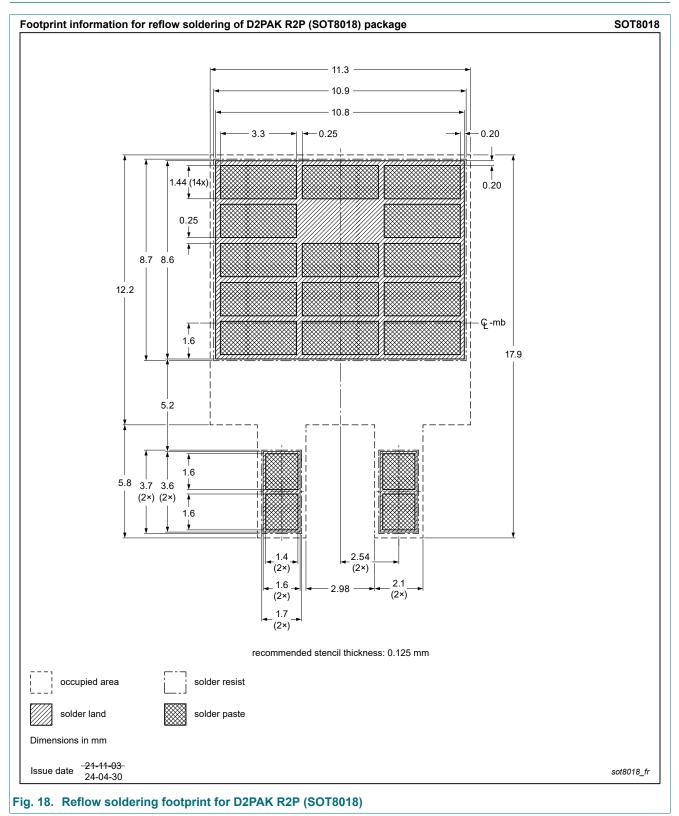
with I<sub>RMS</sub> defined as RMS current.

#### 12. Package outline



#### 650 V, 30 A ultrafast recovery rectifier

### 13. Soldering



# 14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PNU650300AEJ v.1	20240503	Product data sheet	-	-		

PNU650300AEJ

650 V, 30 A ultrafast recovery rectifier

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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### Contents

1.	General description	1
2.	Features and benefits	. 1
3.	Applications	. 1
4.	Quick reference data	1
5.	Pinning information	2
6.	Ordering information	2
7.	Marking	2
8.	Limiting values	. 3
9.	Thermal characteristics	. 4
10	Characteristics	5
11.	Test information	9
12	Package outline	10
13	Soldering	11
14	Revision history	12
15	Legal information	13

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PNU650300AEJ