1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a leadless ultra small DFN1006-3 (SOT883) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Low leakage current
- Leadless ultra small SMD plastic package: 1.0 × 0.6 × 0.48 mm
- ElectroStatic Discharge (ESD) protection > 1 kV HBM
- Drain-source on-state resistance $R_{DSon} = 470 \, \text{m}\Omega$

3. Applications

- Relay driver
- High-speed line driver
- Low-side load switch
- Switching circuits

4. Quick reference data

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{DS}$</td>
<td>drain-source voltage</td>
<td>$T_j = 25 , ^\circ\text{C}$</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>V</td>
</tr>
<tr>
<td>$V_{GS}$</td>
<td>gate-source voltage</td>
<td></td>
<td>-8</td>
<td>-</td>
<td>8</td>
<td>V</td>
</tr>
<tr>
<td>$I_D$</td>
<td>drain current</td>
<td>$V_{GS} = 4.5 , \text{V}; , T_{amb} = 25 , ^\circ\text{C}$</td>
<td>[1]</td>
<td>-</td>
<td>-</td>
<td>A</td>
</tr>
</tbody>
</table>

Static characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$R_{DSon}$</td>
<td>drain-source on-state resistance</td>
<td>$V_{GS} = 4.5 , \text{V}; , I_D = 0.6 , \text{A}; , T_j = 25 , ^\circ\text{C}$</td>
<td>-</td>
<td>470</td>
<td>620</td>
<td>mΩ</td>
</tr>
</tbody>
</table>

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².
5. Pinning information

Table 2. Pinning information

<table>
<thead>
<tr>
<th>Pin</th>
<th>Symbol</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>G</td>
<td>gate</td>
</tr>
<tr>
<td>2</td>
<td>S</td>
<td>source</td>
</tr>
<tr>
<td>3</td>
<td>D</td>
<td>drain</td>
</tr>
</tbody>
</table>

6. Ordering information

Table 3. Ordering information

<table>
<thead>
<tr>
<th>Type number</th>
<th>Package Name</th>
<th>Description</th>
<th>Version</th>
</tr>
</thead>
<tbody>
<tr>
<td>PMZ600UNEL</td>
<td>DFN1006-3</td>
<td>DFN1006-3: leadless ultra small plastic package; 3 solder lands</td>
<td>SOT883</td>
</tr>
</tbody>
</table>

7. Marking

Table 4. Marking codes

<table>
<thead>
<tr>
<th>Type number</th>
<th>Marking code</th>
</tr>
</thead>
<tbody>
<tr>
<td>PMZ600UNEL</td>
<td>L3</td>
</tr>
</tbody>
</table>
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>V_DS</td>
<td>drain-source voltage</td>
<td>T_j = 25 °C</td>
<td>-</td>
<td>20</td>
<td>V</td>
</tr>
<tr>
<td>V_GS</td>
<td>gate-source voltage</td>
<td></td>
<td>-8</td>
<td>8</td>
<td>V</td>
</tr>
<tr>
<td>I_D</td>
<td>drain current</td>
<td>V_GS = 4.5 V; T_amb = 25 °C</td>
<td>[1]</td>
<td>0.6</td>
<td>A</td>
</tr>
<tr>
<td></td>
<td></td>
<td>V_GS = 4.5 V; T_amb = 100 °C</td>
<td>[1]</td>
<td>0.4</td>
<td>A</td>
</tr>
<tr>
<td>I_DM</td>
<td>peak drain current</td>
<td>T_amb = 25 °C; single pulse; t_p ≤ 10 µs</td>
<td>-</td>
<td>2.5</td>
<td>A</td>
</tr>
<tr>
<td>P_tot</td>
<td>total power dissipation</td>
<td>T_amb = 25 °C</td>
<td>[2]</td>
<td>360</td>
<td>mW</td>
</tr>
<tr>
<td></td>
<td></td>
<td>T_sp = 25 °C</td>
<td>[1]</td>
<td>715</td>
<td>mW</td>
</tr>
<tr>
<td>T_j</td>
<td>junction temperature</td>
<td></td>
<td>-55</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>T_amb</td>
<td>ambient temperature</td>
<td></td>
<td>-55</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>T_stg</td>
<td>storage temperature</td>
<td></td>
<td>-65</td>
<td>150</td>
<td>°C</td>
</tr>
</tbody>
</table>

Source-drain diode

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>I_S</td>
<td>source current</td>
<td>T_amb = 25 °C</td>
<td>[1]</td>
<td>0.4</td>
<td>A</td>
</tr>
</tbody>
</table>

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².

Fig. 1. Normalized total power dissipation as a function of junction temperature

\[ P_{der} = \frac{P_{tot}}{P_{tot(25°C)}} \times 100 \%

Fig. 2. Normalized continuous drain current as a function of junction temperature

\[ I_{der} = \frac{I_D}{I_D(25°C)} \times 100 \% \]
9. Thermal characteristics

Table 6. Thermal characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$R_{th(j-a)}$</td>
<td>thermal resistance from junction to ambient</td>
<td>in free air</td>
<td>[1]</td>
<td>-</td>
<td>305</td>
<td>360  K/W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[2]</td>
<td>-</td>
<td>150</td>
<td>175  K/W</td>
</tr>
<tr>
<td>$R_{th(j-sp)}$</td>
<td>thermal resistance from junction to solder point</td>
<td></td>
<td>-</td>
<td>-</td>
<td>40</td>
<td>40   K/W</td>
</tr>
</tbody>
</table>


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage
Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values
### 10. Characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{BRDSS}$</td>
<td>drain-source breakdown voltage</td>
<td>$I_D = 250 \mu A; V_{GS} = 0 \ V; T_j = 25 \ °C$</td>
<td>20</td>
<td>-</td>
<td>-</td>
<td>V</td>
</tr>
<tr>
<td>$V_{GSth}$</td>
<td>gate-source threshold voltage</td>
<td>$I_D = 250 \mu A; V_{DS} = V_{GS}; T_j = 25 \ °C$</td>
<td>0.45</td>
<td>0.7</td>
<td>0.95</td>
<td>V</td>
</tr>
<tr>
<td>$I_{DSS}$</td>
<td>drain leakage current</td>
<td>$V_{DS} = 20 \ V; V_{GS} = 0 \ V; T_j = 25 \ °C$</td>
<td>-</td>
<td>-</td>
<td>1</td>
<td>µA</td>
</tr>
<tr>
<td>$I_{GSS}$</td>
<td>gate leakage current</td>
<td>$V_{GS} = 8 \ V; V_{DS} = 0 \ V; T_j = 25 \ °C$</td>
<td>-</td>
<td>-</td>
<td>-10</td>
<td>µA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_{GS} = -8 \ V; V_{DS} = 0 \ V; T_j = 25 \ °C$</td>
<td>-</td>
<td>-</td>
<td>-1</td>
<td>µA</td>
</tr>
<tr>
<td>$R_{DSon}$</td>
<td>drain-source on-state resistance</td>
<td>$V_{GS} = 4.5 \ V; I_D = 0.6 \ A; T_j = 25 \ °C$</td>
<td>-</td>
<td>470</td>
<td>620</td>
<td>mΩ</td>
</tr>
<tr>
<td>$g_{fs}$</td>
<td>forward transconductance</td>
<td>$V_{DS} = 5 \ V; I_D = 0.6 \ A; T_j = 25 \ °C$</td>
<td>-</td>
<td>1</td>
<td>-</td>
<td>S</td>
</tr>
<tr>
<td>$R_G$</td>
<td>gate resistance</td>
<td>$f = 1 \ MHz$</td>
<td>-</td>
<td>34</td>
<td>-</td>
<td>Ω</td>
</tr>
</tbody>
</table>

#### Dynamic characteristics

- $Q_{G(tot)}$: total gate charge
  - $V_{DS} = 10 \ V; I_D = 0.6 \ A; V_{GS} = 4.5 \ V; T_j = 25 \ °C$
    - 0.4
    - 0.7
    - nC

- $Q_{GS}$: gate-source charge
  - $V_{DS} = 4.5 \ V; I_D = 0.6 \ A; T_j = 150 \ °C$
    - 0.1
    - nC

- $Q_{GD}$: gate-drain charge
  - 0.1
    - nC

- $C_{iss}$: input capacitance
  - $V_{DS} = 10 \ V; f = 1 \ MHz; V_{GS} = 0 \ V; T_j = 25 \ °C$
    - 21.3
    - pF

- $C_{oss}$: output capacitance
  - 5.4
    - pF

- $C_{rss}$: reverse transfer capacitance
  - 4.2
    - pF

- $t_{d(on)}$: turn-on delay time
  - $V_{DS} = 10 \ V; I_D = 0.6 \ A; V_{GS} = 4.5 \ V; T_j = 25 \ °C$
    - 5.6
    - ns

- $t_r$: rise time
  - $R_{G(ext)} = 6 \Omega; T_j = 25 \ °C$
    - 9.2
    - ns
Symbol | Parameter | Conditions | Min | Typ | Max | Unit
--- | --- | --- | --- | --- | --- | ---
\( t_{(\text{off})} \) | turn-off delay time | - | 19 | - | ns |
\( t_f \) | fall time | - | 51 | - | ns |

**Source-drain diode**

\( V_{SD} \) | source-drain voltage | \( I_S = 0.36 \, \text{A}; \ V_{GS} = 0 \, \text{V}; \ T_j = 25 \, ^\circ\text{C} \) | - | 0.8 | 1.2 | V

---

**Fig. 6.** Output characteristics: drain current as a function of drain-source voltage; typical values

**Fig. 7.** Sub-threshold drain current as a function of gate-source voltage

**Fig. 8.** Drain-source on-state resistance as a function of drain current; typical values

**Fig. 9.** Drain-source on-state resistance as a function of gate-source voltage; typical values
Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

\[ V_{DS} > I_D \times R_{DS_{on}} \]

Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

\[ a = \frac{R_{DS_{on}}}{R_{DS_{on}(25\,\degree C)}} \]

Fig. 12. Gate-source threshold voltage as a function of junction temperature

\[ I_D = 0.25\,mA; V_{DS} = V_{GS} \]

Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

\[ f = 1\,MHz; V_{GS} = 0\,V \]
Fig. 14. Gate-source voltage as a function of gate charge; typical values

\[ V_{GS} = 0 \text{ V} \]

Fig. 15. MOSFET transistor: Gate charge waveform definitions

Fig. 16. Source current as a function of source-drain voltage; typical values

11. Test information

Fig. 17. Duty cycle definition
12. Package outline

Leadless ultra small plastic package; 3 solder lands; body 1.0 x 0.6 x 0.5 mm

**Fig. 18. Package outline DFN1006-3 (SOT883)**

<table>
<thead>
<tr>
<th>UNIT</th>
<th>A(1)</th>
<th>A1 max</th>
<th>b</th>
<th>b1</th>
<th>D</th>
<th>E</th>
<th>e1</th>
<th>L</th>
<th>L1</th>
</tr>
</thead>
<tbody>
<tr>
<td>mm</td>
<td>0.50</td>
<td>0.03</td>
<td>0.20</td>
<td>0.12</td>
<td>0.55</td>
<td>0.62</td>
<td>1.02</td>
<td>0.95</td>
<td>0.35</td>
</tr>
</tbody>
</table>

**Note**
1. Including plating thickness

<table>
<thead>
<tr>
<th>OUTLINE VERSION</th>
<th>REFERENCES</th>
<th>EUROPEAN PROJECTION</th>
<th>ISSUE DATE</th>
</tr>
</thead>
<tbody>
<tr>
<td>SOT883</td>
<td>IEC</td>
<td>JEDEC</td>
<td>SC-101</td>
</tr>
</tbody>
</table>

DIMENSIONS (mm are the original dimensions)

0 0.5 1 mm

scale
13. Soldering

Fig. 19. Reflow soldering footprint for DFN1006-3 (SOT883)
### 14. Revision history

<table>
<thead>
<tr>
<th>Data sheet ID</th>
<th>Release date</th>
<th>Data sheet status</th>
<th>Change notice</th>
<th>Supersedes</th>
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</thead>
<tbody>
<tr>
<td>PMZ600UNEL v.1</td>
<td>20160628</td>
<td>Product data sheet</td>
<td>-</td>
<td>-</td>
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</table>
15. Legal information

15.1 Data sheet status

<table>
<thead>
<tr>
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<th></th>
<th></th>
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</thead>
<tbody>
<tr>
<td>Objective [short] data sheet</td>
<td>Development</td>
<td>This document contains data from the objective specification for product development.</td>
</tr>
<tr>
<td>Preliminary [short] data sheet</td>
<td>Qualification</td>
<td>This document contains data from the preliminary specification.</td>
</tr>
<tr>
<td>Product [short] data sheet</td>
<td>Production</td>
<td>This document contains the product specification.</td>
</tr>
</tbody>
</table>

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term ‘short data sheet’ is explained in section “Definitions”.

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

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