



# PMP4501Y

45 V, 100 mA NPN/NPN matched double transistor

28 December 2022

Product data sheet

## 1. General description

NPN/NPN matched double transistor in a very small Surface-Mounted Device (SMD) SOT363 (SC-88) plastic package. The transistors in the SOT363 package are fully isolated internally.

NPN/NPN  $h_{FE1}/h_{FE2}$  0.98 complement: PMP4201Y

PNP/PNP complement: PMP5501Y

## 2. Features and benefits

- Current gain matching
- Base-emitter voltage matching
- Application-optimized pinout
- AEC-Q101 qualified

## 3. Applications

- Current mirror
- Differential amplifier

## 4. Quick reference data

Table 1. Quick reference data

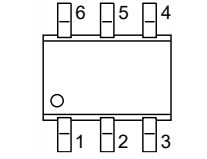
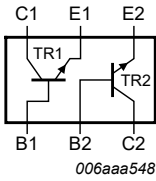
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
<b>Per transistor</b>							
$V_{CEO}$	collector-emitter voltage	open base		-	-	45	V
$I_C$	collector current			-	-	100	mA
$h_{FE}$	DC current gain	$V_{CE} = 5\text{ V}; I_C = 2\text{ mA}; T_{amb} = 25\text{ °C}$		200	290	450	
<b>Per device</b>							
$h_{FE1}/h_{FE2}$	DC current gain matching	$V_{CE} = 5\text{ V}; I_C = 2\text{ mA}; T_{amb} = 25\text{ °C}$	[1]	0.95	1	-	
$V_{BE1}-V_{BE2}$	base-emitter voltage matching		[2]	-	-	2	mV

[1] The smaller of the two values is taken as the numerator.

[2] The smaller of the two values is subtracted from the larger value.

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B1	base TR1	 <p>TSSOP6 (SOT363)</p>	 <p>006aaa548</p>
2	B2	base TR2		
3	C2	collector TR2		
4	E2	emitter TR2		
5	E1	emitter TR1		
6	C1	collector TR1		

## 6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
<a href="#">PMP4501Y</a>	TSSOP6	plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	<a href="#">SOT363</a>

## 7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PMP4501Y	S8%

[1] % = placeholder for manufacturing site code

## 8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
<b>Per transistor</b>					
$V_{CBO}$	collector-base voltage	open emitter	-	50	V
$V_{CEO}$	collector-emitter voltage	open base	-	45	V
$V_{EBO}$	emitter-base voltage	open collector	-	6	V
$I_C$	collector current		-	100	mA
$I_{CM}$	peak collector current	$t_p \leq 1$ ms	-	200	mA
$P_{tot}$	total power dissipation	$T_{amb} \leq 25$ °C	[1]	200	mW
<b>Per device</b>					
$P_{tot}$	total power dissipation	$T_{amb} \leq 25$ °C	[1]	300	mW
$T_j$	junction temperature		-	150	°C
$T_{amb}$	ambient temperature		-65	150	°C
$T_{stg}$	storage temperature		-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

## 9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
<b>Per transistor</b>							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W
<b>Per device</b>							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	416	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

## 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
<b>Per transistor</b>							
$I_{CBO}$	collector-base cut-off current	$V_{CB} = 30 \text{ V}; I_E = 0 \text{ A}; T_{amb} = 25 \text{ }^\circ\text{C}$		-	-	15	nA
		$V_{CB} = 30 \text{ V}; I_E = 0 \text{ A}; T_j = 150 \text{ }^\circ\text{C}$		-	-	5	$\mu\text{A}$
$I_{EBO}$	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_C = 0 \text{ A}; T_{amb} = 25 \text{ }^\circ\text{C}$		-	-	100	nA
$h_{FE}$	DC current gain	$V_{CE} = 5 \text{ V}; I_C = 10 \text{ } \mu\text{A}; T_{amb} = 25 \text{ }^\circ\text{C}$		-	250	-	
		$V_{CE} = 5 \text{ V}; I_C = 2 \text{ mA}; T_{amb} = 25 \text{ }^\circ\text{C}$		200	290	450	
$V_{CEsat}$	collector-emitter saturation voltage	$I_C = 10 \text{ mA}; I_B = 0.5 \text{ mA}; T_{amb} = 25 \text{ }^\circ\text{C}$		-	50	200	mV
		$I_C = 100 \text{ mA}; I_B = 5 \text{ mA}; T_{amb} = 25 \text{ }^\circ\text{C}$		-	200	400	mV
$V_{BEsat}$	base-emitter saturation voltage	$I_C = 10 \text{ mA}; I_B = 0.5 \text{ mA}; T_{amb} = 25 \text{ }^\circ\text{C}$	[1]	-	760	-	mV
		$I_C = 100 \text{ mA}; I_B = 5 \text{ mA}; T_{amb} = 25 \text{ }^\circ\text{C}$	[1]	-	910	-	mV
$V_{BE}$	base-emitter voltage	$V_{CE} = 5 \text{ V}; I_C = 2 \text{ mA}; T_{amb} = 25 \text{ }^\circ\text{C}$	[2]	610	660	710	mV
		$V_{CE} = 5 \text{ V}; I_C = 10 \text{ mA}; T_{amb} = 25 \text{ }^\circ\text{C}$	[2]	-	-	770	mV
$C_c$	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; T_{amb} = 25 \text{ }^\circ\text{C}$		-	-	1.5	pF
$C_e$	emitter capacitance	$V_{EB} = 0.5 \text{ V}; I_C = 0 \text{ A}; i_c = 0 \text{ A}; f = 1 \text{ MHz}; T_{amb} = 25 \text{ }^\circ\text{C}$		-	11	-	pF
$f_T$	transition frequency	$V_{CE} = 5 \text{ V}; I_C = 10 \text{ mA}; f = 100 \text{ MHz}; T_{amb} = 25 \text{ }^\circ\text{C}$		100	250	-	MHz
NF	noise figure	$V_{CE} = 5 \text{ V}; I_C = 0.2 \text{ mA}; R_S = 2 \text{ k}\Omega; f = 10 \text{ Hz to } 15.7 \text{ kHz}; T_{amb} = 25 \text{ }^\circ\text{C}$		-	2.8	-	dB
		$V_{CE} = 5 \text{ V}; I_C = 0.2 \text{ mA}; R_S = 2 \text{ k}\Omega; B = 200 \text{ Hz}; f = 1 \text{ kHz}; T_{amb} = 25 \text{ }^\circ\text{C}$		-	3.3	-	dB
<b>Per device</b>							
$h_{FE1}/h_{FE2}$	DC current gain matching	$V_{CE} = 5 \text{ V}; I_C = 2 \text{ mA}; T_{amb} = 25 \text{ }^\circ\text{C}$	[3]	0.95	1	-	
$V_{BE1}-V_{BE2}$	base-emitter voltage matching		[4]	-	-	2	mV

[1]  $V_{BEsat}$  decreases by about 1.7 mV/K with increasing temperature.

[2]  $V_{BE}$  decreases by about 2 mV/K with increasing temperature.

[3] The smaller of the two values is taken as the numerator.

[4] The smaller of the two values is subtracted from the larger value.

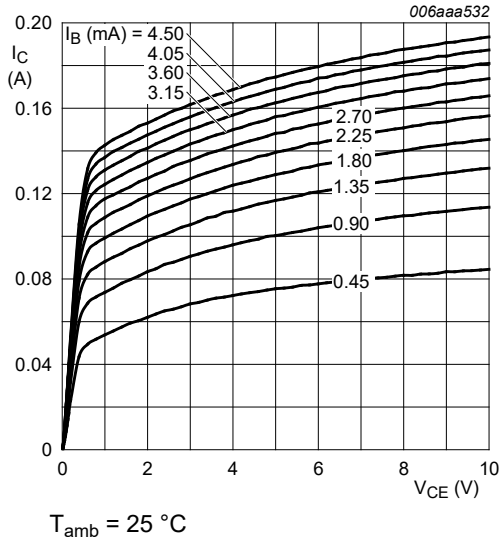


Fig. 1. Collector current as a function of collector-emitter voltage; typical values

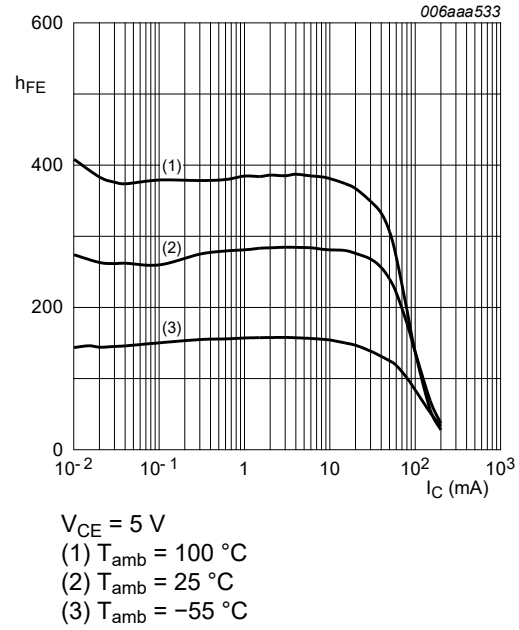


Fig. 2. DC current gain as a function of collector current; typical values

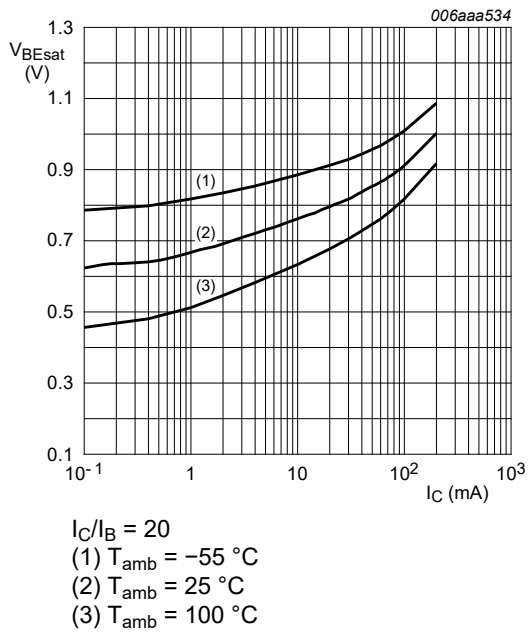


Fig. 3. Base-emitter saturation voltage as a function of collector current; typical values

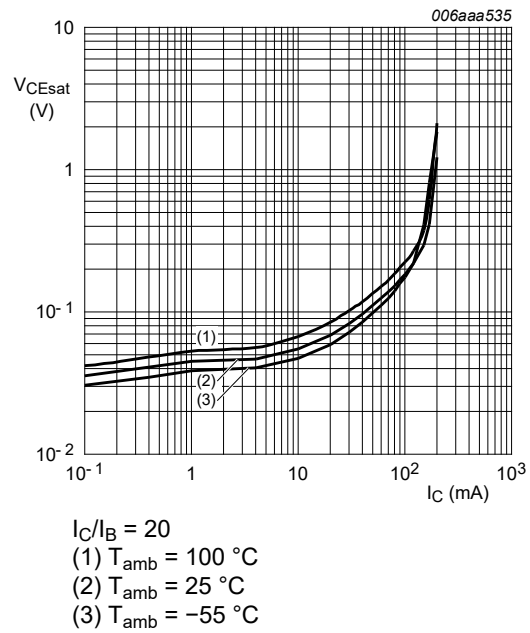
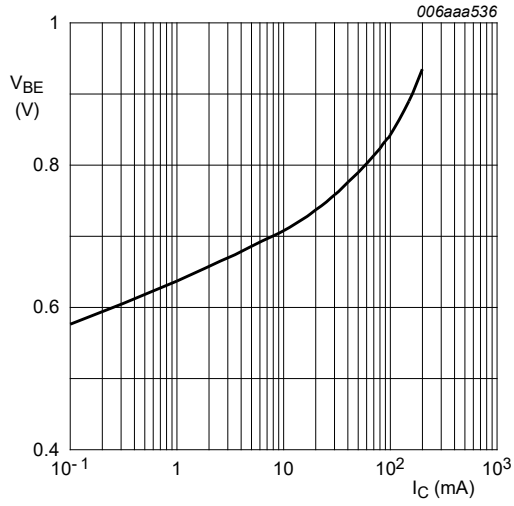
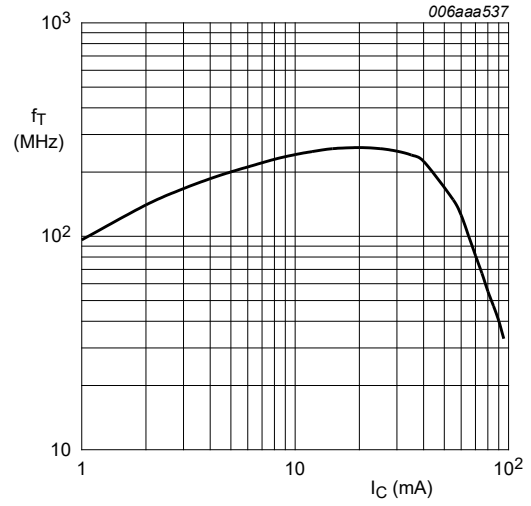


Fig. 4. Collector-emitter saturation voltage as a function of collector current; typical values



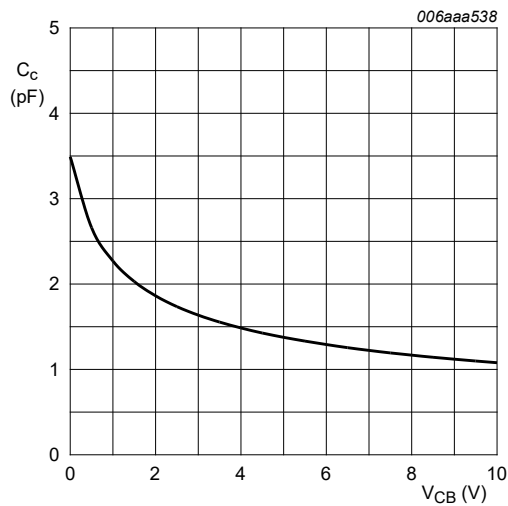
$V_{CE} = 5$  V;  $T_{amb} = 25$  °C

Fig. 5. Base-emitter voltage as a function of collector current; typical values



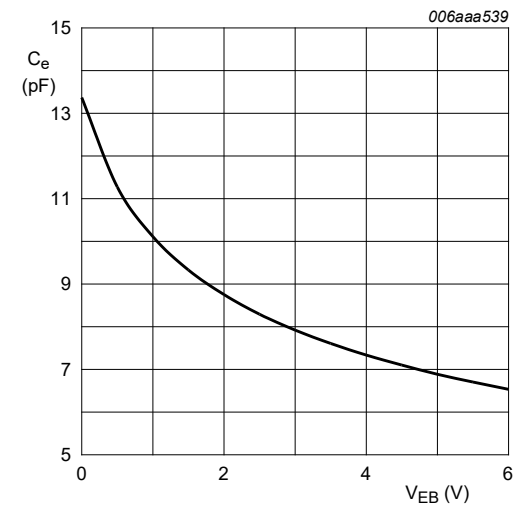
$V_{CE} = 5$  V;  $T_{amb} = 25$  °C

Fig. 6. Transition frequency as a function of collector current; typical values



$f = 1$  MHz;  $T_{amb} = 25$  °C

Fig. 7. Collector capacitance as a function of collector-base voltage; typical values



$f = 1$  MHz;  $T_{amb} = 25$  °C

Fig. 8. Emitter capacitance as a function of emitter-base voltage; typical values

## 11. Application information

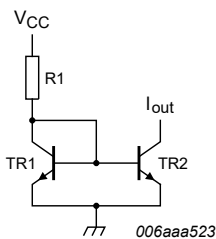


Fig. 9. Current mirror

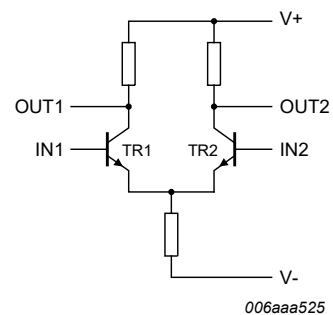


Fig. 10. Differential amplifier

## 12. Test information

### Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 13. Package outline

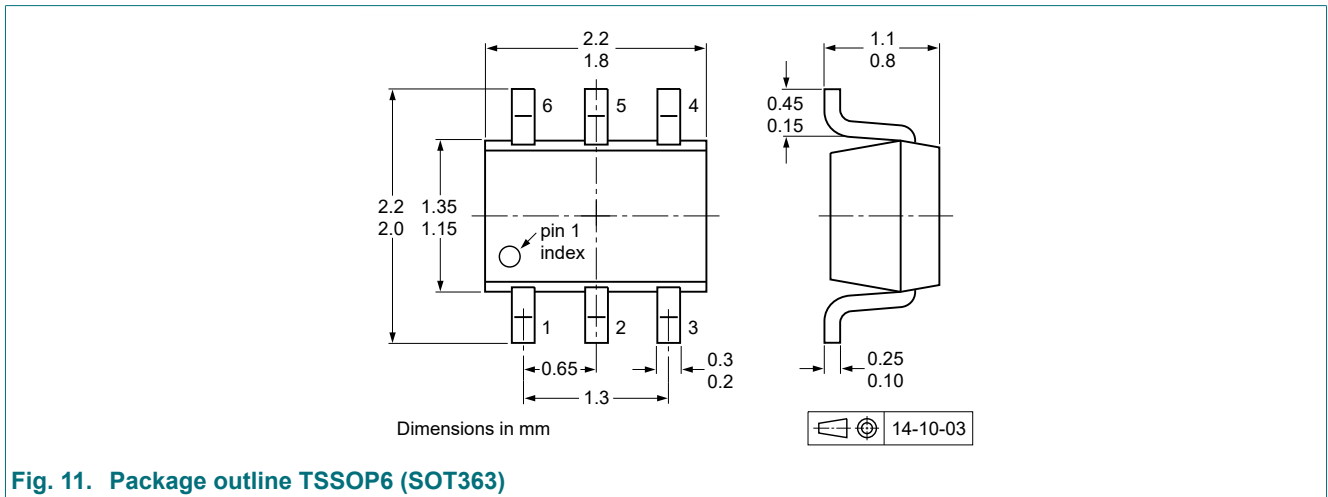


Fig. 11. Package outline TSSOP6 (SOT363)

## 14. Soldering

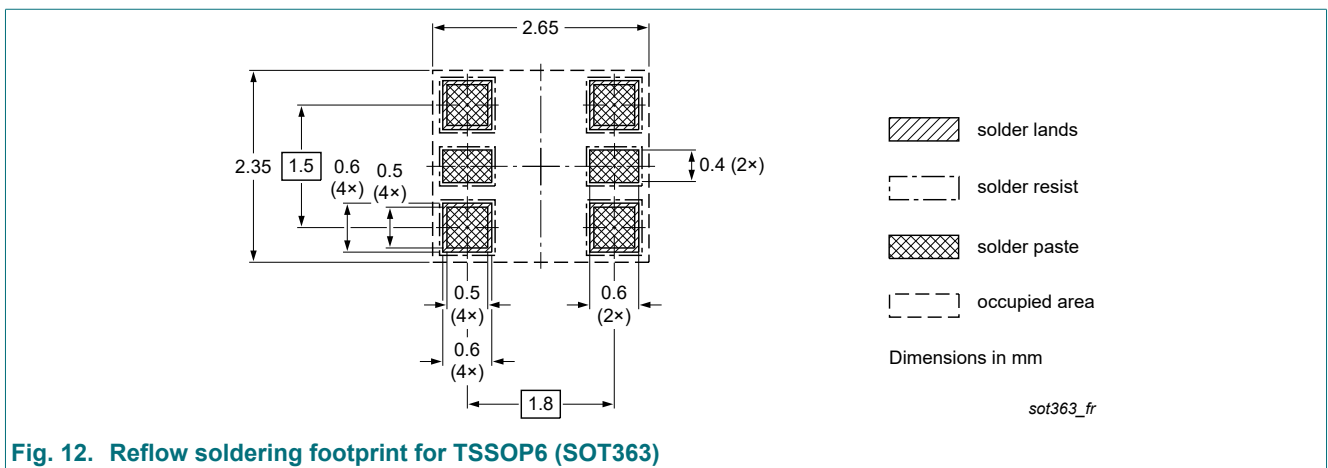
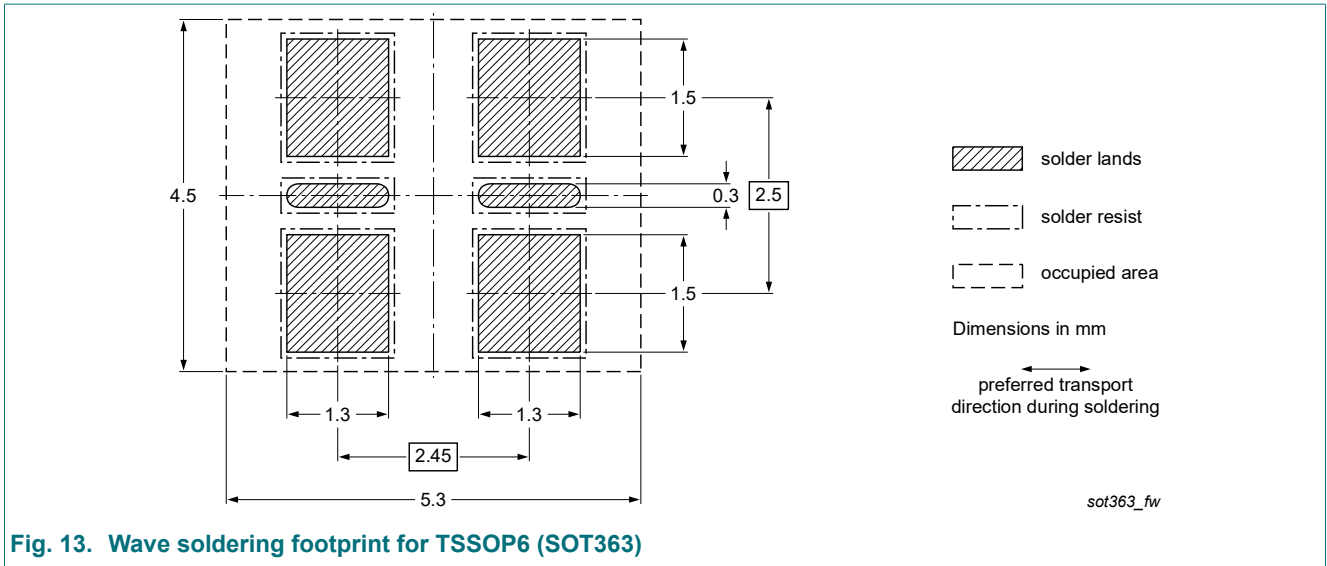


Fig. 12. Reflow soldering footprint for TSSOP6 (SOT363)



## 15. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMP4501Y v.5	20221228	Product data sheet	-	PMP4501V_G_Y_4
Modifications:	<ul style="list-style-type: none"> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Packing information removed.</li> </ul>			
PMP4501V_G_Y_4	20090828	Product data sheet	-	PMP4501V_G_Y_3
PMP4501V_G_Y_3	20060919	Product data sheet	-	PMP4501G_Y_2
PMP4501G_Y_2	20060214	Product data sheet	-	PMP4501G_Y_1
PMP4501G_Y_1	20060202	Product data sheet	-	-



## 16. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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