1. General description

Trench Maximum Efficiency General Application (MEGA) Schottky barrier rectifier encapsulated in a CFP5 (SOD128) small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: $I_{F(AV)} \leq 3$ A
- Reverse voltage: $V_R \leq 60$ V
- Low forward voltage
- Low leakage current due to Trench MEGA Schottky technology
- High power capability due to clip-bonding technology
- Small and flat lead SMD power plastic package
- Capable for reflow and wave soldering
- AEC-Q101 qualified

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Freewheeling application
- Reverse polarity protection
- Low power consumption application

4. Quick reference data

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter Description</th>
<th>Conditions</th>
<th>$I_{F(AV)}$</th>
<th>$V_R$</th>
<th>$V_F$</th>
<th>$I_R$</th>
</tr>
</thead>
<tbody>
<tr>
<td>$I_{F(AV)}$</td>
<td>average forward current</td>
<td>$\delta = 0.5; f = 20$ kHz; $T_{sp} \leq 155$ °C; square wave</td>
<td>$-\cdot-\cdot3$ A</td>
<td>$-\cdot-\cdot60$ V</td>
<td>$[1]\ 550\ 620$ mV</td>
<td>$[1]\ 0.14\ 0.9$ µA</td>
</tr>
<tr>
<td>$V_R$</td>
<td>reverse voltage</td>
<td>$T_j = 25$ °C</td>
<td>$-\cdot-\cdot60$ V</td>
<td>$-\cdot-\cdot60$ V</td>
<td>$[1]\ 0.3\ 1.8$ µA</td>
<td></td>
</tr>
<tr>
<td>$V_F$</td>
<td>forward voltage</td>
<td>$I_F = 3$ A; pulsed; $T_j = 25$ °C</td>
<td>$[1]\ 550\ 620$ mV</td>
<td>$[1]\ 0.14\ 0.9$ µA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$I_R$</td>
<td>reverse current</td>
<td>$V_R = 10$ V; pulsed; $T_j = 25$ °C</td>
<td>$[1]\ 0.3\ 1.8$ µA</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

[1] Very short pulse, in order to maintain a stable junction temperature.
5. Pinning information

Table 2. Pinning information

<table>
<thead>
<tr>
<th>Pin</th>
<th>Symbol</th>
<th>Description</th>
<th>Simplified outline</th>
<th>Graphic symbol</th>
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</thead>
<tbody>
<tr>
<td>1</td>
<td>K</td>
<td>cathode</td>
<td><img src="image" alt="Simplified outline" /></td>
<td>K <img src="image" alt="Symbol" /></td>
</tr>
<tr>
<td>2</td>
<td>A</td>
<td>anode</td>
<td></td>
<td>A <img src="image" alt="Symbol" /></td>
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</table>

6. Ordering information

Table 3. Ordering information

<table>
<thead>
<tr>
<th>Type number</th>
<th>Package</th>
<th>Name</th>
<th>Description</th>
<th>Version</th>
</tr>
</thead>
<tbody>
<tr>
<td>PMEG60T30ELP</td>
<td>CFP5</td>
<td>plastic, surface mounted package; 2 terminals; 4 mm pitch; 3.8 mm x 2.6 mm x 1 mm body</td>
<td>SOD128</td>
<td></td>
</tr>
</tbody>
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7. Marking

Table 4. Marking codes

<table>
<thead>
<tr>
<th>Type number</th>
<th>Marking code</th>
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<tbody>
<tr>
<td>PMEG60T30ELP</td>
<td>E2</td>
</tr>
</tbody>
</table>
8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>V_R</td>
<td>reverse voltage</td>
<td>T_j = 25 °C</td>
<td>-</td>
<td>60</td>
<td>V</td>
</tr>
<tr>
<td>I_F</td>
<td>forward current</td>
<td>δ = 1; T_sp ≤ 150 °C</td>
<td>-</td>
<td>4.2</td>
<td>A</td>
</tr>
<tr>
<td>I_F(AV)</td>
<td>average forward current</td>
<td>δ = 0.5; f = 20 kHz; T_sp ≤ 155 °C; square wave</td>
<td>-</td>
<td>3</td>
<td>A</td>
</tr>
<tr>
<td>I_FSM</td>
<td>non-repetitive peak forward current</td>
<td>t_p = 8 ms; square wave; T_j(init) = 25 °C</td>
<td>-</td>
<td>50</td>
<td>A</td>
</tr>
<tr>
<td>P_tot</td>
<td>total power dissipation</td>
<td>T_amb ≤ 25 °C</td>
<td></td>
<td>0.75</td>
<td>W</td>
</tr>
<tr>
<td></td>
<td>[1]</td>
<td></td>
<td></td>
<td>1.2</td>
<td>W</td>
</tr>
<tr>
<td>T_j</td>
<td>junction temperature</td>
<td></td>
<td>-</td>
<td>175</td>
<td>°C</td>
</tr>
<tr>
<td>T_amb</td>
<td>ambient temperature</td>
<td></td>
<td></td>
<td></td>
<td>°C</td>
</tr>
<tr>
<td>T_stg</td>
<td>storage temperature</td>
<td></td>
<td>-65</td>
<td>175</td>
<td>°C</td>
</tr>
</tbody>
</table>


9. Thermal characteristics

Table 6. Thermal characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>R_θ(j-a)</td>
<td>thermal resistance from junction to ambient</td>
<td>in free air</td>
<td></td>
<td></td>
<td>200</td>
<td>KW</td>
</tr>
<tr>
<td></td>
<td>[1]</td>
<td>[2]</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>R_θ(j-sp)</td>
<td>thermal resistance from junction to solder point</td>
<td></td>
<td></td>
<td></td>
<td>120</td>
<td>KW</td>
</tr>
<tr>
<td></td>
<td>[3]</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.
**60 V, 3 A low leakage current Trench MEGA Schottky barrier rectifier**

![Graph 1](aaa-028064)

**FR4 PCB, standard footprint**

**Fig. 1.** Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

![Graph 2](aaa-028065)

**FR4 PCB, mounting pad for cathode 1 cm²**

**Fig. 2.** Transient thermal impedance from junction to ambient as a function of pulse duration; typical values
## 10. Characteristics

### Table 7. Characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>(V_{(BR)})</td>
<td>reverse breakdown voltage</td>
<td>1 mA; pulsed; (T_j = 25 ^\circ C)</td>
<td>[1]</td>
<td>60</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>(V_F)</td>
<td>forward voltage</td>
<td>0.1 A; pulsed; (T_j = 25 ^\circ C)</td>
<td>[1]</td>
<td>380</td>
<td>450</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>0.2 A; pulsed; (T_j = 25 ^\circ C)</td>
<td>[1]</td>
<td>440</td>
<td>510</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>0.5 A; pulsed; (T_j = 25 ^\circ C)</td>
<td>[1]</td>
<td>470</td>
<td>540</td>
<td>mV</td>
</tr>
<tr>
<td>(I_R)</td>
<td>reverse current</td>
<td>V(_R) = 10 V; pulsed; (T_j = 25 ^\circ C)</td>
<td>[1]</td>
<td>0.14</td>
<td>0.9</td>
<td>µA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>V(_R) = 40 V; pulsed; (T_j = 25 ^\circ C)</td>
<td>[1]</td>
<td>0.18</td>
<td>-</td>
<td>µA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>V(_R) = 60 V; pulsed; (T_j = 25 ^\circ C)</td>
<td>[1]</td>
<td>0.3</td>
<td>1.8</td>
<td>µA</td>
</tr>
<tr>
<td>(C_d)</td>
<td>diode capacitance</td>
<td>V(_R) = 1 V; f = 1 MHz; (T_j = 25 ^\circ C)</td>
<td>[1]</td>
<td>560</td>
<td>-</td>
<td>pF</td>
</tr>
<tr>
<td></td>
<td></td>
<td>V(_R) = 10 V; f = 1 MHz; (T_j = 25 ^\circ C)</td>
<td>[1]</td>
<td>170</td>
<td>-</td>
<td>pF</td>
</tr>
<tr>
<td>(t_{rr})</td>
<td>reverse recovery time step recovery</td>
<td>0.5 A; (I_R = 0.5 A; I_{R\text{meas}} = 0.1 A; T_j = 25 ^\circ C)</td>
<td>[1]</td>
<td>16</td>
<td>-</td>
<td>ns</td>
</tr>
<tr>
<td></td>
<td>reverse recovery time ramp recovery</td>
<td>d(_I_F/dt = 200 A/µs; I_F = 6 A; V_R = 26 V; T_j = 25 ^\circ C)</td>
<td>[1]</td>
<td>16</td>
<td>-</td>
<td>ns</td>
</tr>
<tr>
<td>(V_{FRM})</td>
<td>peak forward recovery voltage</td>
<td>0.5 A; d(_I_F/dt = 20 A/µs; T_j = 25 ^\circ C)</td>
<td>[1]</td>
<td>460</td>
<td>-</td>
<td>mV</td>
</tr>
</tbody>
</table>

[1] Very short pulse, in order to maintain a stable junction temperature.
Fig. 3. Forward current as a function of forward voltage; typical values

Fig. 4. Reverse current as a function of reverse voltage; typical values

Fig. 5. Diode capacitance as a function of reverse voltage; typical values

Fig. 6. Average forward power dissipation as a function of average forward current; typical values
**PMEG60T30ELP**

60 V, 3 A low leakage current Trench MEGA Schottky barrier rectifier

---

**Fig. 7.** Average reverse power dissipation as a function of reverse voltage; typical values

- $T_j = 100 \, ^\circ\text{C}$
  - (1) $\delta = 1$; DC
  - (2) $\delta = 0.9$
  - (3) $\delta = 0.8$
  - (4) $\delta = 0.5$
  - (5) $\delta = 0.2$

**FR4 PCB, standard footprint**
- $T_j = 175 \, ^\circ\text{C}$
  - (1) $\delta = 1$; DC
  - (2) $\delta = 0.5$; $f = 20 \, \text{kHz}$
  - (3) $\delta = 0.2$; $f = 20 \, \text{kHz}$
  - (4) $\delta = 0.1$; $f = 20 \, \text{kHz}$

**Fig. 8.** Average forward current as a function of ambient temperature; typical values

- $T_j = 175 \, ^\circ\text{C}$
  - (1) $\delta = 1$; DC
  - (2) $\delta = 0.5$; $f = 20 \, \text{kHz}$
  - (3) $\delta = 0.2$; $f = 20 \, \text{kHz}$
  - (4) $\delta = 0.1$; $f = 20 \, \text{kHz}$

**Fig. 9.** Average forward current as a function of ambient temperature; typical values

- $T_j = 175 \, ^\circ\text{C}$
  - (1) $\delta = 1$; DC
  - (2) $\delta = 0.5$; $f = 20 \, \text{kHz}$
  - (3) $\delta = 0.2$; $f = 20 \, \text{kHz}$
  - (4) $\delta = 0.1$; $f = 20 \, \text{kHz}$

**Fig. 10.** Average forward current as a function of solder point temperature; typical values

- $T_j = 175 \, ^\circ\text{C}$
  - (1) $\delta = 1$; DC
  - (2) $\delta = 0.5$; $f = 20 \, \text{kHz}$
  - (3) $\delta = 0.2$; $f = 20 \, \text{kHz}$
  - (4) $\delta = 0.1$; $f = 20 \, \text{kHz}$

---

FR4 PCB, mounting pad for cathode 1 cm²

- $T_j = 175 \, ^\circ\text{C}$
  - (1) $\delta = 1$; DC
  - (2) $\delta = 0.5$; $f = 20 \, \text{kHz}$
  - (3) $\delta = 0.2$; $f = 20 \, \text{kHz}$
  - (4) $\delta = 0.1$; $f = 20 \, \text{kHz}$

---

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Product data sheet

24 May 2018
11. Test information

Fig. 11. Reverse recovery definition; step recovery

Fig. 12. Reverse recovery definition; ramp recovery

Fig. 13. Forward recovery definition
The current ratings for the typical waveforms are calculated according to the equations:

$I_{F(\text{AV})} = I_M \times \delta$ with $I_M$ defined as peak current,

$I_{\text{RMS}} = I_{F(\text{AV})}$ at DC, and $I_{\text{RMS}} = I_M \times \sqrt{\delta}$

with $I_{\text{RMS}}$ defined as RMS current.

**Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

**12. Package outline**

---

**Fig. 14. Duty cycle definition**

**Fig. 15. Package outline CFP5 (SOD128)**
13. Soldering

Fig. 16. Reflow soldering footprint for CFP5 (SOD128)
60 V, 3 A low leakage current Trench MEGA Schottky barrier rectifier

Wave soldering footprint information

Fig. 17. Wave soldering footprint for CFP5 (SOD128)
14. Revision history

Table 8. Revision history

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<th>Release date</th>
<th>Data sheet status</th>
<th>Change notice</th>
<th>Supersedes</th>
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<td>20180524</td>
<td>Product data sheet</td>
<td>-</td>
<td>PMEG60T30ELP v.1</td>
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<td>PMEG60T30ELP v.1</td>
<td>20180227</td>
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15. Legal information

Data sheet status

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<td>Development</td>
<td>This document contains data from the objective specification for product development.</td>
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<tr>
<td>Preliminary [short] data sheet</td>
<td>Qualification</td>
<td>This document contains data from the preliminary specification.</td>
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<tr>
<td>Product [short] data sheet</td>
<td>Production</td>
<td>This document contains the product specification.</td>
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[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com.

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