1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD128 small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: $I_{F(AV)} \leq 3$ A
- Reverse voltage: $V_R \leq 40$ V
- Low forward voltage
- High power capability due to clip-bonding technology
- Small and flat lead SMD plastic package
- AEC-Q101 qualified
- High temperature $T_j \leq 175 \degree$ C
- Capable for reflow and wave soldering

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications
- High temperature applications

4. Quick reference data

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$I_{F(AV)}$</td>
<td>average forward current</td>
<td>$\delta = 0.5; f = 20$ kHz; $T_{amb} \leq 85 \degree$ C; square wave</td>
<td>[1]</td>
<td>-</td>
<td>-</td>
<td>A</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$\delta = 0.5; f = 20$ kHz; $T_{sp} \leq 165 \degree$ C; square wave</td>
<td>-</td>
<td>-</td>
<td>3</td>
<td>A</td>
</tr>
<tr>
<td>$V_R$</td>
<td>reverse voltage</td>
<td>$T_j = 25 \degree$ C</td>
<td>-</td>
<td>-</td>
<td>40</td>
<td>V</td>
</tr>
<tr>
<td>$V_F$</td>
<td>forward voltage</td>
<td>$I_F = 3$ A; $T_j = 25 \degree$ C</td>
<td>-</td>
<td>430</td>
<td>490</td>
<td>mV</td>
</tr>
<tr>
<td>$I_R$</td>
<td>reverse current</td>
<td>$V_R = 40$ V; $T_j = 25 \degree$ C</td>
<td>-</td>
<td>35</td>
<td>200</td>
<td>$\mu$A</td>
</tr>
</tbody>
</table>

5. Pinning information

Table 2. Pinning information

<table>
<thead>
<tr>
<th>Pin</th>
<th>Symbol</th>
<th>Description</th>
<th>Simplified outline</th>
<th>Graphic symbol</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>K</td>
<td>cathode[1]</td>
<td>![Simplified outline]</td>
<td>K</td>
</tr>
<tr>
<td>2</td>
<td>A</td>
<td>anode</td>
<td></td>
<td>A</td>
</tr>
</tbody>
</table>

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

<table>
<thead>
<tr>
<th>Type number</th>
<th>Package</th>
<th>Name</th>
<th>Description</th>
<th>Version</th>
</tr>
</thead>
<tbody>
<tr>
<td>PMEG4030ETP</td>
<td>CFP5</td>
<td>plastic, surface mounted package; 2 terminals; 4 mm pitch; 3.8 mm x 2.6 mm x 1 mm body</td>
<td>SOD128</td>
<td></td>
</tr>
</tbody>
</table>

7. Marking

Table 4. Marking codes

<table>
<thead>
<tr>
<th>Type number</th>
<th>Marking code</th>
</tr>
</thead>
<tbody>
<tr>
<td>PMEG4030ETP</td>
<td>C3</td>
</tr>
</tbody>
</table>
8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>VR</td>
<td>reverse voltage</td>
<td>Tj = 25 °C</td>
<td>-</td>
<td>40</td>
<td>V</td>
</tr>
<tr>
<td>IF(AV)</td>
<td>average forward current</td>
<td>δ = 0.5; f = 20 kHz; Tamb ≤ 85 °C; square wave</td>
<td>-</td>
<td>3</td>
<td>A</td>
</tr>
<tr>
<td></td>
<td></td>
<td>δ = 0.5; f = 20 kHz; Tsp ≤ 165 °C; square wave</td>
<td>-</td>
<td>3</td>
<td>A</td>
</tr>
<tr>
<td>IFSM</td>
<td>non-repetitive peak forward current</td>
<td>tp = 8 ms; square wave; Tj(init) = 25 °C</td>
<td>-</td>
<td>50</td>
<td>A</td>
</tr>
<tr>
<td>Ptot</td>
<td>total power dissipation</td>
<td>Tamb ≤ 25 °C</td>
<td>[2]</td>
<td>-</td>
<td>750 mW</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[3]</td>
<td>-</td>
<td>1.25 W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[1]</td>
<td>-</td>
<td>2.5 W</td>
</tr>
<tr>
<td>Tj</td>
<td>junction temperature</td>
<td></td>
<td>-</td>
<td>175</td>
<td>°C</td>
</tr>
<tr>
<td>Tamb</td>
<td>ambient temperature</td>
<td></td>
<td>-55</td>
<td>175</td>
<td>°C</td>
</tr>
<tr>
<td>Tstg</td>
<td>storage temperature</td>
<td></td>
<td>-65</td>
<td>175</td>
<td>°C</td>
</tr>
</tbody>
</table>


9. Thermal characteristics

Table 6. Thermal characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rth(j-a)</td>
<td>thermal resistance from junction to ambient</td>
<td>in free air</td>
<td>[1]</td>
<td>[2]</td>
<td>-</td>
<td>200 K/W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[1]</td>
<td>[3]</td>
<td>-</td>
<td>120 K/W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[1]</td>
<td>[4]</td>
<td>-</td>
<td>60 K/W</td>
</tr>
<tr>
<td>Rth(j-sp)</td>
<td>thermal resistance from junction to solder point</td>
<td></td>
<td>[5]</td>
<td>-</td>
<td>12</td>
<td>K/W</td>
</tr>
</tbody>
</table>

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.
**Fig. 1.** Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

**Fig. 2.** Transient thermal impedance from junction to ambient as a function of pulse duration; typical values
**10. Characteristics**

Table 7. Characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_F$</td>
<td>forward voltage</td>
<td>$I_F = 0.1 , A; ; T_J = 25 , ^{\circ}C$</td>
<td>- 285</td>
<td>320</td>
<td>-</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_F = 1 , A; ; T_J = 25 , ^{\circ}C$</td>
<td>- 360</td>
<td>420</td>
<td>-</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_F = 3 , A; ; T_J = 25 , ^{\circ}C$</td>
<td>- 430</td>
<td>490</td>
<td>-</td>
<td>mV</td>
</tr>
<tr>
<td>$I_R$</td>
<td>reverse current</td>
<td>$V_R = 10 , V; ; T_J = 25 , ^{\circ}C$</td>
<td>7</td>
<td>-</td>
<td>-</td>
<td>µA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_R = 40 , V; ; T_J = 25 , ^{\circ}C$</td>
<td>35</td>
<td>200</td>
<td>-</td>
<td>µA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_R = 10 , V; ; T_J = 125 , ^{\circ}C$</td>
<td>6</td>
<td>-</td>
<td>-</td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_R = 40 , V; ; T_J = 125 , ^{\circ}C$</td>
<td>23</td>
<td>-</td>
<td>-</td>
<td>mA</td>
</tr>
<tr>
<td>$C_d$</td>
<td>diode capacitance</td>
<td>$V_R = 1 , V; ; f = 1 , MHz; ; T_J = 25 , ^{\circ}C$</td>
<td>350</td>
<td>-</td>
<td>-</td>
<td>pF</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_R = 10 , V; ; f = 1 , MHz; ; T_J = 25 , ^{\circ}C$</td>
<td>140</td>
<td>-</td>
<td>-</td>
<td>pF</td>
</tr>
</tbody>
</table>
40 V, 3 A low VF MEGA Schottky barrier rectifier

Fig. 4. Forward current as a function of forward voltage; typical values

- (1) $T_J = 175 \, ^\circ C$
- (2) $T_J = 150 \, ^\circ C$
- (3) $T_J = 125 \, ^\circ C$
- (4) $T_J = 85 \, ^\circ C$
- (5) $T_J = 25 \, ^\circ C$
- (6) $T_J = -40 \, ^\circ C$

Fig. 5. Reverse current as a function of reverse voltage; typical values

- (1) $T_J = 150 \, ^\circ C$
- (2) $T_J = 125 \, ^\circ C$
- (3) $T_J = 85 \, ^\circ C$
- (4) $T_J = 25 \, ^\circ C$
- (5) $T_J = -40 \, ^\circ C$

Fig. 6. Diode capacitance as a function of reverse voltage; typical values

- $f = 1 \, MHz$; $T_{amb} = 25 \, ^\circ C$

Fig. 7. Average forward power dissipation as a function of average forward current; typical values

- $T_J = 175 \, ^\circ C$
  - (1) $\delta = 0.1$
  - (2) $\delta = 0.2$
  - (3) $\delta = 0.5$
  - (4) $\delta = 1.0$
40 V, 3 A low VF MEGA Schottky barrier rectifier

Fig. 8. Average reverse power dissipation as a function of reverse voltage; typical values

\[ V_R \text{ (V)} \]

\[ P_{R(\text{AV})} \text{ (W)} \]

\[ T_j = 150 \, ^\circ C \]

1. \( \delta = 1.0 \)
2. \( \delta = 0.9 \)
3. \( \delta = 0.8 \)
4. \( \delta = 0.5 \)

Fig. 9. Average forward current as a function of ambient temperature; typical values

\[ T_{\text{amb}} \text{ (°C)} \]

\[ I_{F(\text{AV})} \text{ (A)} \]

FR4 PCB, standard footprint

\[ T_j = 175 \, ^\circ C \]

1. \( \delta = 1.0 \) (DC)
2. \( \delta = 0.5; f = 20 \, \text{kHz} \)
3. \( \delta = 0.2; f = 20 \, \text{kHz} \)
4. \( \delta = 0.1; f = 20 \, \text{kHz} \)

Fig. 10. Average forward current as a function of ambient temperature; typical values

FR4 PCB, mounting pad for cathode 1 cm²

\[ T_j = 175 \, ^\circ C \]

1. \( \delta = 1.0 \)
2. \( \delta = 0.9 \)
3. \( \delta = 0.8 \)
4. \( \delta = 0.5 \)

Fig. 11. Average forward current as a function of ambient temperature; typical values

Ceramic PCB, \( \text{Al}_2\text{O}_3 \), standard footprint

\[ T_j = 175 \, ^\circ C \]

1. \( \delta = 1.0 \) (DC)
2. \( \delta = 0.5; f = 20 \, \text{kHz} \)
3. \( \delta = 0.2; f = 20 \, \text{kHz} \)
4. \( \delta = 0.1; f = 20 \, \text{kHz} \)
**11. Test information**

The current ratings for the typical waveforms are calculated according to the equations:

\[ I_{F(AV)} = I_M \times \delta \]

with \( I_M \) defined as peak current, \( I_{RMS} = I_{F(AV)} \) at DC, and \( I_{RMS} = I_M \times \sqrt{\delta} \) with \( I_{RMS} \) defined as RMS current.

**Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.
12. Package outline

![Package outline CFP5 (SOD128)](image1)

Fig. 14. Package outline CFP5 (SOD128)

13. Soldering

![Reflow soldering footprint for CFP5 (SOD128)](image2)

Fig. 15. Reflow soldering footprint for CFP5 (SOD128)
Wave soldering footprint information SOD128

Fig. 16. Wave soldering footprint for CFP5 (SOD128)
14. Revision history

Table 8. Revision history

<table>
<thead>
<tr>
<th>Data sheet ID</th>
<th>Release date</th>
<th>Data sheet status</th>
<th>Change notice</th>
<th>Supersedes</th>
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<tr>
<td>PMEG4030ETP v.2</td>
<td>20180328</td>
<td>Product data sheet</td>
<td>-</td>
<td>PMEG4030ETP v.1</td>
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<td>Modifications:</td>
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</tr>
<tr>
<td></td>
<td></td>
<td>Features and benefits: Capable for reflow and wave soldering added</td>
<td></td>
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</tr>
<tr>
<td></td>
<td></td>
<td>Soldering: Wave soldering footprint added</td>
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<tr>
<td>PMEG4030ETP v.1</td>
<td>20111010</td>
<td>Product data sheet</td>
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</table>
15. Legal information

Data sheet status

<table>
<thead>
<tr>
<th>Document status</th>
<th>Product status</th>
<th>Definition</th>
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<tbody>
<tr>
<td>Objective [short] data sheet</td>
<td>Development</td>
<td>This document contains data from the objective specification for product development.</td>
</tr>
<tr>
<td>Preliminary [short] data sheet</td>
<td>Qualification</td>
<td>This document contains data from the preliminary specification.</td>
</tr>
<tr>
<td>Product [short] data sheet</td>
<td>Production</td>
<td>This document contains the product specification.</td>
</tr>
</tbody>
</table>

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term "short data sheet" is explained in section "Definitions".
[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

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For more information, please visit: http://www.nexperia.com
For sales office addresses, please send an email to: salesaddresses@nexperia.com
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