

High temperature 40 V, 3 A low VF Schottky barrier rectifier 20 February 2023 Product data sheet

1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD128 small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: $I_{F(AV)} \le 3 A$
- Reverse voltage: $V_R \le 40 V$ •
- Low forward voltage •
- High power capability due to clip-bonding technology •
- Small and flat lead SMD plastic package
- High temperature T_i ≤ 175 °C
- Suitable for both reflow and wave soldering •
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications
- High temperature applications

4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 165 °C	-	-	3
V _R	reverse voltage	T _j = 25 °C	-	-	40
V _F	forward voltage	I _F = 3 A; T _j = 25 °C	-	430	490
I _R	reverse current	V _R = 40 V; T _i = 25 °C	-	35	200



Unit

A

V

mV

μA

5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	К	cathode[1]	n h	× F 4 A
2	A	anode		K 🛃 A
			CFP5 (SOD128)	sym001

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information						
Type number	Package	(age				
	Name	Description	Version			
PMEG4030ETP-Q	CFP5	plastic, surface mounted package; 2 terminals; 4 mm pitch; 3.8 mm x 2.6 mm x 1 mm body	<u>SOD128</u>			

7. Marking

Table 4. Marking codes			
Type number	Marking code		
PMEG4030ETP-Q	C3		

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	40	V
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{amb} ≤ 85 °C	[1]	-	3	A
		δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 165 °C		-	3	A
I _{FSM}	non-repetitive peak forward current	t_p = 8.3 ms; half-sine wave; $T_{j(init)}$ = 25 °C		-	50	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[2]	-	750	mW
			[3]	-	1.25	W
			[1]	-	2.5	W
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

[1] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics Symbol Parameter Conditions Min Тур Max Unit R_{th(j-a)} thermal resistance from in free air 200 K/W [1] [2] junction to ambient 120 K/W [1] [3] K/W [1] [4] 60 thermal resistance from K/W R_{th(j-sp)} [5] 12 junction to solder point

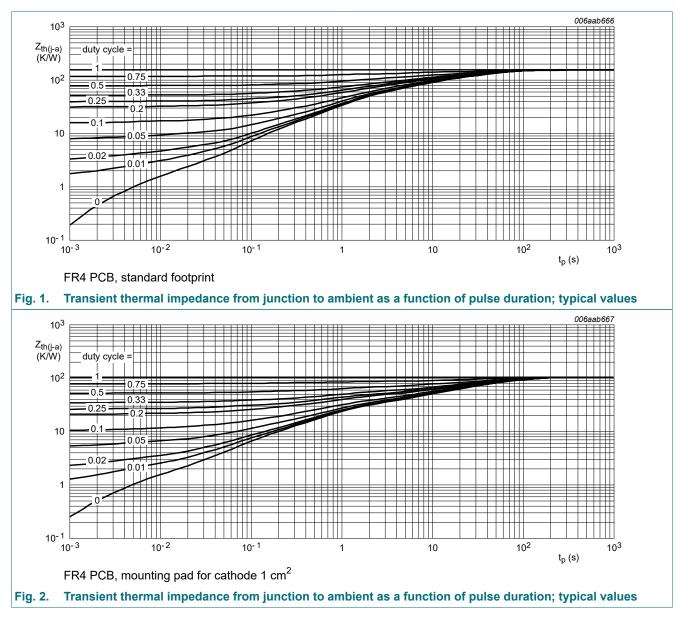
[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

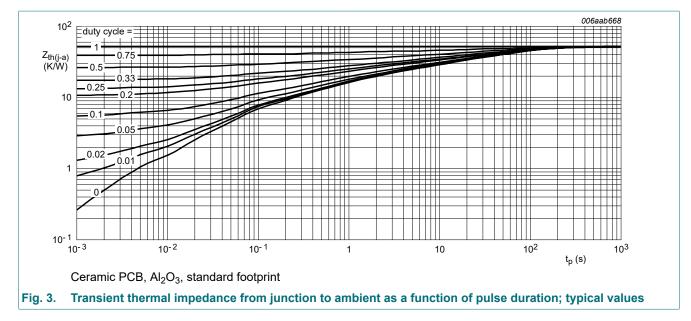
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[4] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

[5] Soldering point of cathode tab.



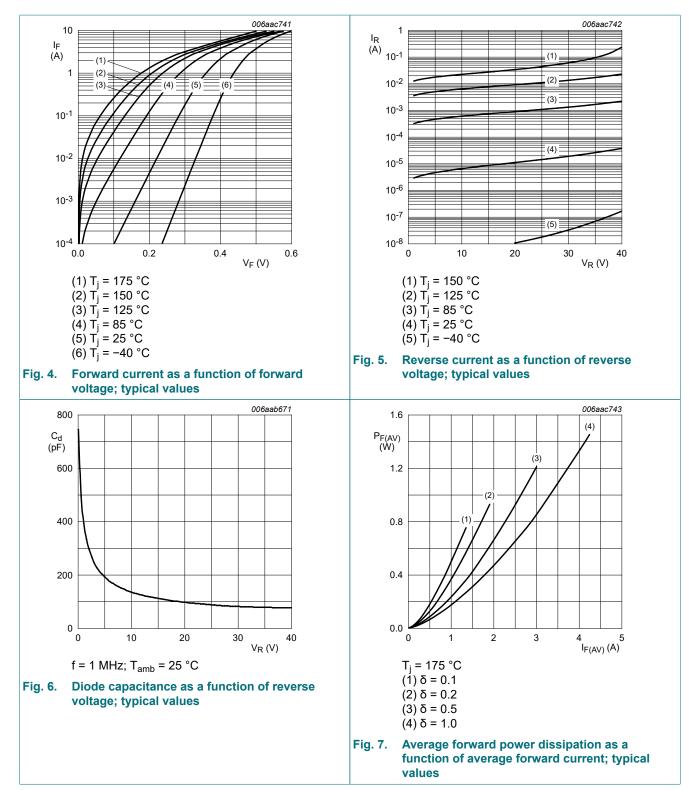
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10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _F forward v	forward voltage	I _F = 0.1 A; T _j = 25 °C	-	285	320	mV
		I _F = 1 A; T _j = 25 °C	-	360	420	mV
		I _F = 3 A; T _j = 25 °C	-	430	490	mV
		I _F = 3 A; T _j = 125 °C	-	330	380	mV
I _R	reverse current	V _R = 10 V; T _j = 25 °C	-	7	-	μA
		V _R = 40 V; T _j = 25 °C	-	35	200	μA
		V _R = 10 V; T _j = 125 °C	-	6	-	mA
		V _R = 40 V; T _j = 125 °C	-	23	-	mA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C	-	350	-	pF
		V _R = 10 V; f = 1 MHz; T _i = 25 °C	-	140	-	pF

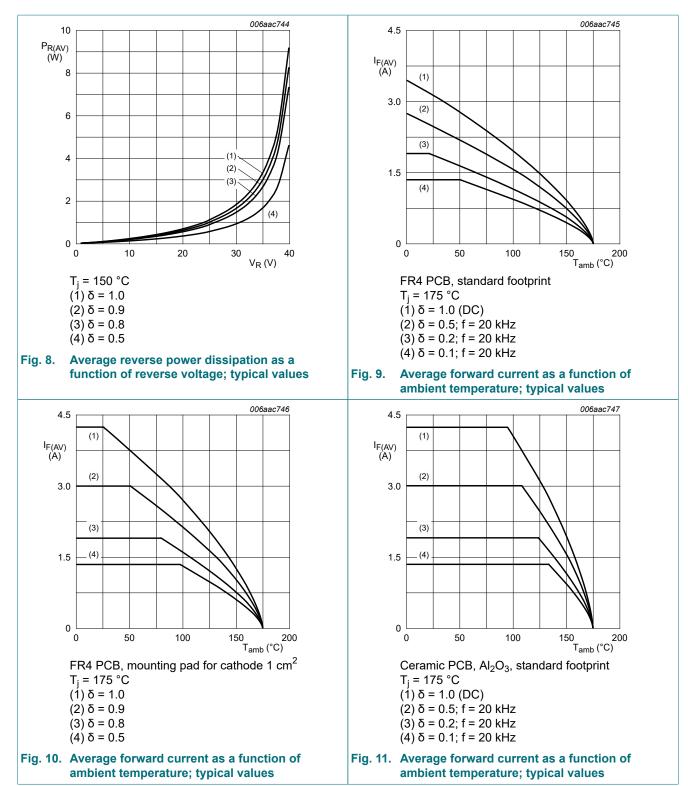
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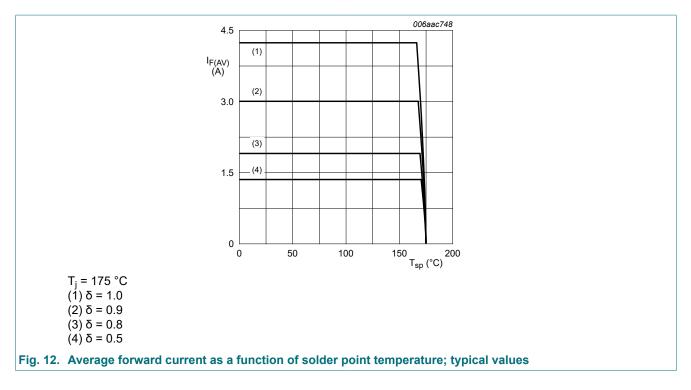
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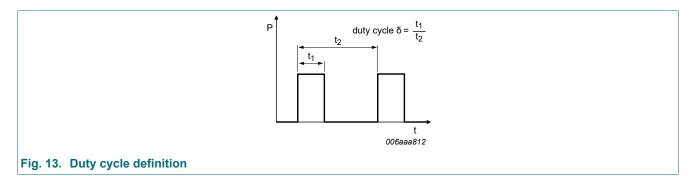


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11. Test information



The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current

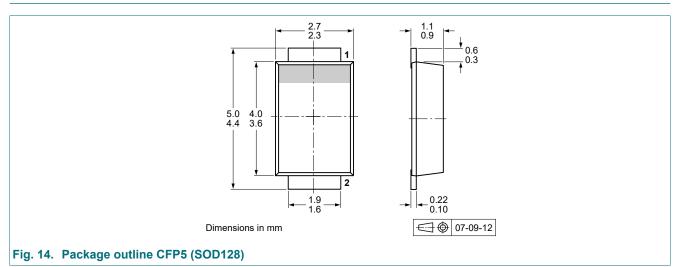
I_{RMS}=I_{F(AV)} at DC

 $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current

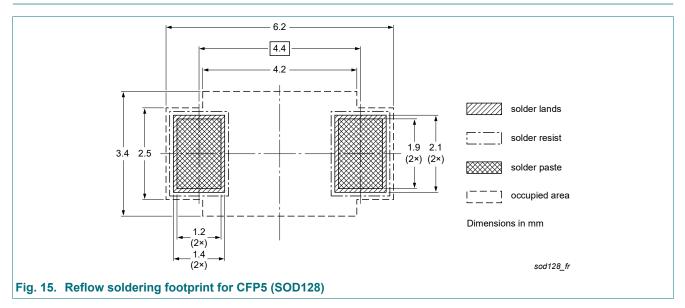
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

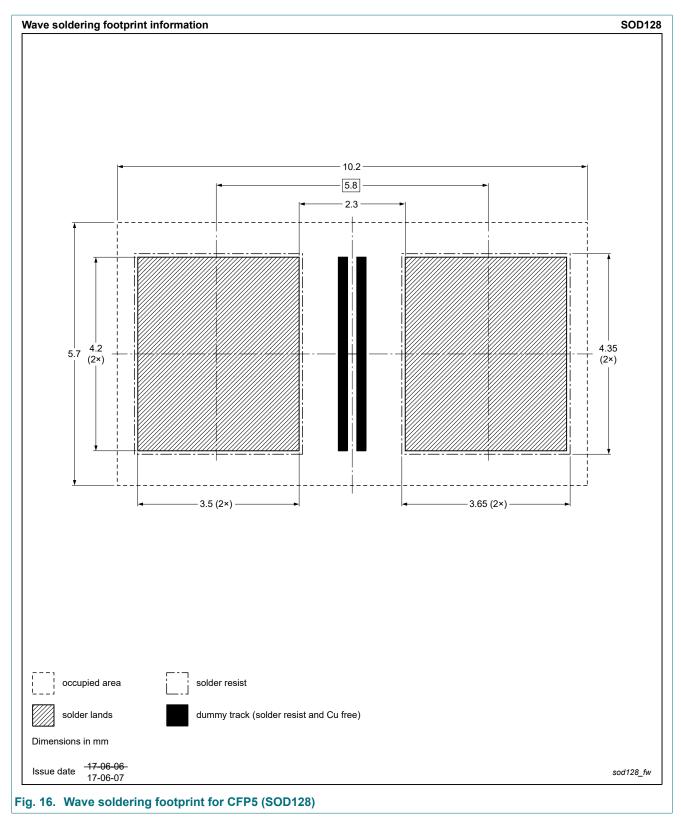
12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMEG4030ETP-Q v.2	20230220	Product data sheet	-	PMEG4030ETP-Q v.1		
Modifications:	Ū.	 Limiting values: Measurement conditions for I_{FSM} changed from square wave to half-sine wave. 				
PMEG4030ETP-Q v.1	20210719	Product data sheet	-	-		

PMEG4030ETP-Q

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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