



PMEG3030CEP

30 V, 3 A low VF Schottky barrier rectifier

10 March 2025

Product data sheet

1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD128 small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: $I_{F(AV)} \leq 3$ A
- Reverse voltage: $V_R \leq 30$ V
- Low forward voltage
- High power capability due to clip-bond technology
- Small and flat lead SMD plastic package
- Suitable for both reflow and wave soldering

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- Low power consumption applications

4. Quick reference data

Table 1. Quick reference data

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|-------------|-------------------------|--|-----|-----|-----|-----|---------|
| $I_{F(AV)}$ | average forward current | $\delta = 0.5$; $f = 20$ kHz; square wave; $T_{sp} \leq 163$ °C | | - | - | 3 | A |
| V_R | reverse voltage | $T_j = 25$ °C | | - | - | 30 | V |
| V_F | forward voltage | $I_F = 3$ A; pulsed; $T_j = 25$ °C | [1] | - | 430 | 500 | mV |
| I_R | reverse current | $V_R = 30$ V; pulsed; $T_j = 25$ °C | [1] | - | 35 | 100 | μ A |

[1] Very short pulse, in order to maintain a stable junction temperature.

5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-------------|--------------------|--------------------|
| 1 | K | cathode[1] | CFP5 (SOD128) | K A sym001 |
| 2 | A | anode | | |

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

| Type number | Package | | |
|-------------|---------|--|---------|
| | Name | Description | Version |
| PMEG3030CEP | CFP5 | plastic, surface mounted package; 2 terminals; 4 mm pitch; 3.8 mm x 2.6 mm x 1 mm body | SOD128 |

7. Marking

Table 4. Marking codes

| Type number | Marking code |
|-------------|--------------|
| PMEG3030CEP | GC |

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|--------------------|-------------------------------------|---|-----|-----|------|------|
| V _R | reverse voltage | T _j = 25 °C | | - | 30 | V |
| I _{F(AV)} | average forward current | δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 163 °C | | - | 3 | A |
| I _{FSM} | non-repetitive peak forward current | t _p = 8.3 ms; half-sine wave; T _{j(init)} = 25 °C | | - | 50 | A |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C | [1] | - | 750 | mW |
| | | | [2] | - | 1.25 | W |
| T _j | junction temperature | | | - | 175 | °C |
| T _{amb} | ambient temperature | | | -55 | 175 | °C |
| T _{stg} | storage temperature | | | -65 | 175 | °C |

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|----------------|--|-------------|---------|-----|-----|-----|------|
| $R_{th(j-a)}$ | thermal resistance from junction to ambient | in free air | [1] [2] | - | - | 200 | K/W |
| | | | [3] [2] | - | - | 120 | K/W |
| $R_{th(j-sp)}$ | thermal resistance from junction to solder point | | [4] | - | - | 12 | K/W |

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [4] Soldering point of cathode tab.

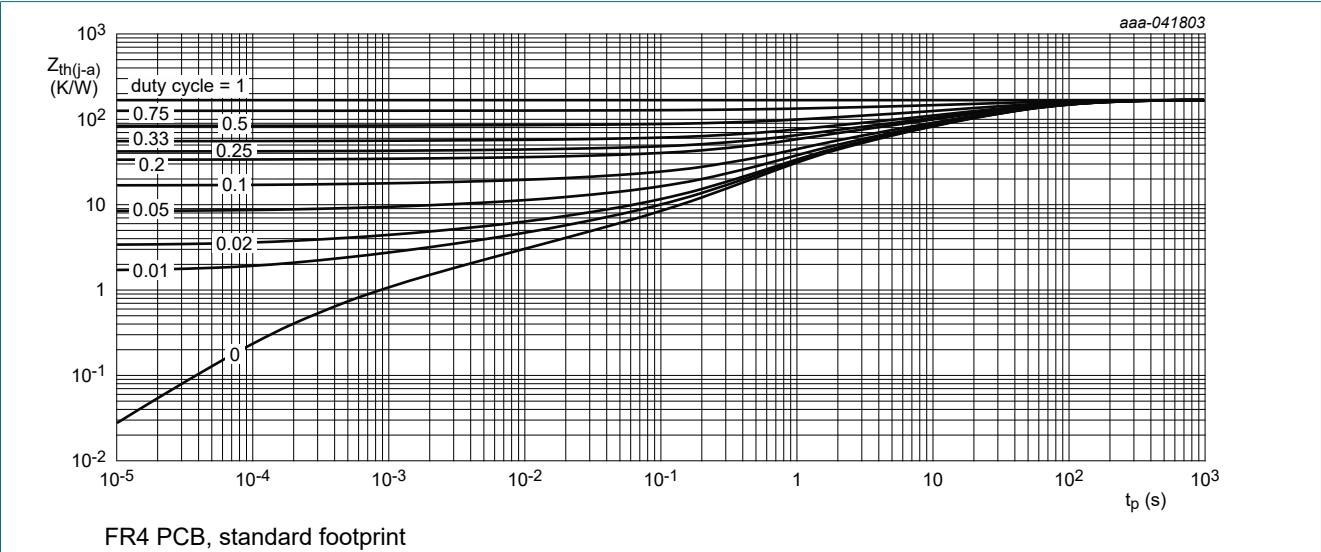


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

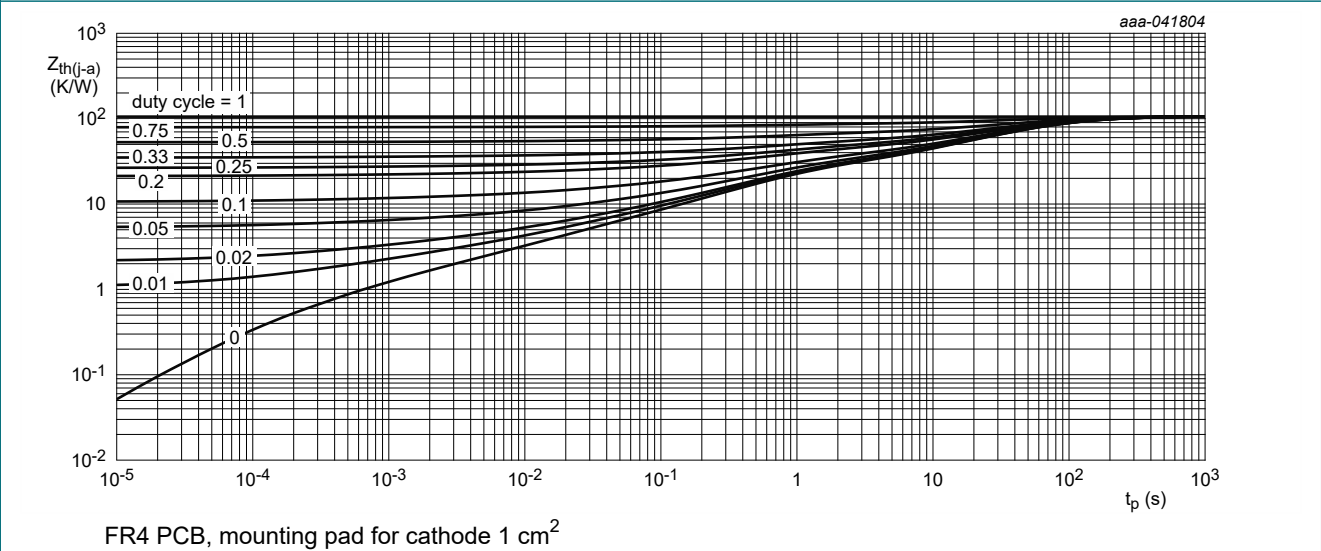


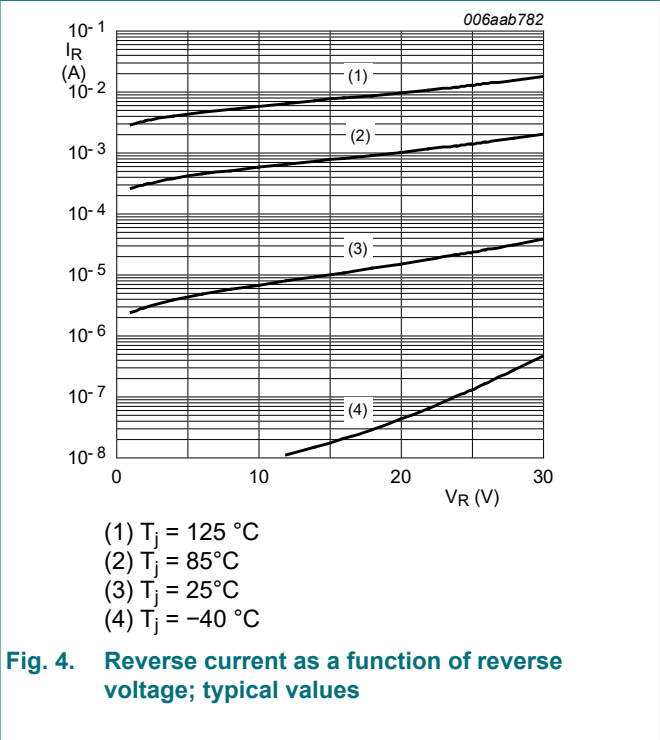
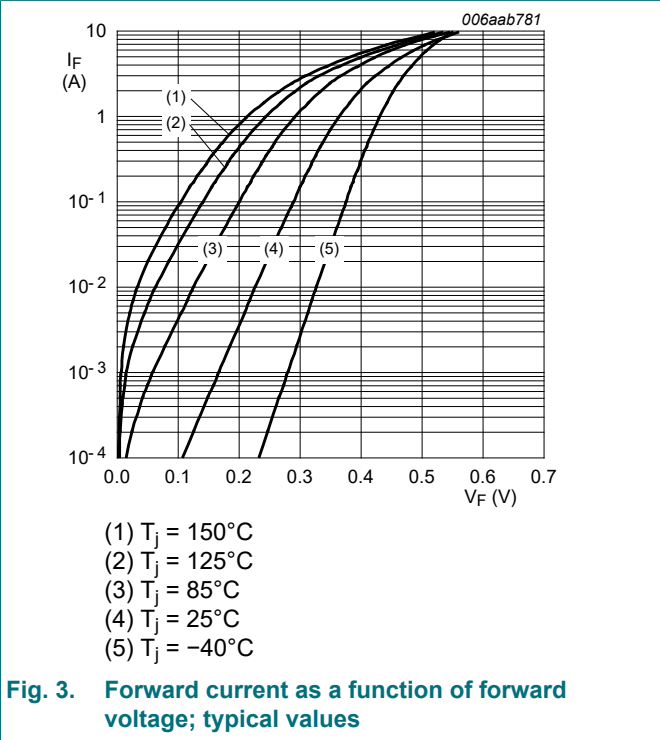
Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|-----------------|--|--|-----|-----|-----|-----|------|
| V _F | forward voltage | I _F = 1 A; pulsed; T _j = 25 °C | [1] | - | 365 | 420 | mV |
| | | I _F = 3 A; pulsed; T _j = 25 °C | [1] | - | 430 | 500 | mV |
| | | I _F = 3 A; pulsed; T _j = -40 °C | [1] | - | 490 | - | mV |
| | | I _F = 3 A; pulsed; T _j = 125 °C | [1] | - | 340 | - | mV |
| I _R | reverse current | V _R = 10 V; pulsed; T _j = 25 °C | [1] | - | 6 | - | μA |
| | | V _R = 30 V; pulsed; T _j = 25 °C | [1] | - | 35 | 100 | μA |
| C _d | diode capacitance | V _R = 1 V; f = 1 MHz; T _j = 25 °C | | - | 340 | - | pF |
| | | V _R = 10 V; f = 1 MHz; T _j = 25 °C | | - | 120 | - | pF |
| t _{rr} | reverse recovery time ramp recovery | dl _F /dt = 200 A/μs; I _F = 6 A; V _R = 26 V; T _j = 25 °C | | - | 9.7 | - | ns |
| I _{RM} | peak reverse recovery current | | | - | 0.9 | - | A |
| Q _{rr} | reverse recovery charge | | | - | 5 | - | nC |

[1] Very short pulse, in order to maintain a stable junction temperature.



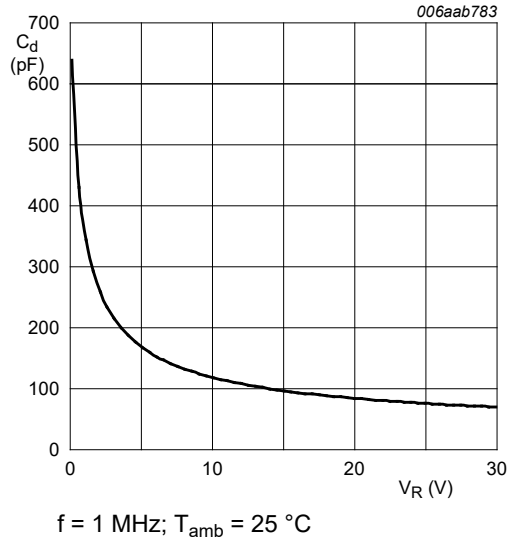


Fig. 5. Diode capacitance as a function of reverse voltage; typical values

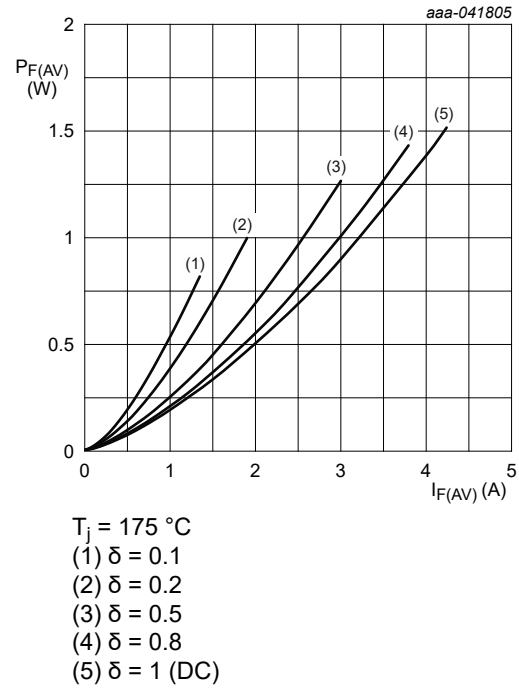


Fig. 6. Average forward power dissipation as a function of average forward current; typical values

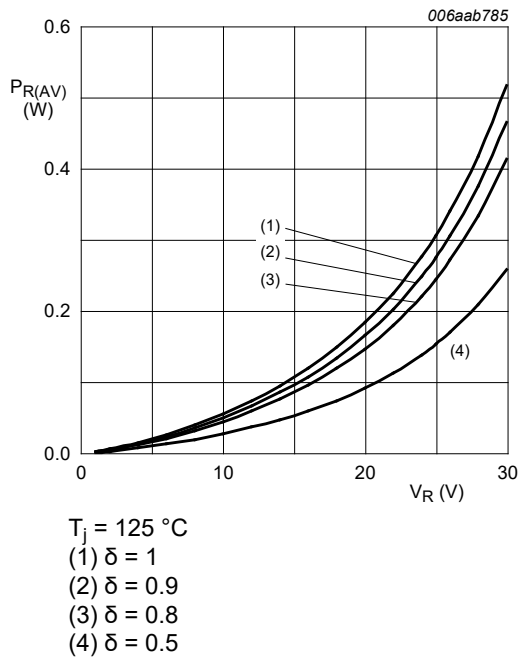


Fig. 7. Average reverse power dissipation as a function of reverse voltage; typical values

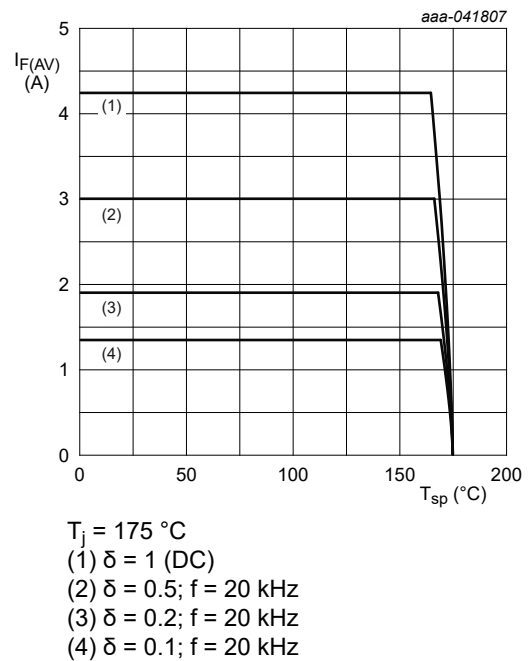


Fig. 8. Average forward current as a function of solder point temperature; typical values

11. Test information

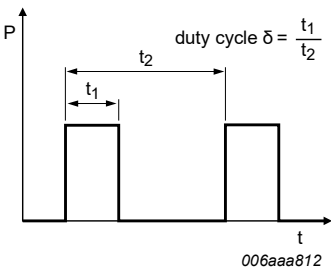


Fig. 9. Duty cycle definition

The current ratings for the typical waveforms are calculated according to the equations:

$I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current

$I_{RMS} = I_{F(AV)}$ at DC

$I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current

12. Package outline

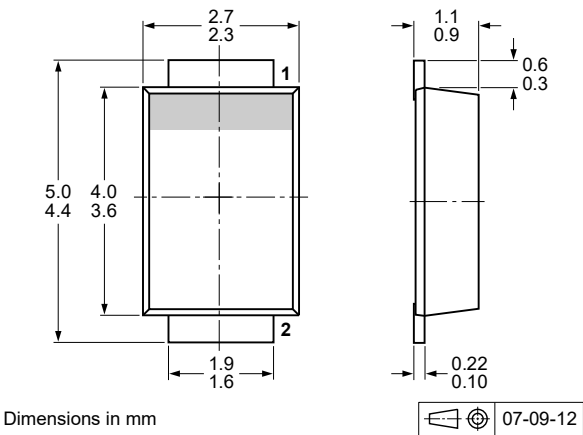


Fig. 10. Package outline CFP5 (SOD128)

13. Soldering

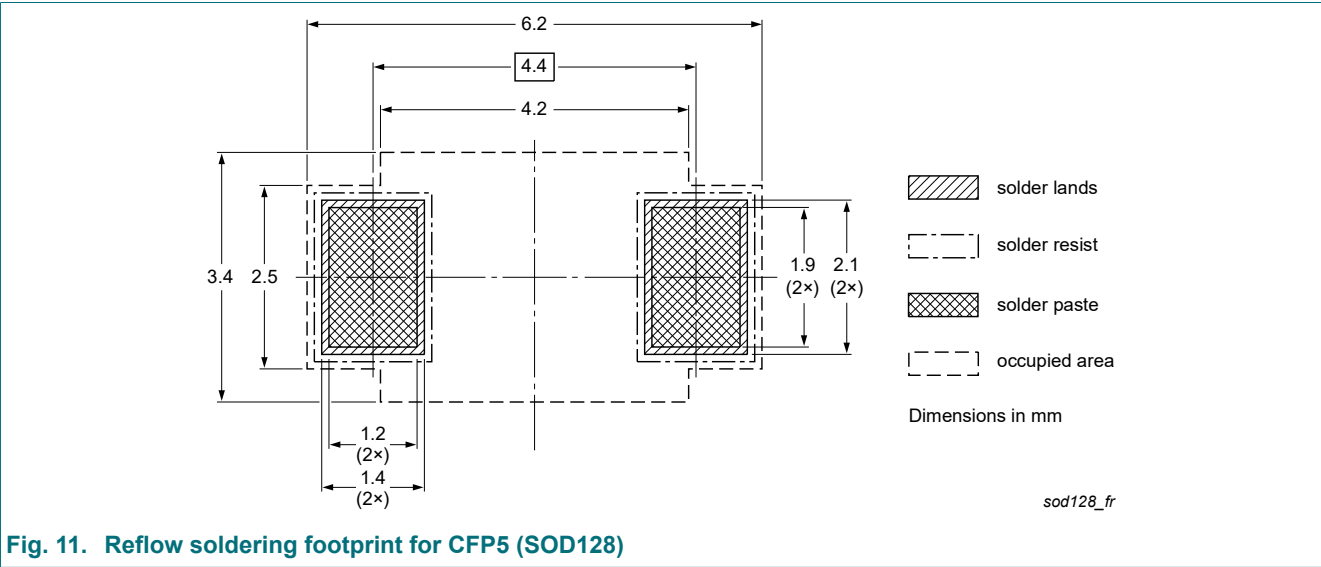


Fig. 11. Reflow soldering footprint for CFP5 (SOD128)

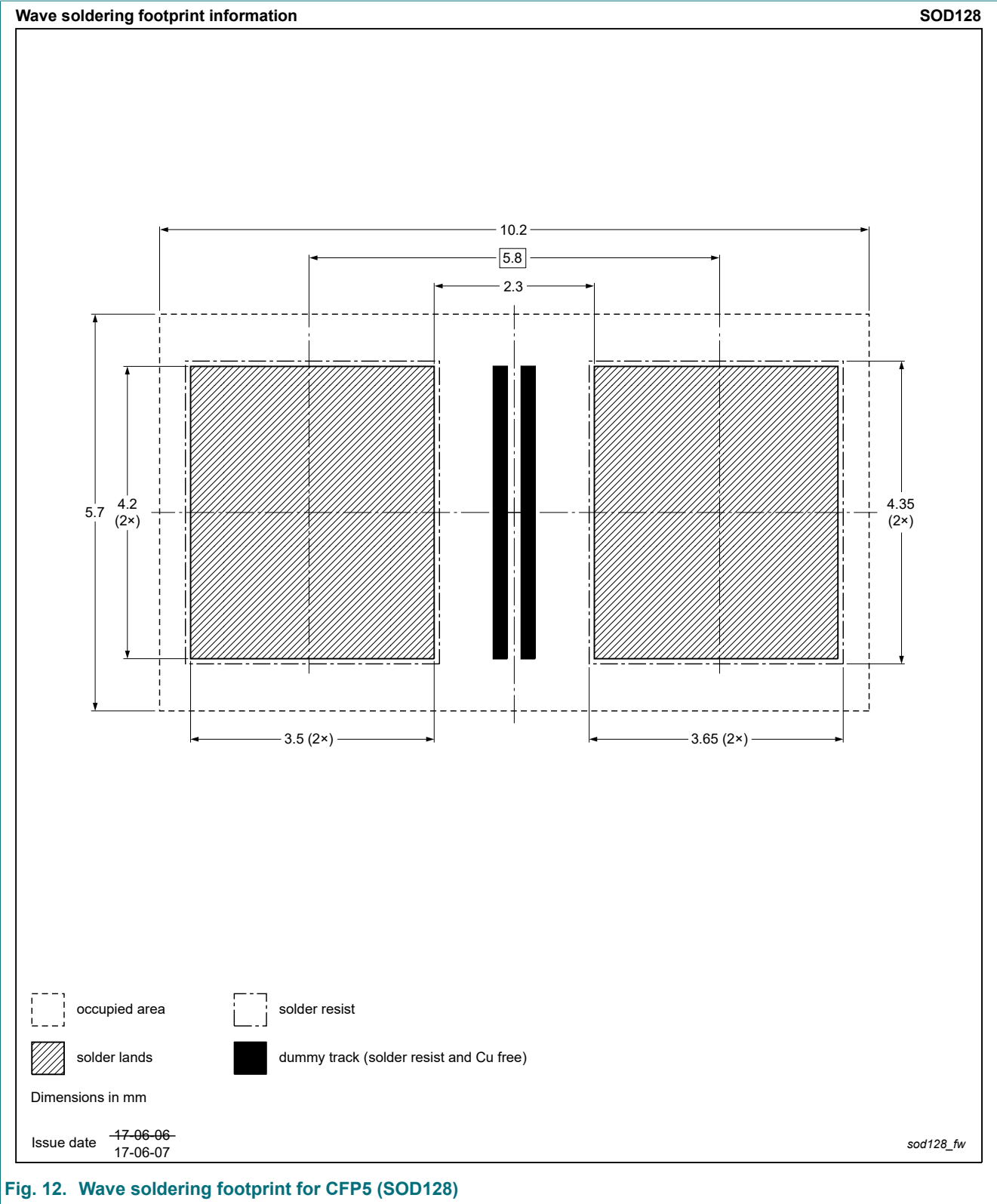


Fig. 12. Wave soldering footprint for CFP5 (SOD128)

14. Revision history

Table 8. Revision history

| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes |
|-----------------|--------------|--------------------|---------------|------------|
| PMEG3030CEP v.1 | 20250310 | Product data sheet | - | - |

15. Legal information

Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------|--------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

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