1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD128 small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: $I_{F(AV)} \leq 1 \text{ A}$
- Reverse voltage: $V_R \leq 30 \text{ V}$
- Low forward voltage
- High power capability due to clip-bond technology
- AEC-Q101 qualified
- Small and flat lead SMD plastic package
- Capable for reflow and wave soldering

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- Low power consumption applications

4. Quick reference data

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$I_{F(AV)}$</td>
<td>average forward current</td>
<td>$\delta = 0.5; f = 20 \text{ kHz}; T_{amb} \leq 120 \degree \text{ C};$ square wave</td>
<td>1</td>
<td></td>
<td></td>
<td>A</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$\delta = 0.5; f = 20 \text{ kHz}; T_{sp} \leq 140 \degree \text{ C};$ square wave</td>
<td></td>
<td></td>
<td></td>
<td>A</td>
</tr>
<tr>
<td>$V_R$</td>
<td>reverse voltage</td>
<td>$T_j = 25 \degree \text{ C}$</td>
<td></td>
<td></td>
<td>30</td>
<td>V</td>
</tr>
<tr>
<td>$V_F$</td>
<td>forward voltage</td>
<td>$I_F = 1 \text{ A}; T_j = 25 \degree \text{ C}$</td>
<td></td>
<td>405</td>
<td>450</td>
<td>mV</td>
</tr>
<tr>
<td>$I_R$</td>
<td>reverse current</td>
<td>$V_R = 30 \text{ V}; T_j = 25 \degree \text{ C}$</td>
<td></td>
<td>15</td>
<td>50</td>
<td>$\mu\text{A}$</td>
</tr>
</tbody>
</table>

[1] Device mounted on a ceramic PCB, $\text{Al}_2\text{O}_3$, standard footprint.
5. Pinning information

Table 2. Pinning information

<table>
<thead>
<tr>
<th>Pin</th>
<th>Symbol</th>
<th>Description</th>
<th>Simplified outline</th>
<th>Graphic symbol</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>K</td>
<td>cathode[1]</td>
<td>![CFP5 (SOD128)]</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>A</td>
<td>anode</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

<table>
<thead>
<tr>
<th>Type number</th>
<th>Package Name</th>
<th>Description</th>
<th>Version</th>
</tr>
</thead>
<tbody>
<tr>
<td>PMEG3010BEP</td>
<td>CFP5</td>
<td>plastic, surface mounted package; 2 terminals; 4 mm pitch; 3.8 mm x 2.6 mm x 1 mm body</td>
<td>SOD128</td>
</tr>
</tbody>
</table>

7. Marking

Table 4. Marking codes

<table>
<thead>
<tr>
<th>Type number</th>
<th>Marking code</th>
</tr>
</thead>
<tbody>
<tr>
<td>PMEG3010BEP</td>
<td>A2</td>
</tr>
</tbody>
</table>
8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>(V_R)</td>
<td>reverse voltage</td>
<td>(T_j = 25 , ^\circ C)</td>
<td>-</td>
<td>30</td>
<td>V</td>
</tr>
<tr>
<td>(I_{F(AV)})</td>
<td>average forward current</td>
<td>(\delta = 0.5; f = 20 , kHz; , T_{amb} \leq 120 , ^\circ C; ) square wave</td>
<td>[1]</td>
<td>-</td>
<td>1</td>
</tr>
<tr>
<td>(I_{FSM})</td>
<td>non-repetitive peak forward current</td>
<td>(t_p = 8 , ms; ) square wave; (T_{j(init)} = 25 , ^\circ C)</td>
<td>-</td>
<td>50</td>
<td>A</td>
</tr>
<tr>
<td>(P_{tot})</td>
<td>total power dissipation</td>
<td>(T_{amb} \leq 25 , ^\circ C)</td>
<td>[2]</td>
<td>-</td>
<td>625</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[3]</td>
<td>-</td>
<td>1.05</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[1]</td>
<td>-</td>
<td>2.1</td>
</tr>
<tr>
<td>(T_j)</td>
<td>junction temperature</td>
<td></td>
<td>-</td>
<td>150</td>
<td>(^\circ C)</td>
</tr>
<tr>
<td>(T_{amb})</td>
<td>ambient temperature</td>
<td></td>
<td>-55</td>
<td>150</td>
<td>(^\circ C)</td>
</tr>
<tr>
<td>(T_{stg})</td>
<td>storage temperature</td>
<td></td>
<td>-65</td>
<td>150</td>
<td>(^\circ C)</td>
</tr>
</tbody>
</table>

[1] Device mounted on a ceramic PCB, \(\text{Al}_2\text{O}_3\), standard footprint.
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm\(^2\).

9. Thermal characteristics

Table 6. Thermal characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>(R_{th(j-a)})</td>
<td>thermal resistance from junction to ambient</td>
<td>in free air</td>
<td>[1] [2]</td>
<td>-</td>
<td>-</td>
<td>200</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[3] [2]</td>
<td>-</td>
<td>-</td>
<td>120</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[4] [2]</td>
<td>-</td>
<td>-</td>
<td>60</td>
</tr>
<tr>
<td>(R_{th(j-sp)})</td>
<td>thermal resistance from junction to solder point</td>
<td></td>
<td>[5]</td>
<td>-</td>
<td>-</td>
<td>12</td>
</tr>
</tbody>
</table>

[2] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses \(P_R\) are a significant part of the total power losses.
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm\(^2\).
[4] Device mounted on a ceramic PCB, \(\text{Al}_2\text{O}_3\), standard footprint.
Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

FR4 PCB, standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

FR4 PCB, mounting pad for cathode 1 cm²
## 10. Characteristics

### Table 7. Characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_F$</td>
<td>forward voltage</td>
<td>$I_F = 0.1\ A; \ T_J = 25\ ^\circ C$</td>
<td>-</td>
<td>315</td>
<td>360</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_F = 0.7\ A; \ T_J = 25\ ^\circ C$</td>
<td>-</td>
<td>390</td>
<td>430</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_F = 1\ A; \ T_J = 25\ ^\circ C$</td>
<td>-</td>
<td>405</td>
<td>450</td>
<td>mV</td>
</tr>
<tr>
<td>$I_R$</td>
<td>reverse current</td>
<td>$V_R = 5\ V; \ T_J = 25\ ^\circ C$</td>
<td>-</td>
<td>2</td>
<td>-</td>
<td>µA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_R = 10\ V; \ T_J = 25\ ^\circ C$</td>
<td>-</td>
<td>3</td>
<td>-</td>
<td>µA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_R = 30\ V; \ T_J = 25\ ^\circ C$</td>
<td>-</td>
<td>15</td>
<td>50</td>
<td>µA</td>
</tr>
<tr>
<td>$C_d$</td>
<td>diode capacitance</td>
<td>$V_R = 1\ V; \ f = 1\ MHz; \ T_J = 25\ ^\circ C$</td>
<td>-</td>
<td>170</td>
<td>-</td>
<td>pF</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_R = 10\ V; \ f = 1\ MHz; \ T_J = 25\ ^\circ C$</td>
<td>-</td>
<td>60</td>
<td>-</td>
<td>pF</td>
</tr>
</tbody>
</table>

Ceramic PCB, Al₂O₃, standard footprint

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values
Fig. 4. Forward current as a function of forward voltage; typical values

(1) $T_J = 150 \, ^\circ\text{C}$
(2) $T_J = 125 \, ^\circ\text{C}$
(3) $T_J = 85 \, ^\circ\text{C}$
(4) $T_J = 25 \, ^\circ\text{C}$
(5) $T_J = -40 \, ^\circ\text{C}$

Fig. 5. Reverse current as a function of reverse voltage; typical values

(1) $T_J = 125 \, ^\circ\text{C}$
(2) $T_J = 85 \, ^\circ\text{C}$
(3) $T_J = 25 \, ^\circ\text{C}$
(4) $T_J = -40 \, ^\circ\text{C}$

Fig. 6. Diode capacitance as a function of reverse voltage; typical values

$f = 1 \, \text{MHz}; \ T_{\text{amb}} = 25 \, ^\circ\text{C}$

Fig. 7. Average forward power dissipation as a function of average forward current; typical values

$P_{F(\text{AV})}$ (W)

(1) $\delta = 0.1$
(2) $\delta = 0.2$
(3) $\delta = 0.5$
(4) $\delta = 1$

$T_J = 150 \, ^\circ\text{C}$
A low Vf MEGA Schottky barrier rectifier

**Fig. 8.** Average reverse power dissipation as a function of reverse voltage; typical values

- \( T_j = 125 \, ^\circ C \)
  - (1) \( \delta = 1 \)
  - (2) \( \delta = 0.9 \)
  - (3) \( \delta = 0.8 \)
  - (4) \( \delta = 0.5 \)

**FR4 PCB, standard footprint**
- \( T_j = 150 \, ^\circ C \)
  - (1) \( \delta = 1 \); DC
  - (2) \( \delta = 0.5 \); \( f = 20 \, kHz \)
  - (3) \( \delta = 0.2 \); \( f = 20 \, kHz \)
  - (4) \( \delta = 0.1 \); \( f = 20 \, kHz \)

**Fig. 9.** Average forward current as a function of ambient temperature; typical values

- \( I_F(AV) \) (A)
- \( T_{amb} \) (°C)

**FR4 PCB, mounting pad for cathode 1 cm\(^2\)**
- \( T_j = 150 \, ^\circ C \)
  - (1) \( \delta = 1 \); DC
  - (2) \( \delta = 0.5 \); \( f = 20 \, kHz \)
  - (3) \( \delta = 0.2 \); \( f = 20 \, kHz \)
  - (4) \( \delta = 0.1 \); \( f = 20 \, kHz \)

**Fig. 10.** Average forward current as a function of ambient temperature; typical values

**Ceramic PCB, Al\(_2\)O\(_3\), standard footprint**
- \( T_j = 150 \, ^\circ C \)
  - (1) \( \delta = 1 \); DC
  - (2) \( \delta = 0.5 \); \( f = 20 \, kHz \)
  - (3) \( \delta = 0.2 \); \( f = 20 \, kHz \)
  - (4) \( \delta = 0.1 \); \( f = 20 \, kHz \)

**Fig. 11.** Average forward current as a function of ambient temperature; typical values
11. Test information

The current ratings for the typical waveforms are calculated according to the equations:

\[ I_{F(AV)} = I_M \times \delta \] with \( I_M \) defined as peak current, \( I_{RMS} = I_{F(AV)} \) at DC, and \( I_{RMS} = I_M \times \sqrt{\delta} \) with \( I_{RMS} \) defined as RMS current.

**Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.
12. Package outline

Fig. 14. Package outline CFP5 (SOD128)

13. Soldering

Fig. 15. Reflow soldering footprint for CFP5 (SOD128)
Wave soldering footprint information

Fig. 16. Wave soldering footprint for CFP5 (SOD128)
# 14. Revision history

Table 8. Revision history

<table>
<thead>
<tr>
<th>Data sheet ID</th>
<th>Release date</th>
<th>Data sheet status</th>
<th>Change notice</th>
<th>Supersedes</th>
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</thead>
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<tr>
<td>PMEG3010BEP v.2</td>
<td>20180328</td>
<td>Product data sheet</td>
<td>-</td>
<td>PMEG3010BEP_1</td>
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</tbody>
</table>

**Modifications:**
- Features and benefits: Capable for reflow and wave soldering added
- Soldering: Wave soldering footprint added

<table>
<thead>
<tr>
<th>Data sheet ID</th>
<th>Release date</th>
<th>Data sheet status</th>
<th>Change notice</th>
<th>Supersedes</th>
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<tbody>
<tr>
<td>PMEG3010BEP_1</td>
<td>20090420</td>
<td>Product data sheet</td>
<td>-</td>
<td>-</td>
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15. Legal information

Data sheet status

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<thead>
<tr>
<th></th>
<th></th>
<th></th>
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</thead>
<tbody>
<tr>
<td>Objective [short] data sheet</td>
<td>Development</td>
<td>This document contains data from the objective specification for product development.</td>
</tr>
<tr>
<td>Preliminary [short] data sheet</td>
<td>Qualification</td>
<td>This document contains data from the preliminary specification.</td>
</tr>
<tr>
<td>Product [short] data sheet</td>
<td>Production</td>
<td>This document contains the product specification.</td>
</tr>
</tbody>
</table>

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term "short data sheet" is explained in section "Definitions".
[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on Nexperia’s website.

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