



PMEG2010ET-Q

20 V, 1 A very low VF Schottky barrier rectifier

12 June 2023

Product data sheet

1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in small SOT23 Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Forward current: $I_F \leq 1$ A
- Reverse voltage: $V_R \leq 20$ V
- Very low forward voltage
- Small SMD plastic package
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications

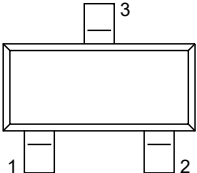
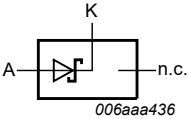
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I_F	forward current	$T_{sp} \leq 55$ °C	-	-	1	A
V_R	reverse voltage		-	-	20	V
V_F	forward voltage	$I_F = 1$ A; pulsed; $t_p \leq 300$ μ s; $\delta \leq 0.02$; $T_{amb} = 25$ °C	-	420	500	mV

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	A	anode	 SOT23	 006aaa436
2	n.c.	not connected		
3	K	cathode		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMEG2010ET-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PMEG2010ET-Q	%AU

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_R	reverse voltage			-	20	V
I_F	forward current	$T_{sp} \leq 55\text{ °C}$		-	1	A
I_{FRM}	repetitive peak forward current	$t_p \leq 1\text{ ms}$; $\delta \leq 0.25$		-	5	A
I_{FSM}	non-repetitive peak forward current	$t_p = 8\text{ }\mu\text{s}$; square wave		-	9	A
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ °C}$	[1]	-	280	mW
			[2]	-	420	mW
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-65	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	440	K/W
			[1] [3]	-	-	300	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[4]	-	-	120	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.
 [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
 [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
 [4] Soldering point of cathode tab.

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_F	forward voltage	$I_F = 0.1 \text{ mA}$; pulsed; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; $T_{amb} = 25 \text{ }^\circ\text{C}$		-	90	130	mV
		$I_F = 1 \text{ mA}$; pulsed; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; $T_{amb} = 25 \text{ }^\circ\text{C}$		-	150	190	mV
		$I_F = 10 \text{ mA}$; pulsed; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; $T_{amb} = 25 \text{ }^\circ\text{C}$		-	210	240	mV
		$I_F = 100 \text{ mA}$; pulsed; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; $T_{amb} = 25 \text{ }^\circ\text{C}$		-	280	330	mV
		$I_F = 500 \text{ mA}$; pulsed; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; $T_{amb} = 25 \text{ }^\circ\text{C}$		-	355	390	mV
		$I_F = 1 \text{ A}$; pulsed; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; $T_{amb} = 25 \text{ }^\circ\text{C}$		-	420	500	mV
I_R	reverse current	$V_R = 10 \text{ V}$; $T_{amb} = 25 \text{ }^\circ\text{C}$		-	15	40	μA
		$V_R = 20 \text{ V}$; $T_{amb} = 25 \text{ }^\circ\text{C}$		-	40	200	μA
C_d	diode capacitance	$V_R = 1 \text{ V}$; $f = 1 \text{ MHz}$; $T_{amb} = 25 \text{ }^\circ\text{C}$		-	66	80	pF

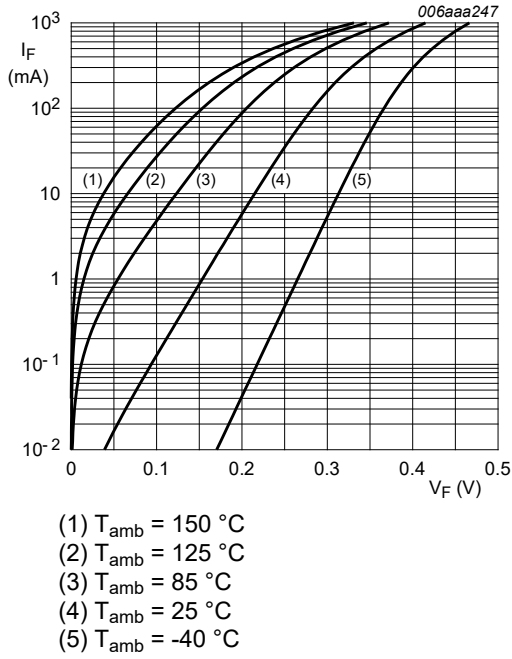


Fig. 1. Forward current as a function of forward voltage; typical values

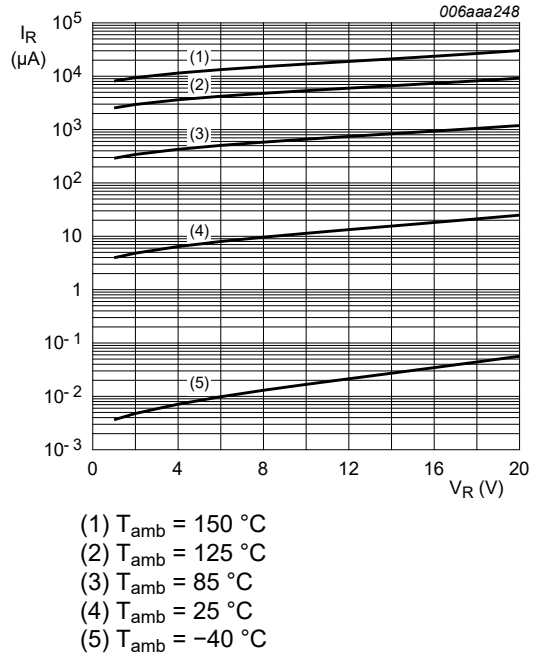


Fig. 2. Reverse current as a function of reverse voltage; typical values

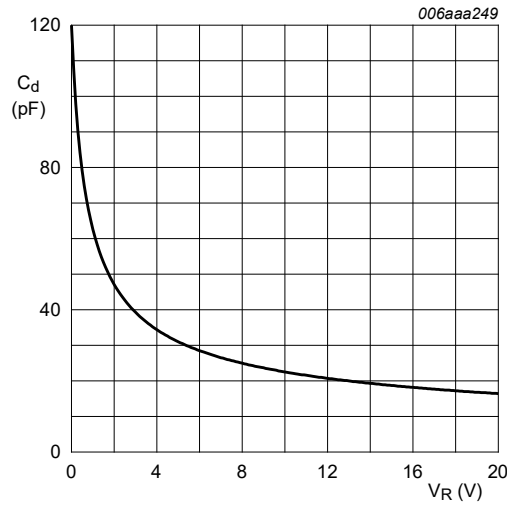


Fig. 3. Diode capacitance as a function of reverse voltage; typical values

11. Test information

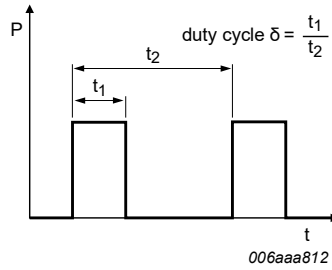


Fig. 4. Duty cycle definition

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

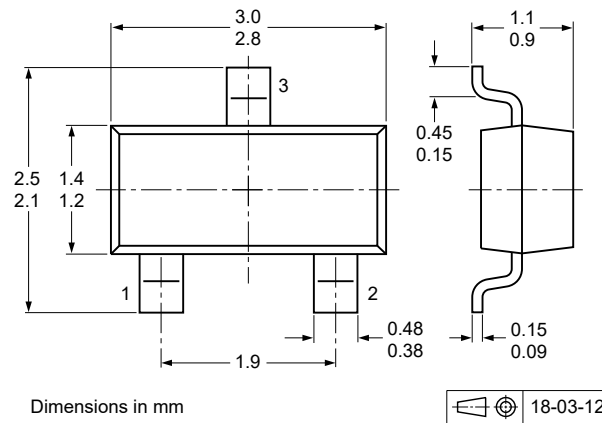


Fig. 5. Package outline SOT23

13. Soldering

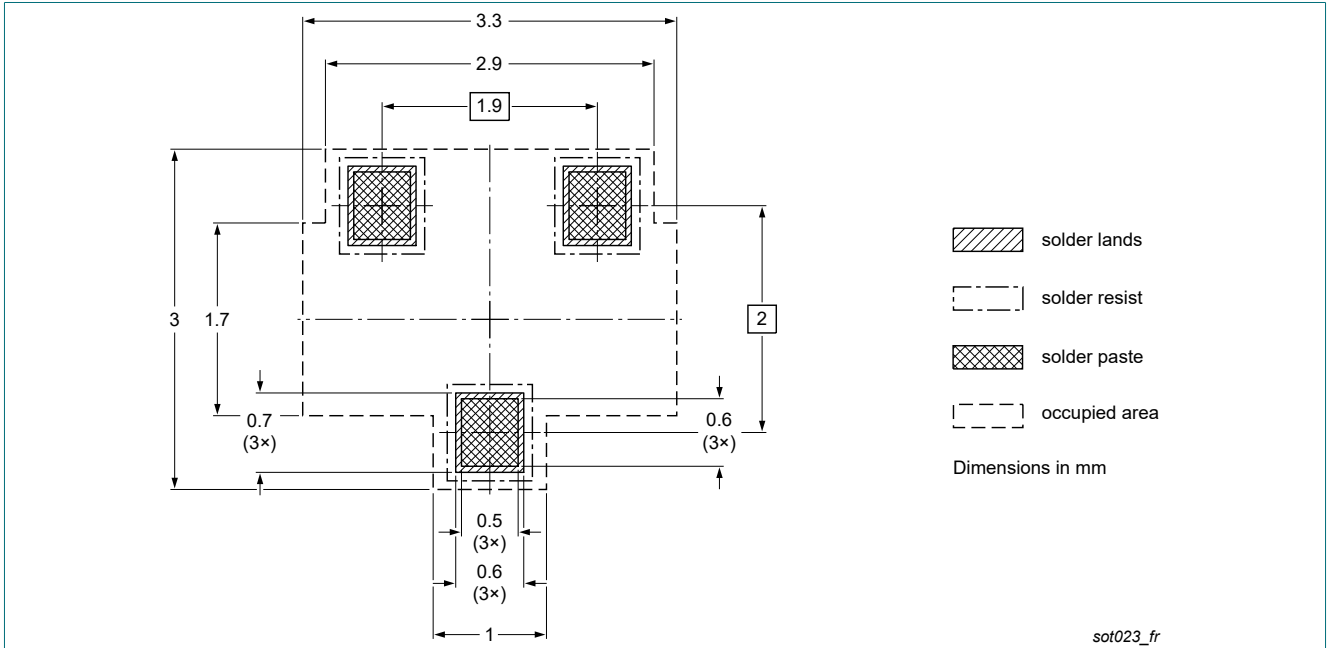


Fig. 6. Reflow soldering footprint for SOT23

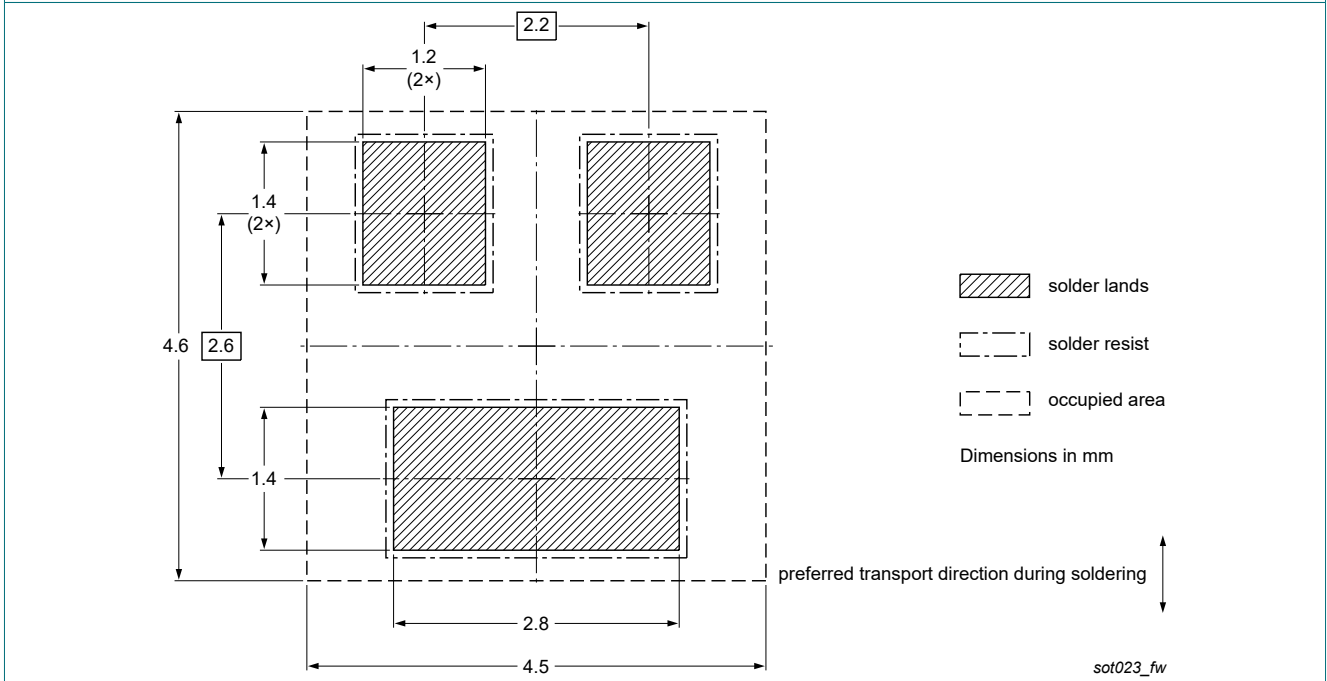


Fig. 7. Wave soldering footprint for SOT23

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG2010ET-Q v.1	20230612	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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