



# PMEG2010AEH

20 V, 1 A very low VF Schottky barrier rectifier

13 June 2023

Product data sheet

## 1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a small SOD123F Surface-Mounted Device (SMD) plastic package.

## 2. Features and benefits

- Forward current:  $I_F \leq 1$  A
- Reverse voltage:  $V_R \leq 20$  V
- Very low forward voltage
- Small SMD plastic packages
- AEC-Q101 qualified

## 3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications



## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$I_F$	forward current	$T_{sp} \leq 55$ °C	-	-	1	A
$V_R$	reverse voltage		-	-	20	V
$V_F$	forward voltage	$I_F = 1$ A; pulsed; $t_p \leq 300$ $\mu$ s; $\delta \leq 0.02$ ; $T_{amb} = 25$ °C	-	380	430	mV

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode	 SOD123F	 $K \rightarrow A$ sym001
2	A	anode		

## 6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
<a href="#">PMEG2010AEH</a>	SOD123F	plastic, surface-mounted package; 2 leads; 2.6 mm x 1.6 mm x 1.1 mm body	<a href="#">SOD123F</a>

## 7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG2010AEH	AF

## 8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_R$	reverse voltage			-	20	V
$I_F$	forward current	$T_{sp} \leq 55\text{ °C}$		-	1	A
$I_{FRM}$	repetitive peak forward current	$t_p \leq 1\text{ ms}$ ; $\delta \leq 0.25$		-	7	A
$I_{FSM}$	non-repetitive peak forward current	$t_p = 8\text{ }\mu\text{s}$ ; square wave		-	9	A
$P_{tot}$	total power dissipation	$T_{amb} \leq 25\text{ °C}$	[1]	-	375	mW
			[2]	-	830	mW
$T_j$	junction temperature			-	150	°C
$T_{amb}$	ambient temperature			-65	150	°C
$T_{stg}$	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

## 9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	330	K/W
			[1] [3]	-	-	150	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[4]	-	-	60	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses  $P_R$  are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

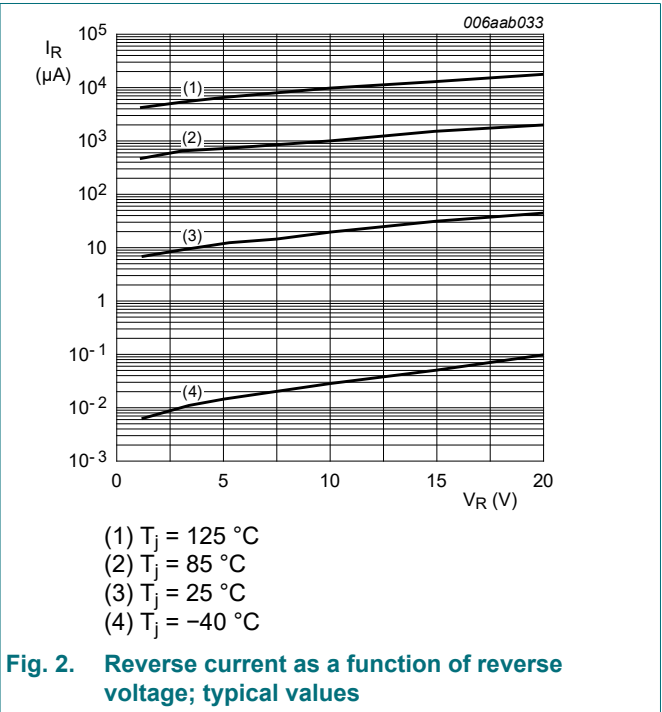
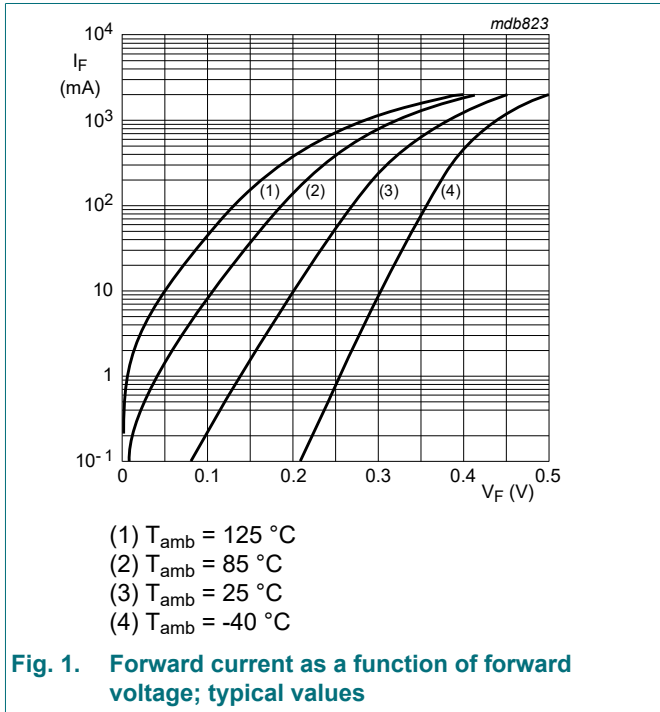
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

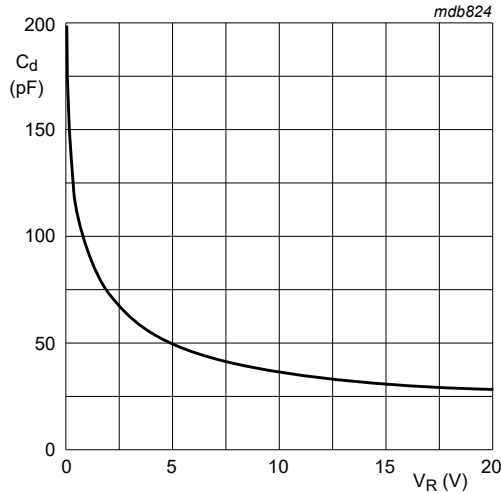
[4] Soldering point of cathode tab.

### 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 10 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	200	220	mV
		I <sub>F</sub> = 100 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	265	290	mV
		I <sub>F</sub> = 1 A; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	380	430	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 5 V; T <sub>amb</sub> = 25 °C	-	15	50	μA
		V <sub>R</sub> = 10 V; T <sub>amb</sub> = 25 °C	-	20	80	μA
		V <sub>R</sub> = 20 V; T <sub>amb</sub> = 25 °C	-	50	200	μA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 5 V; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	55	70	pF





$f = 1 \text{ MHz}; T_{\text{amb}} = 25 \text{ }^\circ\text{C}$

Fig. 3. Diode capacitance as a function of reverse voltage; typical values

### 11. Test information

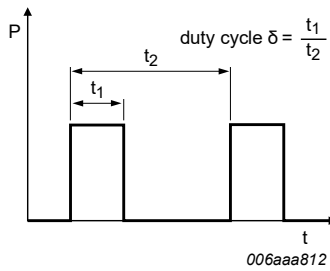


Fig. 4. Duty cycle definition

#### Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline

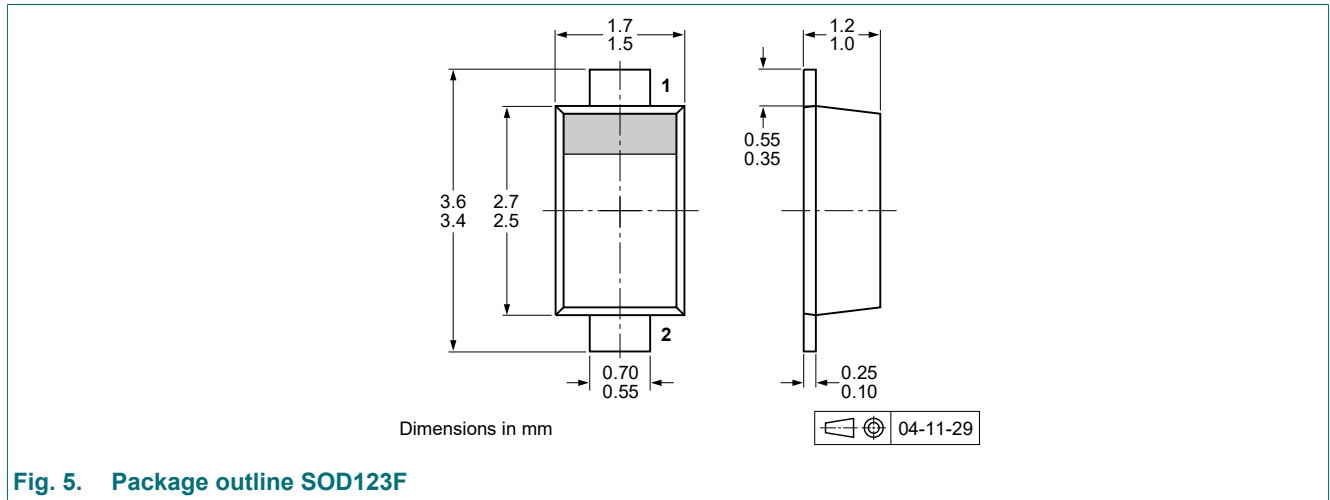


Fig. 5. Package outline SOD123F

## 13. Soldering

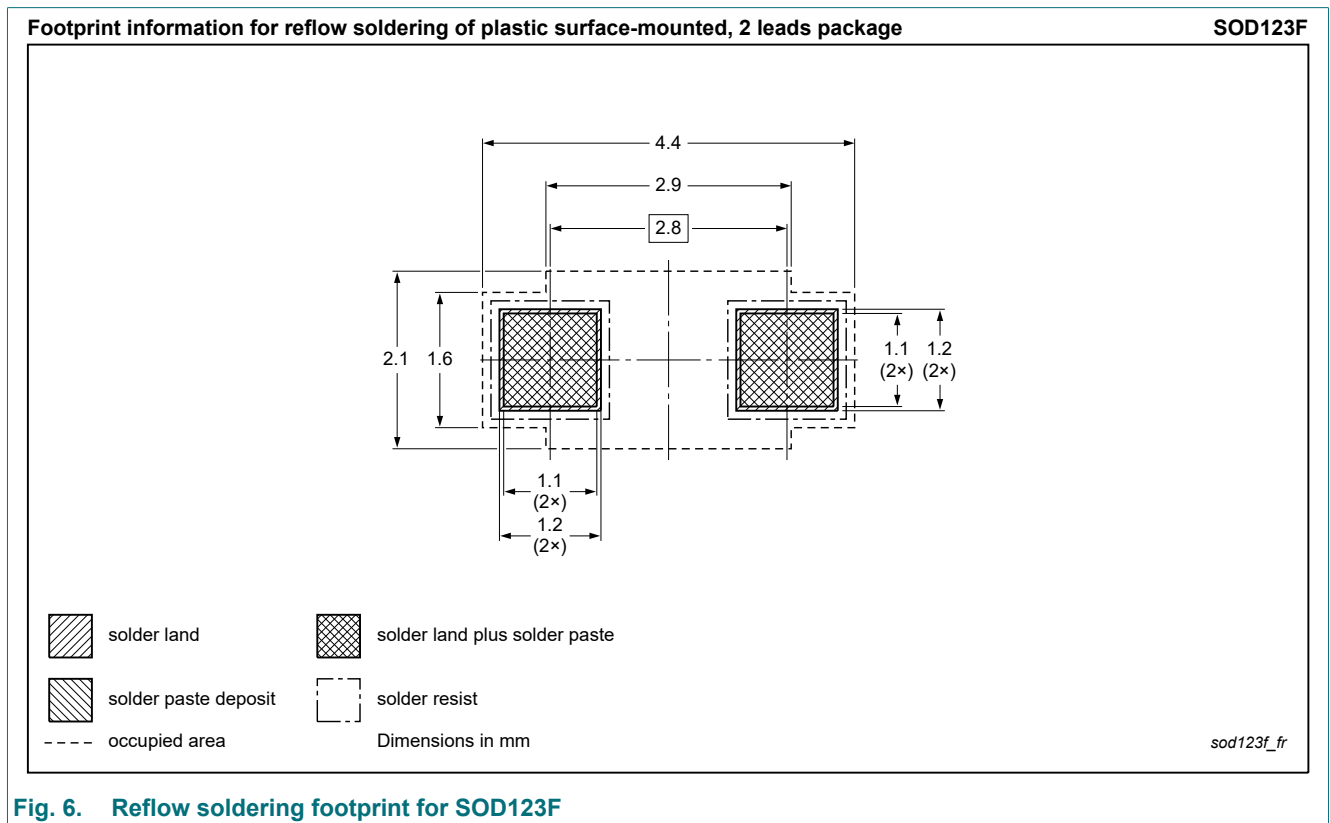


Fig. 6. Reflow soldering footprint for SOD123F

## 14. Revision history

**Table 8. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG2010AEH v.4	20230613	Product data sheet	-	PMEG2010AEH_PME G2010AET_3
Modifications:	<ul style="list-style-type: none"><li>• Family data sheet splitted to single type data sheets.</li><li>• Section "Packing information" removed.</li></ul>			
PMEG2010AEH_PME G2010AET_3	20070328	Product data sheet	-	PMEG2010AEH_2
PMEG2010AEH_2	20050526	Product data sheet	-	PMEG2010AEH_1
PMEG2010AEH_1	20050406	Product data sheet	-	-

## 15. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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