

100 V, 1 A Trench Schottky barrier rectifier

1 April 2022

Product data sheet

1. General description

Trench Schottky barrier rectifier encapsulated in a CFP2-HP (SOD323HP) power flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Low forward voltage
- Low Q_{rr} and low I_{RM}
- Low leakage current
- High power capability due to clip-bonding technology
- · Power flat lead plastic package with exposed heatsink for optimal thermal connection

3. Applications

- High efficiency DC-to-DC conversion
- LED lighting
- Switch mode power supply
- Freewheeling applications
- Reverse polarity protection
- OR-ing

4. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 170 °C		-	-	1	A
V _R	reverse voltage	T _j = 25 °C		-	-	100	V
V _F	forward voltage	I _F = 1 A; pulsed; T _j = 25 °C	[1]	-	720	795	mV
I _R	reverse current	V _R = 100 V; pulsed; T _j = 25 °C	[1]	-	0.1	0.5	μA
		V _R = 100 V; pulsed; T _j = 125 °C	[1]	-	0.15	0.6	mA

[1] Very short pulse, in order to maintain a stable junction temperature.

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5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode		
2	A	anode	1](2	К 🖌 А
			Transparent top view CFP2-HP (SOD323HP)	sym001

6. Ordering information

Table 3. Ordering information							
Type number	Package						
	Name	Description	Version				
PMEG100T10ELXD	CFP2-HP	SOD323HP: plastic surface-mounted package with solderable lead ends; 2.2 mm x 1.3 mm x 0.68 mm body	SOD323HP				

7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG100T10ELXD	2N

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	100	V
l _F	forward current	δ = 1; T _{sp} ≤ 168 °C		-	1.4	А
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 170 °C		-	1	A
I _{FSM}	non-repetitive peak forward current	t_p = 8.3 ms; half sine wave; $T_{j(init)}$ = 25 °C		-	27	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.65	W
			[2]	-	1.2	W
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

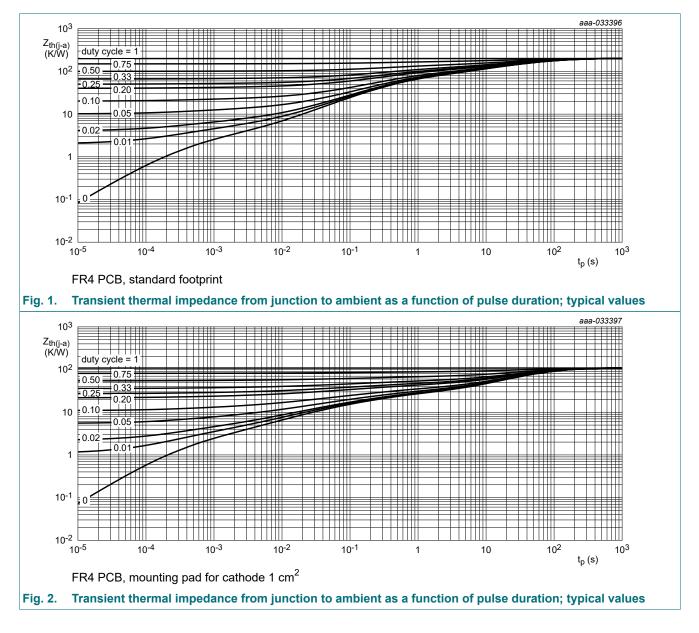
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ui(j-a)	thermal resistance from in free air	[1] [2]	-	-	230	K/W	
	junction to ambient		[1] [3]	-	-	125	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[4]	-	-	6	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[4] Soldering point of cathode tab.

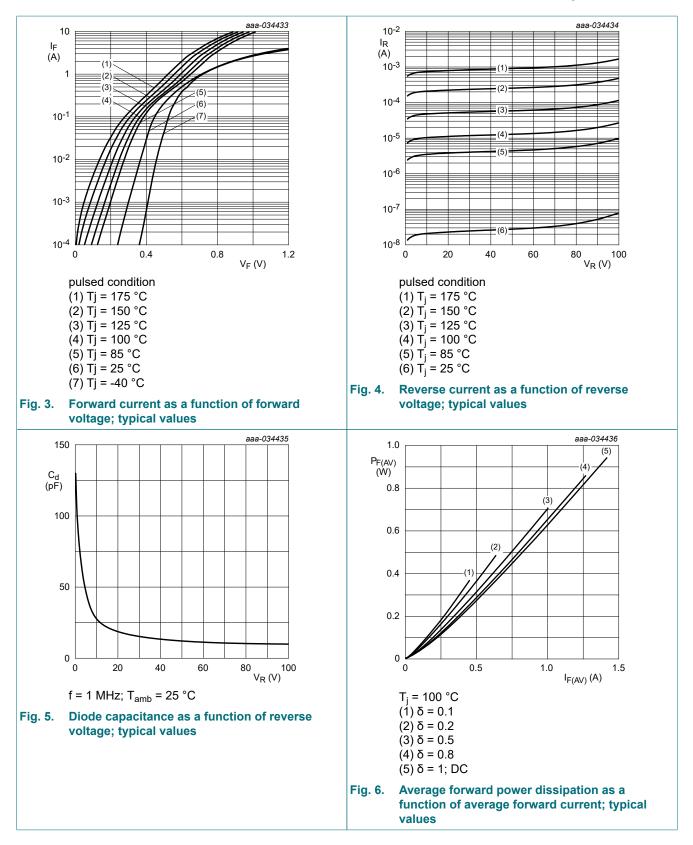


10. Characteristics

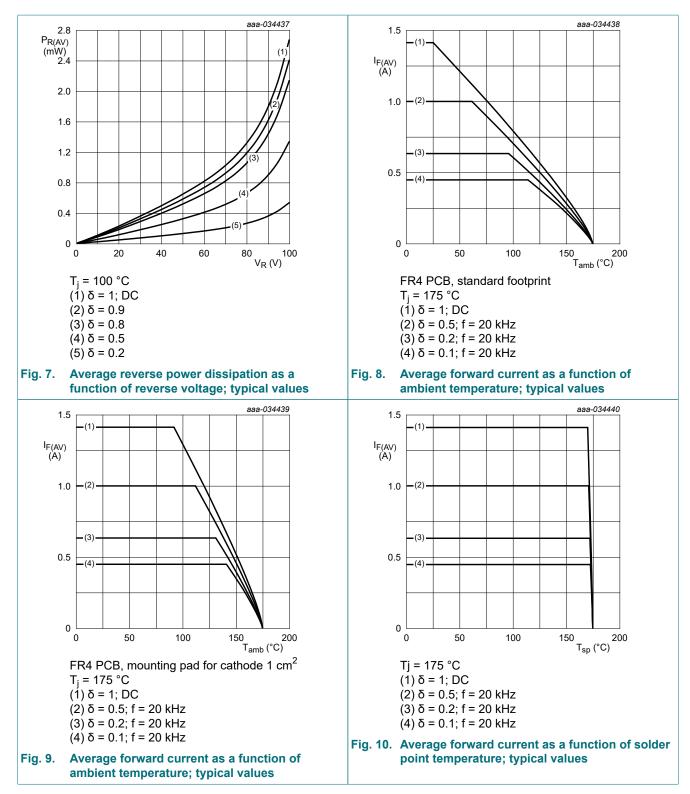
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)R}	reverse breakdown voltage	I_R = 1 mA; pulsed; T_j = 25 °C	[1]	100	-	-	V
V _F	forward voltage	I _F = 0.1 A; pulsed; T _j = 25 °C	[1]	-	455	515	mV
		I _F = 0.5 A; pulsed; T _j = 25 °C	[1]	-	600	695	mV
		I _F = 1 A; pulsed; T _j = 25 °C	[1]	-	720	795	mV
		I _F = 1 A; pulsed; T _j = -40 °C	[1]	-	720	795	mV
		I _F = 1 A; pulsed; T _j = 125 °C	[1]	-	590	665	mV
I _R	reverse current	V _R = 100 V; pulsed; T _j = 25 °C	[1]	-	0.1	0.5	μA
		V _R = 100 V; pulsed; T _j = 125 °C	[1]	-	0.15	0.6	mA
		V _R = 100 V; pulsed; T _j = 150 °C	[1]	-	0.6	3	mA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C		-	95	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C		-	30	-	pF
t _{rr}	reverse recovery time step recovery	$I_F = 0.5 \text{ A}; I_R = 1 \text{ A}; I_{R(meas)} = 0.25 \text{ A};$ $T_j = 25 \text{ °C}$		-	2.5	-	ns
	reverse recovery time ramp recovery	dI _F /dt = 100 A/µs; I _F = 1 A; V _R = 30 V; T _j = 25 °C		-	6	-	ns
I _{RM}	peak reverse recovery current			-	0.275	-	A
Q _{rr}	reverse recovery charge			-	1	-	nC
V _{FRM}	peak forward recovery voltage	I _F = 0.5 A; dI _F /dt = 20 A/μs; T _j = 25 °C		-	600	-	mV

[1] Very short pulse, in order to maintain a stable junction temperature.

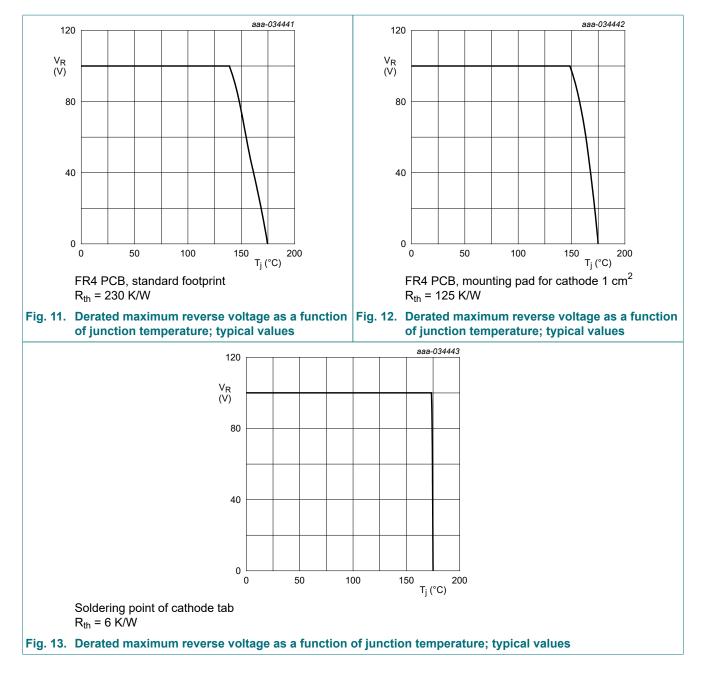
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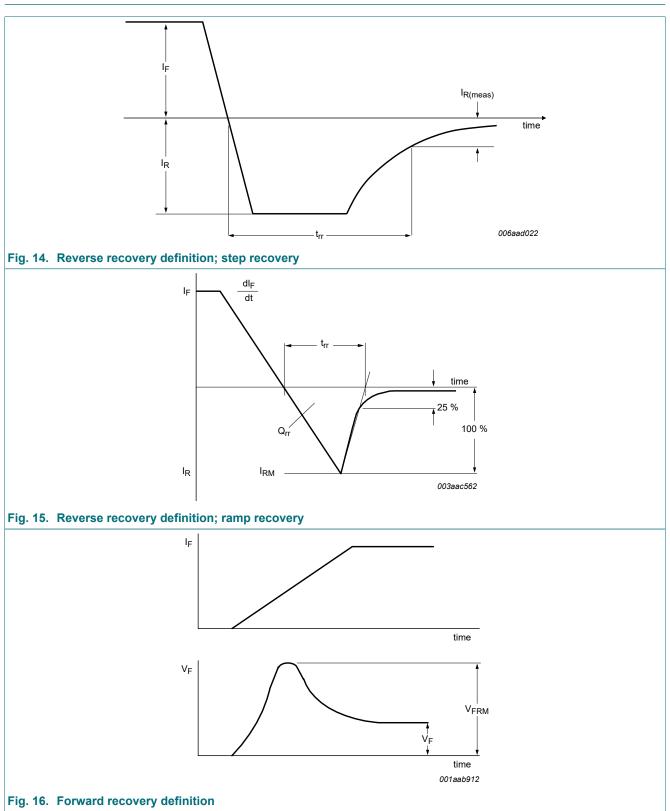


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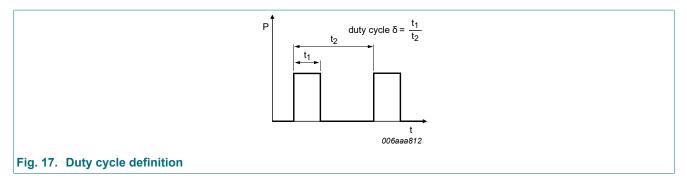


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11. Test information



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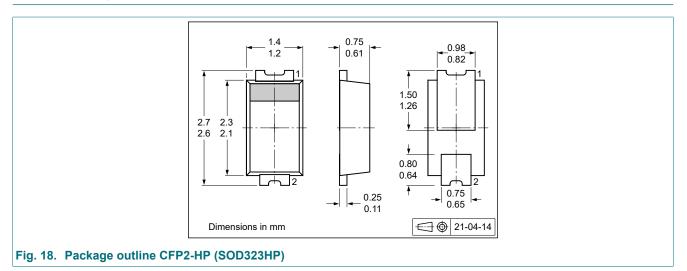
The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current

 $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$

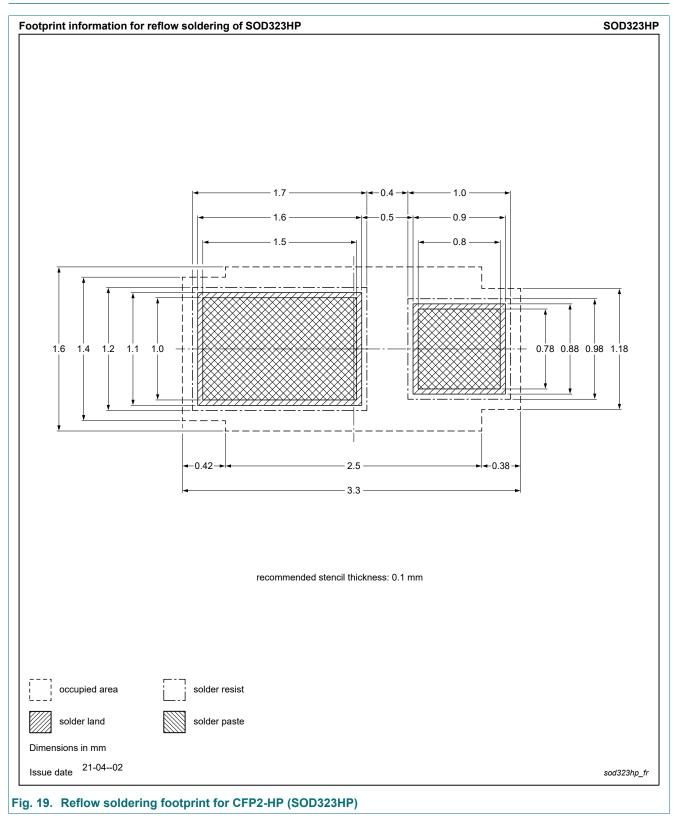
with $\mathsf{I}_{\mathsf{RMS}}$ defined as RMS current.

12. Package outline



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13. Soldering



14. Revision history

Table 8. Revision history								
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
PMEG100T10ELXD v.2	20220401	Product data sheet	-	PMEG100T10ELXD v.1				
Modifications:	Product statu	is changed						
PMEG100T10ELXD v.1	20220209	Preliminary data sheet	-	-				

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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