1. General description

Complementary N/P-channel enhancement mode Field-Effect Transistor (FET) in an ultra small and flat lead SOT666 Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- · Very fast switching
- Trench MOSFET technology
- ESD protection up to 2 kV

3. Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- · Switching circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
TR1 (N-chann	el), Static characteristic	s					
R _{DSon}	drain-source on-state resistance	$V_{GS} = 4.5 \text{ V}; I_D = 500 \text{ mA}; T_j = 25 \text{ °C}$		-	290	380	mΩ
TR2 (P-chann	el), Static characteristic	s				'	'
R _{DSon}	drain-source on-state resistance	V_{GS} = -4.5 V; I_{D} = -400 mA; T_{j} = 25 °C		-	0.67	0.85	Ω
TR1 (N-chann	iel)						'
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	20	V
V _{GS}	gate-source voltage			-8	-	8	V
I _D	drain current	V _{GS} = 4.5 V; T _{amb} = 25 °C	[1]	-	-	800	mA
TR2 (P-chann	iel)						"
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	-20	V
V _{GS}	gate-source voltage	7		-8	-	8	V
I_D	drain current	V _{GS} = -4.5 V; T _{amb} = 25 °C	[1]	-	-	-550	mA

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm².



20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S1	source TR1	6 5 4	D1 D2
2	G1	gate TR1	6 5 4	
3	D2	drain TR2		G1 A T G2
4	S2	source TR2		\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
5	G2	gate TR2	1 2 3	
6	D1	drain TR1	SOT666	S1 S2 017aaa262

6. Ordering information

Table 3. Ordering information

Type number	Package			
	Name	Description	Version	
PMDT290UCE	SOT666	plastic, surface-mounted package; 6 leads; 0.5 mm pitch; 1.6 mm x 1.2 mm x 0.55 mm body	SOT666	

7. Marking

Table 4. Marking codes

Type number	Marking code
PMDT290UCE	AF

20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

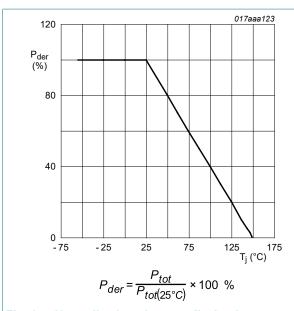
Symbol	Parameter	Conditions		Min	Max	Unit
TR1 (N-chai	nnel)				'	_
V_{DS}	drain-source voltage	T _j = 25 °C		-	20	V
V_{GS}	gate-source voltage			-8	8	V
I _D	drain current	V _{GS} = 4.5 V; T _{amb} = 25 °C	[1]	-	800	mA
		V _{GS} = 4.5 V; T _{amb} = 100 °C	[1]	-	500	mA
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \mu s$		-	3.2	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	330	mW
			[1]	-	390	mW
		T _{sp} = 25 °C		-	1090	mW
TR2 (P-char	nnel)		'	1	'	
V_{DS}	drain-source voltage	T _j = 25 °C		-	-20	V
V_{GS}	gate-source voltage			-8	8	V
I _D dı	drain current	V _{GS} = -4.5 V; T _{amb} = 25 °C	[1]	-	-550	mA
		V _{GS} = -4.5 V; T _{amb} = 100 °C	[1]	-	-350	mA
I _{DM}	peak drain current	T _{amb} = 25 °C; single pulse; t _p ≤ 10 μs		-	-2.2	Α
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	330	mW
			[1]	-	390	mW
		T _{sp} = 25 °C		-	1090	mW
Per device						
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	500	mW
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
TR1 (N-chai	nnel), Source-drain diode			'	'	
I _S	source current	T _{amb} = 25 °C	[1]	-	370	mA
TR2 (P-char	nnel), Source-drain diode			1	'	
I _S	source current	T _{amb} = 25 °C	[1]	-	-370	mA
TR1 (N-chai	nnel), ESD maximum rating		1	1		,
V _{ESD}	electrostatic discharge voltage	НВМ	[3]	-	2000	V
TR2 (P-char	nnel), ESD maximum rating			1		
V _{ESD}	electrostatic discharge voltage	НВМ	[3]	-	2000	V

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm².

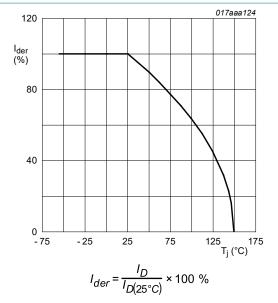
^[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper; tin-plated and standard footprint.

^[3] Measured between all pins.

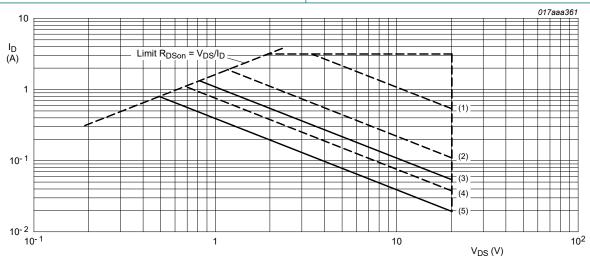
20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET



Normalized total power dissipation as a function of junction temperature



Normalized continuous drain current as a Fig. 2. function of junction temperature



 I_{DM} = single pulse

(1) $t_p = 1 \text{ ms}$

(1) $t_p = 10$ ms (2) $t_p = 10$ ms (3) DC; $T_{sp} = 25$ °C (4) $t_p = 100$ ms (5) DC; $T_{amb} = 25$ °C; drain mounting pad 1 cm²

Safe operating area TR1 (N-channel); junction to ambient; continuous and peak drain currents as a Fig. 3. function of drain-source voltage

20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET

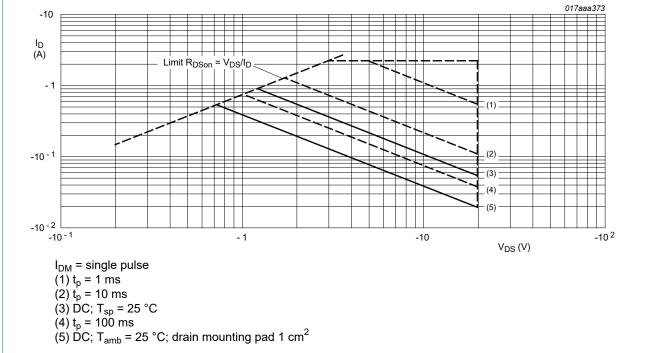


Fig. 4. Safe operating area TR2 (P-channel); junction to ambient; continuous and peak drain currents as a function of drain-source voltage

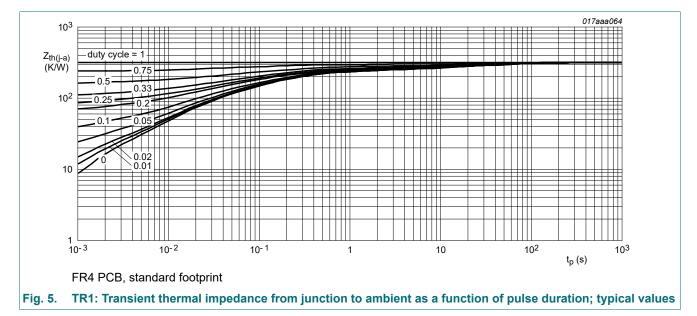
20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
TR1 (N-cha	nnel)		1				
R _{th(j-a)}	thermal resistance from	in free air [1	[1]	-	330	380	K/W
	junction to ambient		[2]	-	280	320	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	115	K/W
TR2 (P-cha	nnel)		,				
R _{th(j-a)}	thermal resistance from	in free air	[1]	-	330	380	K/W
	junction to ambient		[2]	-	280	320	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	115	K/W
Per device	<u>'</u>		,				
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	250	K/W

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm².



20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET

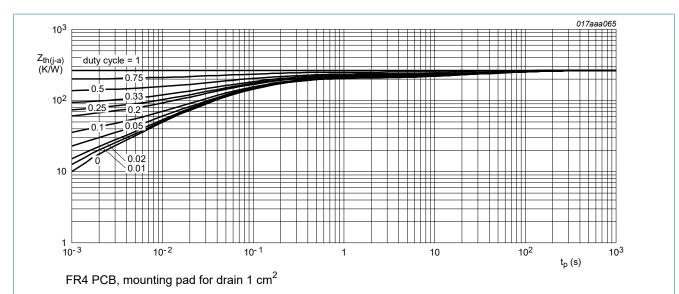
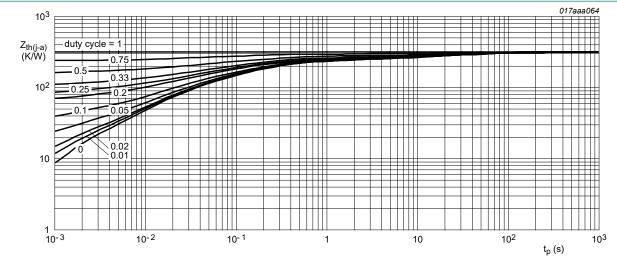


Fig. 6. TR1: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, standard footprint

Fig. 7. TR2: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

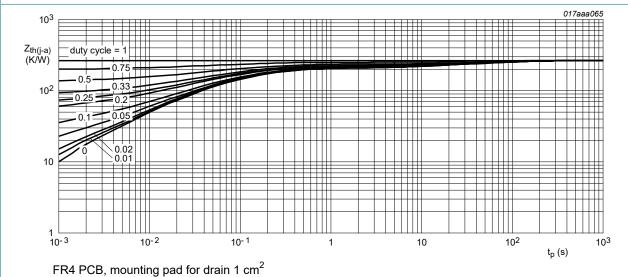


Fig. 8. TR2: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
TR1 (N-cha	nnel), Static characteristic	s				
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 250 μA; V _{GS} = 0 V; T _j = 25 °C	20	-	-	V
V_{GSth}	gate-source threshold voltage	I _D = 250 μA; V _{DS} =V _{GS} ; T _j = 25 °C	0.5	0.75	0.95	V
I _{DSS}	drain leakage current	V _{DS} = 20 V; V _{GS} = 0 V; T _j = 25 °C	-	-	1	μΑ
		V _{DS} = 20 V; V _{GS} = 0 V; T _j = 150 °C	-	-	10	μΑ
I_{GSS}	gate leakage current	V _{GS} = 8 V; V _{DS} = 0 V; T _j = 25 °C	-	-	2	μΑ
		$V_{GS} = -8 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	2	μΑ
		V _{GS} = 4.5 V; V _{DS} = 0 V; T _j = 25 °C	-	-	500	nA
		$V_{GS} = -4.5 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	-	500	nA
R _{DSon}	drain-source on-state	$V_{GS} = 4.5 \text{ V}; I_D = 500 \text{ mA}; T_j = 25 ^{\circ}\text{C}$	-	290	380	mΩ
1 103011	resistance	V _{GS} = 4.5 V; I _D = 500 mA; T _j = 150 °C	-	460	610	mΩ
		V _{GS} = 2.5 V; I _D = 200 mA; T _j = 25 °C	-	420	620	mΩ
		V _{GS} = 1.8 V; I _D = 10 mA; T _j = 25 °C	-	0.6	1.1	Ω
9fs	forward transconductance	V _{DS} = 10 V; I _D = 200 mA; T _j = 25 °C	-	1.6	-	S
TR2 (P-chai	nnel), Static characteristic	s	l .			
V _{(BR)DSS}	drain-source breakdown voltage	I_D = -250 μ A; V_{GS} = 0 V; T_j = 25 °C	-20	-	-	V
V_{GSth}	gate-source threshold voltage	$I_D = -250 \mu A; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$	-0.5	-0.8	-1.3	V
DSS drain leakage currer	drain leakage current	$V_{DS} = -20 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	-1	μA
	3	V _{DS} = -20 V; V _{GS} = 0 V; T _i = 150 °C	-	-	-10	μΑ
I _{GSS}	gate leakage current	V _{GS} = -8 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-2	μΑ
		V _{GS} = 8 V; V _{DS} = 0 V; T _i = 25 °C	-	-	-2	μΑ
		V _{GS} = -4.5 V; V _{DS} = 0 V; T _i = 25 °C	-	-	-0.5	μΑ
		V _{GS} = 4.5 V; V _{DS} = 0 V; T _i = 25 °C	-	-	-0.5	μΑ
R _{DSon}	drain-source on-state	V _{GS} = -4.5 V; I _D = -400 mA; T _i = 25 °C	-	0.67	0.85	Ω
	resistance	V _{GS} = -4.5 V; I _D = -400 mA; T _i = 150 °C	-	1.1	1.4	Ω
		V _{GS} = -2.5 V; I _D = -200 mA; T _i = 25 °C	-	1.2	1.5	Ω
		V _{GS} = -1.8 V; I _D = -10 mA; T _j = 25 °C	-	1.8	2.8	Ω
9 _{fs}	forward transconductance	V_{DS} = -10 V; I_D = -200 mA; T_j = 25 °C	-	610	-	mS
TR1 (N-cha	nnel), Dynamic characteris	stics	<u> </u>			
Q _{G(tot)}	total gate charge	V_{DS} = 10 V; I_{D} = 500 mA; V_{GS} = 4.5 V;	-	0.45	0.68	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.15	-	nC
Q_{GD}	gate-drain charge		-	0.15	-	nC
C _{iss}	input capacitance	V _{DS} = 10 V; f = 1 MHz; V _{GS} = 0 V;	-	55	83	pF
C _{oss}	output capacitance	T _j = 25 °C	-	15	-	pF
C _{rss}	reverse transfer capacitance		-	7	-	pF

20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
t _{d(on)}	turn-on delay time	V_{DS} = 10 V; R_L = 250 Ω ; V_{GS} = 4.5 V;	-	6	12	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega$; $T_j = 25 °C$	-	4	-	ns
t _{d(off)}	turn-off delay time		-	86	172	ns
t _f	fall time		-	31	-	ns
TR2 (P-chai	nnel), Dynamic characteri	stics				
Q _{G(tot)}	total gate charge	V_{DS} = -10 V; I_{D} = -400 mA; V_{GS} = -4.5 V; T_{i} = 25 °C	-	0.76	1.14	nC
Q _{GS}	gate-source charge	$V_{GS} = -4.5 \text{ V}; T_j = 25 \text{ °C}$	-	0.28	-	nC
Q _{GD}	gate-drain charge		-	0.18	-	nC
C _{iss}	input capacitance	V_{DS} = -10 V; f = 1 MHz; V_{GS} = 0 V; T_j = 25 °C	-	58	87	pF
C _{oss}	output capacitance		-	21	-	pF
C _{rss}	reverse transfer capacitance		-	12	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = -10 V; R_L = 250 Ω ; V_{GS} = -4.5 V;	-	18	36	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 ^{\circ}C$	-	30	-	ns
t _{d(off)}	turn-off delay time		-	80	160	ns
t _f	fall time		-	72	-	ns
TR1 (N-cha	nnel), Source-drain diode	characteristics				
V _{SD}	source-drain voltage	$I_S = 300 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$	0.48	0.77	1.2	V
TR2 (P-cha	nnel), Source-drain diode	characteristics	1	'		
V _{SD}	source-drain voltage	I_S = -300 mA; V_{GS} = 0 V; T_j = 25 °C	-0.4	3 -0.84	-1.2	V

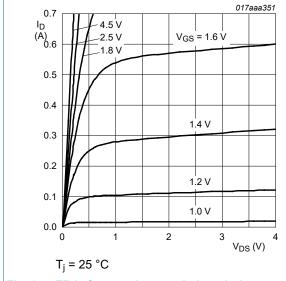
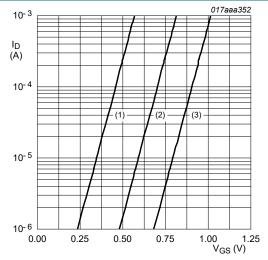


Fig. 9. TR1; Output characteristics: drain current as a function of drain-source voltage; typical values

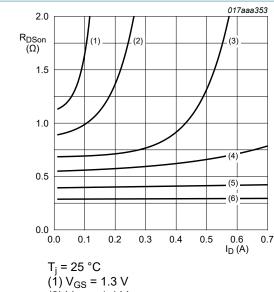


 $T_i = 25 \,^{\circ}C; \, V_{DS} = 5 \,^{\circ}V$

- (1) minimum values
- (2) typical values
- (3) maximum values

Fig. 10. TR1; Sub-threshold drain current as a function of gate-source voltage

20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET

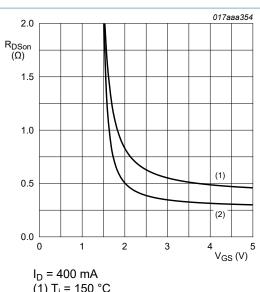


 $(2) V_{GS} = 1.4 V$

 $(3) V_{GS} = 1.6 V$

(6) $V_{GS} = 1.3 \text{ V}$ (4) $V_{GS} = 1.8 \text{ V}$ (5) $V_{GS} = 2.5 \text{ V}$ (6) $V_{GS} = 4.5 \text{ V}$

Fig. 11. TR1; Drain-source on-state resistance as a function of drain current; typical values



 $(1) T_i = 150 °C$

(2) $T_j = 25 \, ^{\circ}C$

Fig. 12. TR1; Drain-source on-state resistance as a function of gate-source voltage; typical values

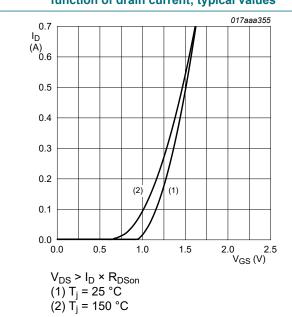


Fig. 13. TR1; Transfer characteristics: drain current as a function of gate-source voltage; typical values

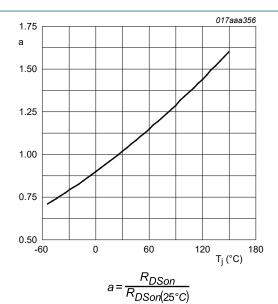
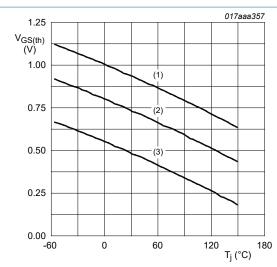


Fig. 14. TR1; Normalized drain-source on-state resistance as a function of junction temperature; typical values

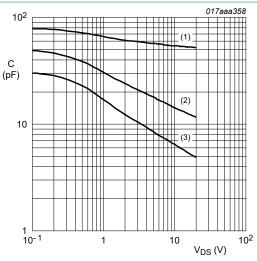
20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET



 I_D = 0.25 mA; V_{DS} = V_{GS}

- (1) maximum values
- (2) typical values
- (3) minimum values

Fig. 15. TR1; Gate-source threshold voltage as a function of junction temperature



 $f = 1 MHz; V_{GS} = 0 V$

- (1) C_{iss}
- (2) Coss
- (3) C_{rss}

Fig. 16. TR1; Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

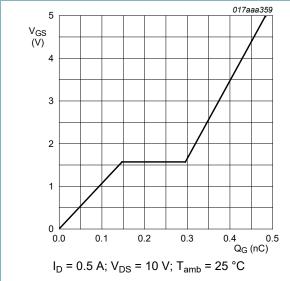


Fig. 17. TR1; Gate-source voltage as a function of gate charge; typical values

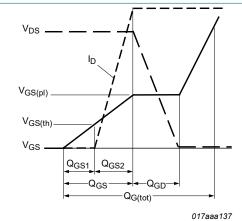


Fig. 18. Gate charge waveform definitions

20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET

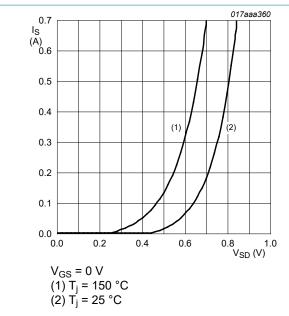


Fig. 19. TR1; Source current as a function of sourcedrain voltage; typical values

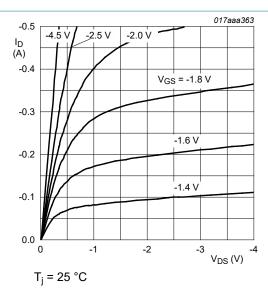
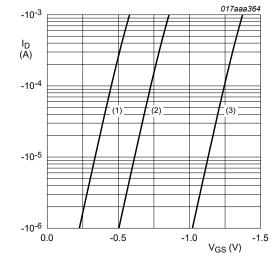
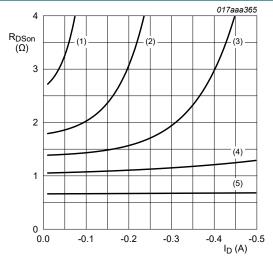


Fig. 20. TR2; Output characteristics: drain current as a function of drain-source voltage; typical values



- $T_j = 25 \,^{\circ}C; \, V_{DS} = -5 \,^{\circ}V$
- (1) minimum values
- (2) typical values
- (3) maximum values

Fig. 21. TR2; Sub-threshold drain current as a function of gate-source voltage



- $T_i = 25 \,^{\circ}C$
- $(1) V_{GS} = -1.5 V$
- $(2) V_{GS} = -1.8 V$
- (3) $V_{GS} = -2.0 \text{ V}$
- $(4) V_{GS} = -2.5 V$
- $(5) V_{GS} = -4.5 V$

Fig. 22. TR2; Drain-source on-state resistance as a function of drain current; typical values

20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET

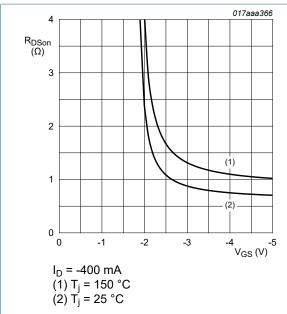


Fig. 23. TR2; Drain-source on-state resistance as a function of gate-source voltage; typical values

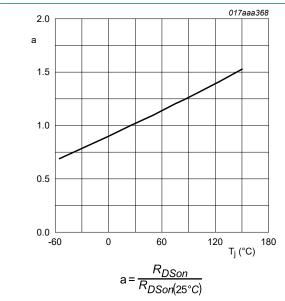


Fig. 25. TR2; Normalized drain-source on-state resistance as a function of ambient temperature; typical values

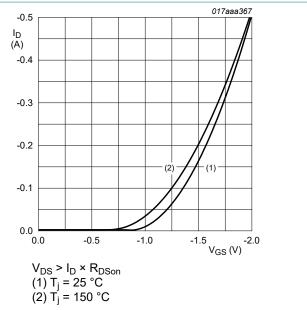
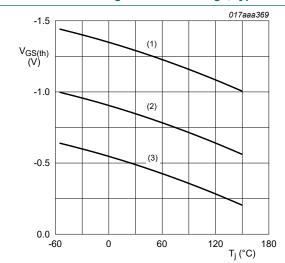


Fig. 24. TR2; Transfer characteristics: drain current as a function of gate-source voltage; typical values

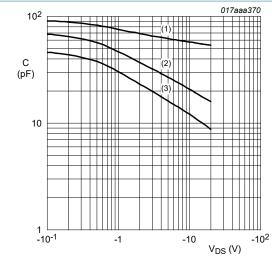


 I_D = -0.25 mA; V_{DS} = V_{GS}

- (1) maximum values
- (2) typical values
- (3) minimum values

Fig. 26. TR2; Gate-source threshold voltage as a function of junction temperature

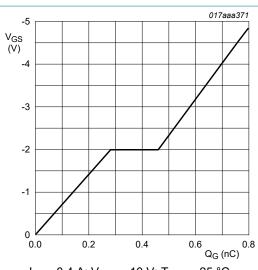
20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET



 $f = 1 MHz; V_{GS} = 0 V$

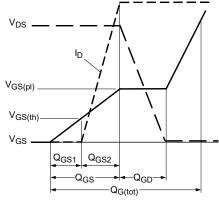
- (1) C_{iss}
- (2) C_{oss}
- (3) C_{rss}

Fig. 27. TR2; Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



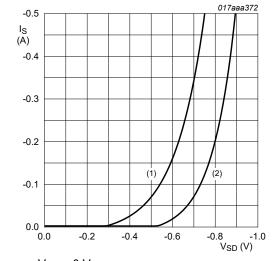
 I_D = -0.4 A; V_{DD} = -10 V; T_{amb} = 25 °C

Fig. 28. TR2; Gate-source voltage as a function of gate charge; typical values



017aaa137

Fig. 29. Gate charge waveform definitions



 $V_{GS} = 0 V$

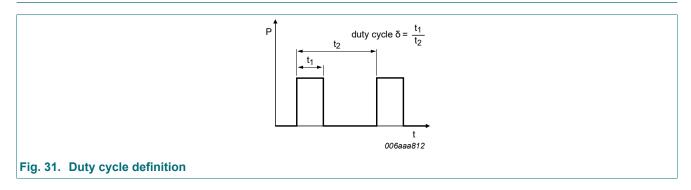
(1) $T_{amb} = 150 \, ^{\circ}C$

(2) $T_{amb} = 25 \, ^{\circ}C$

Fig. 30. TR2; Source current as a function of sourcedrain voltage; typical values

20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET

11. Test information



20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET

12. Package outline

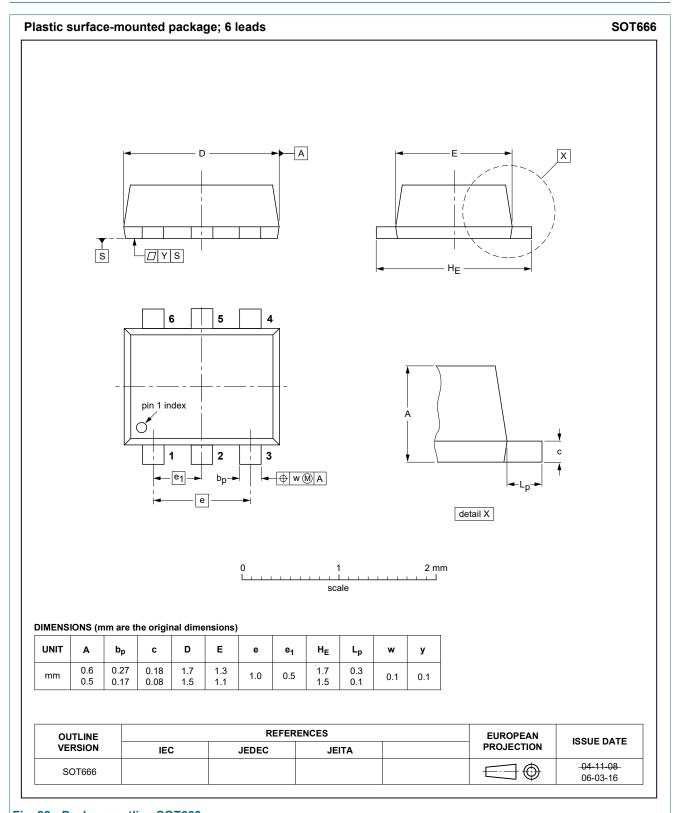
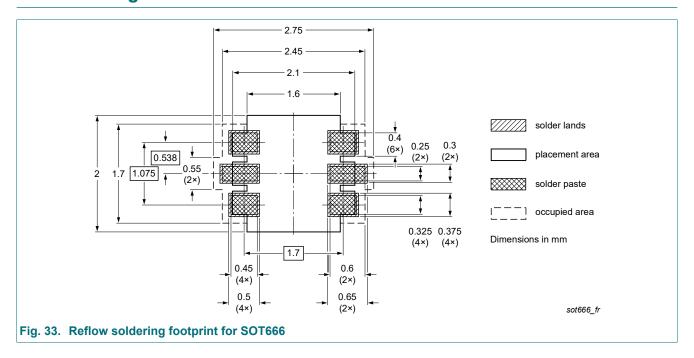


Fig. 32. Package outline SOT666

20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET

13. Soldering



20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMDT290UCE v.2	20221228	Product data sheet	-	PMDT290UCE v.1		
Modifications:	Nexperia Legal texts have bee	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia Legal texts have been adapted to the new company name where appropriate Product changed to non-automotive qualification 				
PMDT290UCE v.1	20111006	Product data sheet	-	-		

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com.

Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal

20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET

injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nexperia.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by sustained.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

PMDT290UCE

All information provided in this document is subject to legal disclaimers.

Nexperia B.V. 2022. All rights reserved

20 / 20 V, 800 / 550 mA N/P-channel Trench MOSFET

Contents

1.	General description	1
2.	Features and benefits	1
3.	Applications	1
4.	Quick reference data	1
5.	Pinning information	2
6.	Ordering information	2
7.	Marking	2
8.	Limiting values	3
9.	Thermal characteristics	6
10	. Characteristics	8
11.	. Test information	15
12	. Package outline	16
	. Soldering	
	. Revision history	
	. Legal information	

For more information, please visit: http://www.nexperia.com
For sales office addresses, please send an email to: salesaddresses@nexperia.com
Date of release: 28 December 2022

[©] Nexperia B.V. 2022. All rights reserved