

40 V, 600 mA PNP switching transistor

16 June 2023

Product data sheet

1. General description

PNP switching transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- High current (max. 600 mA)
- Collector-emitter voltage V_{CEO} = 40 V
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

• Switching and linear amplification

4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	-40	V
I _C	collector current			-	-	-600	mA
h _{FE}	DC current gain	V_{CE} = -2 V; I _C = -150 mA; T _{amb} = 25 °C		100	-	300	

5. Pinning information

Table 2. Pinning information								
Pin	Symbol	Description	Simplified outline	Graphic symbol				
1	В	base	3					
2	E	emitter		С				
3	С	collector		в				
			1 2	sym132				
			SOT23					

6. Ordering information

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Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PMBT4403-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	<u>SOT23</u>			

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7. Marking

Table 4. Marking codes	
Type number	Marking code[1]
PMBT4403-Q	%2T

[1] % = placeholder for manufacturing site code

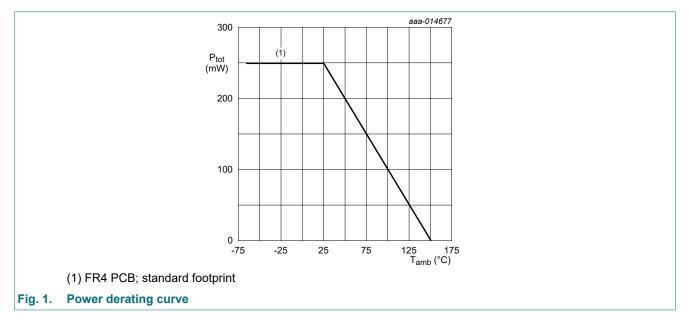
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-40	V
V _{CEO}	collector-emitter voltage	open base		-	-40	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-600	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-800	mA
I _{BM}	peak base current			-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

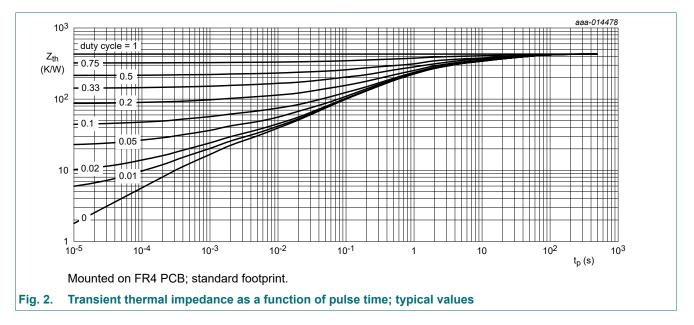
[1] Transistor mounted on an FR4 printed-circuit board, single-sided copper, tin-plated and standard footprint.



9. Thermal characteristics

Table 6. Thermal characteristics							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ui(j-a)	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

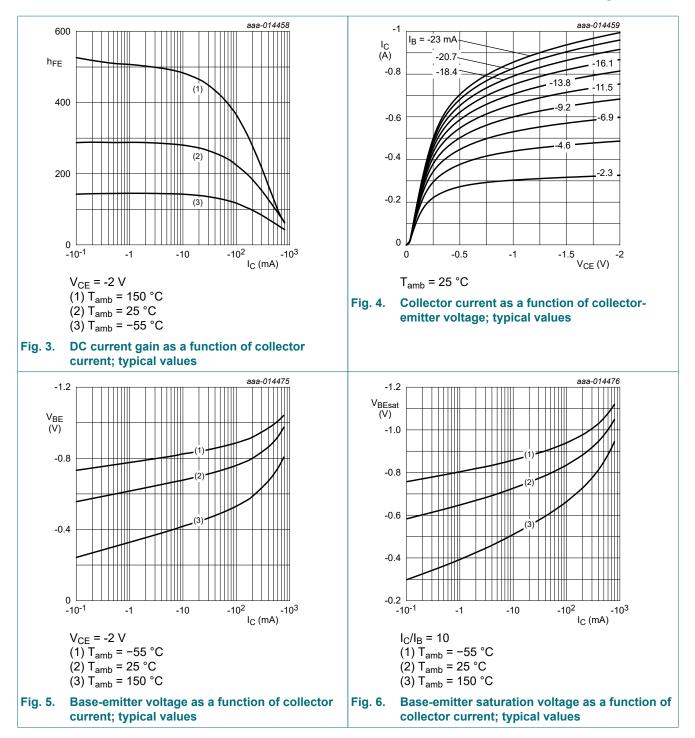
[1] Transistor mounted on an FR4 printed-circuit board, single-sided copper, tin-plated and standard footprint.



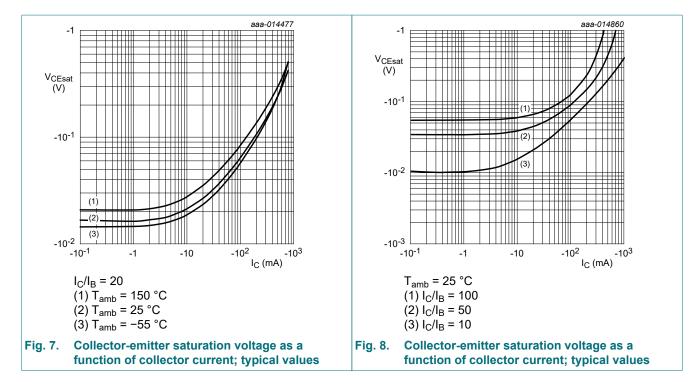
10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = -40 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-50	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-50	nA
h _{FE}	DC current gain	V_{CE} = -1 V; I _C = -0.1 mA; T _{amb} = 25 °C	30	-	-	
		V_{CE} = -1 V; I _C = -1 mA; T _{amb} = 25 °C	60	-	-	
		V_{CE} = -1 V; I _C = -10 mA; T _{amb} = 25 °C	100	-	-	
		V_{CE} = -2 V; I _C = -150 mA; T _{amb} = 25 °C	100	-	300	
		V_{CE} = -2 V; I _C = -500 mA; T _{amb} = 25 °C	20	-	-	
V _{CEsat}	CEsat collector-emitter saturation voltage	I_{C} = -150 mA; I_{B} = -15 mA; T_{amb} = 25 °C	-	-	-400	mV
		I_{C} = -500 mA; I_{B} = -50 mA; T_{amb} = 25 °C	-	-	-750	mV
DEGU	base-emitter saturation	I_{C} = -150 mA; I_{B} = -15 mA; T_{amb} = 25 °C	-	-	-950	mV
	voltage	I_{C} = -500 mA; I_{B} = -50 mA; T_{amb} = 25 °C	-	-	-1.3	V
t _d	delay time	I _C = -150 mA; I _{Bon} = -15 mA;	-	-	15	ns
t _r	rise time	I _{Boff} = 15 mA; T _{amb} = 25 °C	-	-	30	ns
t _{on}	turn-on time	-	-	-	40	ns
t _s	storage time		-	-	300	ns
t _f	fall time	-	-	-	50	ns
t _{off}	turn-off time		-	-	350	ns
C _c	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	8.5	pF
C _e	emitter capacitance	V_{EB} = -500 mV; I _C = 0 A; i _c = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	35	pF
f _T	transition frequency	V _{CE} = -10 V; I _C = -20 mA; f = 100 MHz; T _{amb} = 25 °C	200	-	-	MHz

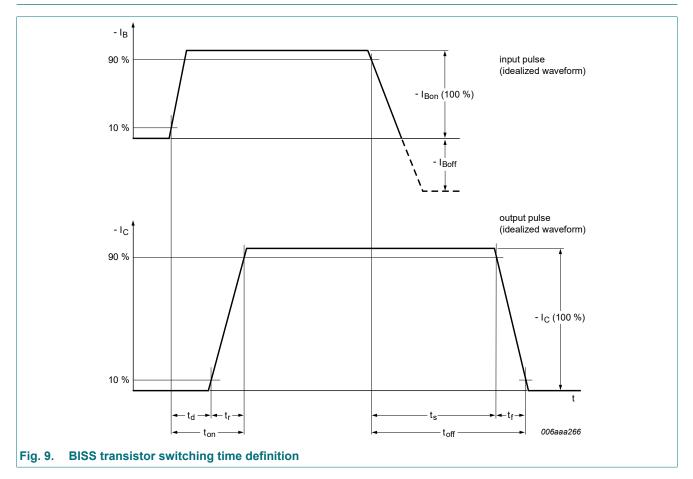
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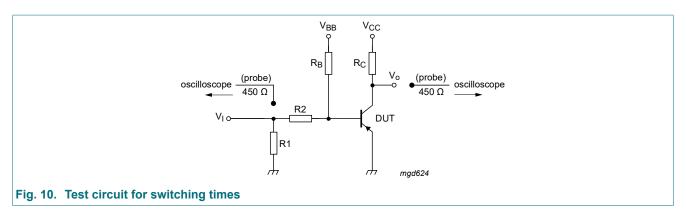
40 V, 600 mA PNP switching transistor



11. Test information



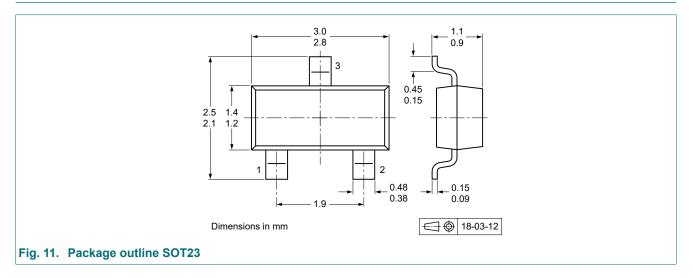
40 V, 600 mA PNP switching transistor



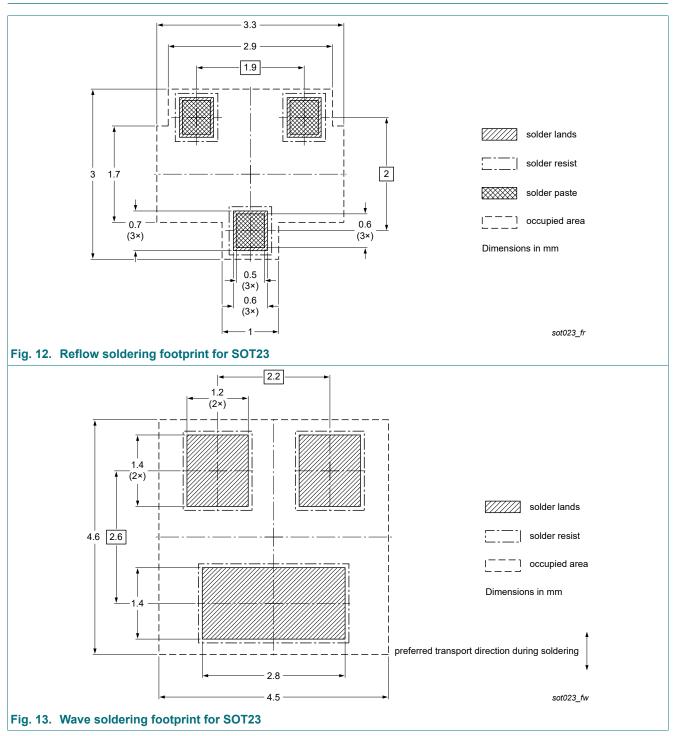
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMBT4403-Q v.1	20230616	Product data sheet	-	-			

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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