### 1. General description

PNP/PNP general-purpose double transistor in a SOT363 (SC-88) a very small Surface-Mounted Device (SMD) plastic package.

NPN/NPN complement: PMBT3904YS NPN/PNP complement: PMBT3946YPN

### 2. Features and benefits

- General-purpose double transistor
- Board-space reduction
- AEC-Q101 qualified

# 3. Applications

· General-purpose switching and amplification

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
Per transistor	Per transistor							
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	-40	V	
I <sub>C</sub>	collector current			-	-	-200	mA	
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -1 V; $I_{C}$ = -10 mA; $T_{amb}$ = 25 °C		100	180	300		

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1	∏6 ∏5 ∏4	C1 B2 E2
2	B1	base TR1		
3	C2	collector TR2		( TR1 )
4	E2	emitter TR2		
5	B2	base TR2		E1 B1 C2
6	C1	collector TR1	TSSOP6 (SOT363)	sym018



### 40 V, 200 mA PNP/PNP general-purpose double transistor

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package				
	Name	Description	Version		
PMBT3906YS	TSSOP6	plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	<u>SOT363</u>		

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code[1]
PMBT3906YS	BD%

<sup>[1] % =</sup> placeholder for manufacturing site code

# 8. Limiting values

#### Table 5. Limiting values

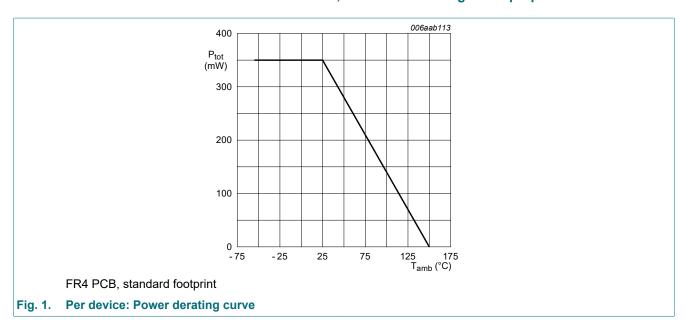
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	or			'	•	
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-40	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-40	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-6	V
Ic	collector current			-	-200	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-200	mA
I <sub>BM</sub>	peak base current			-	-100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	230	mW
Per device	·		·			
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	350	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

PMBT3906YS

#### 40 V, 200 mA PNP/PNP general-purpose double transistor

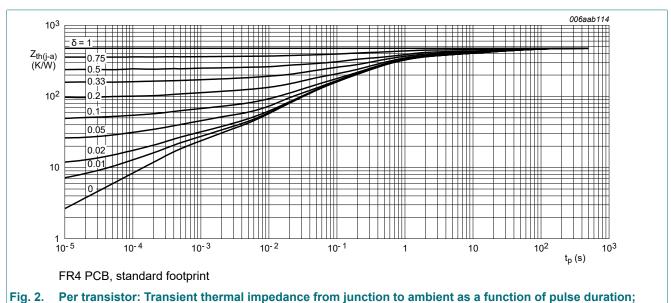


### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

14510 0. 11101	mai characteristics			1			
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transisto	or						
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	543	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	290	K/W
Per device							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	357	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.



typical values

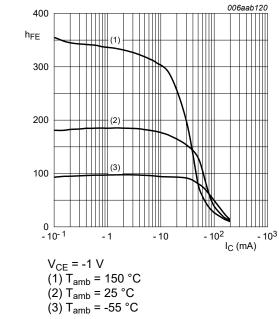
### 40 V, 200 mA PNP/PNP general-purpose double transistor

# 10. Characteristics

#### **Table 7. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transis	tor					
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = -30 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-50	nA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = -6 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	-	-	-50	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -0.1 mA; T <sub>amb</sub> = 25 °C	60	180	-	
		V <sub>CE</sub> = -1 V; I <sub>C</sub> = -1 mA; T <sub>amb</sub> = 25 °C	80	180	-	
		V <sub>CE</sub> = -1 V; I <sub>C</sub> = -10 mA; T <sub>amb</sub> = 25 °C	100	180	300	
		V <sub>CE</sub> = -1 V; I <sub>C</sub> = -50 mA; T <sub>amb</sub> = 25 °C	60	130	-	
		V <sub>CE</sub> = -1 V; I <sub>C</sub> = -100 mA; T <sub>amb</sub> = 25 °C	30	50	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = -10 \text{ mA}; I_B = -1 \text{ mA}; T_{amb} = 25 ^{\circ}C$	-	-100	-250	mV
		I <sub>C</sub> = -50 mA; I <sub>B</sub> = -5 mA; T <sub>amb</sub> = 25 °C	-	-165	-400	mV
V <sub>BEsat</sub> base-emit voltage	base-emitter saturation	$I_C = -10 \text{ mA}; I_B = -1 \text{ mA}; T_{amb} = 25 ^{\circ}C$	-	-750	-850	mV
	voltage	$I_C = -50 \text{ mA}; I_B = -5 \text{ mA}; T_{amb} = 25 ^{\circ}C$	-	-850	-950	mV
t <sub>d</sub>	delay time	I <sub>C</sub> = -10 mA; I <sub>Bon</sub> = -1 mA; I <sub>Boff</sub> = 1 mA;	-	-	35	ns
t <sub>r</sub>	rise time	V <sub>CC</sub> = -3 V; T <sub>amb</sub> = 25 °C	-	-	35	ns
t <sub>on</sub>	turn-on time		-	-	70	ns
t <sub>s</sub>	storage time		-	-	225	ns
t <sub>f</sub>	fall time		-	-	75	ns
t <sub>off</sub>	turn-off time		-	-	300	ns
C <sub>c</sub>	collector capacitance	$V_{CB}$ = -5 V; $I_{E}$ = 0 A; $i_{e}$ = 0 A; $f$ = 1 MHz; $T_{amb}$ = 25 °C	-	-	4.5	pF
C <sub>e</sub>	emitter capacitance	V <sub>EB</sub> = -0.5 V; I <sub>C</sub> = 0 A; i <sub>c</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	-	10	pF
f <sub>T</sub>	transition frequency	$V_{CE}$ = -20 V; $I_{C}$ = -10 mA; f = 100 MHz; $T_{amb}$ = 25 °C	250	-	-	MHz
NF	noise figure	$V_{CE}$ = -5 V; $I_{C}$ = -100 μA; $R_{S}$ = 1 kΩ; f = 10 Hz to 15.7 kHz; $T_{amb}$ = 25 °C	-	-	4	dB

#### 40 V, 200 mA PNP/PNP general-purpose double transistor



$$V_{CE} = -1 V$$

$$(1) T_{amb} = 150 °($$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 3. DC current gain as a function of collector current; typical values

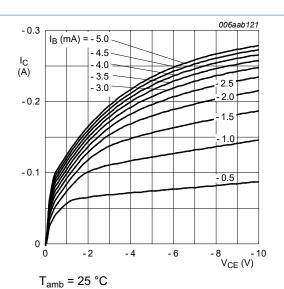
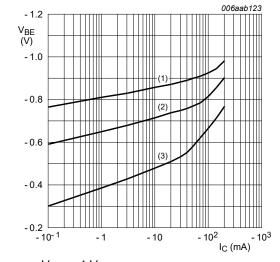


Fig. 4. Collector current as a function of collectoremitter voltage; typical values



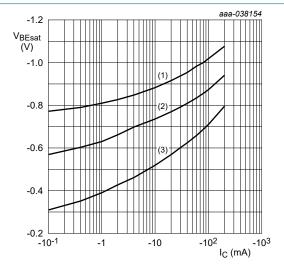
 $V_{CE} = -1 V$ 

$$(1) T_{amb} = -55 °($$

(1) 
$$T_{amb} = -55 \,^{\circ}C$$
  
(2)  $T_{amb} = 25 \,^{\circ}C$ 

(3) 
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 5. Base-emitter voltage as a function of collector current; typical values



$$I_{\rm C}/I_{\rm B} = 10$$

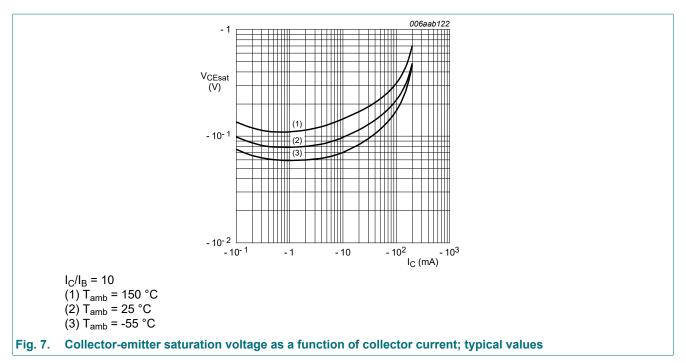
(1) 
$$T_{amb} = -55$$
 °C

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

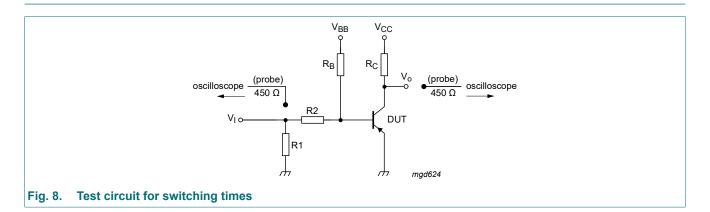
(3) 
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 6. Base-emitter saturation voltage as a function of collector current; typical values

### 40 V, 200 mA PNP/PNP general-purpose double transistor



11. Test information

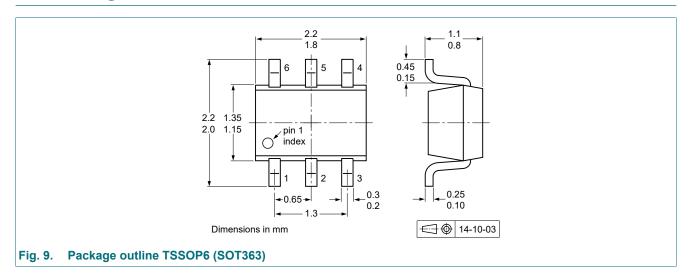


### **Quality information**

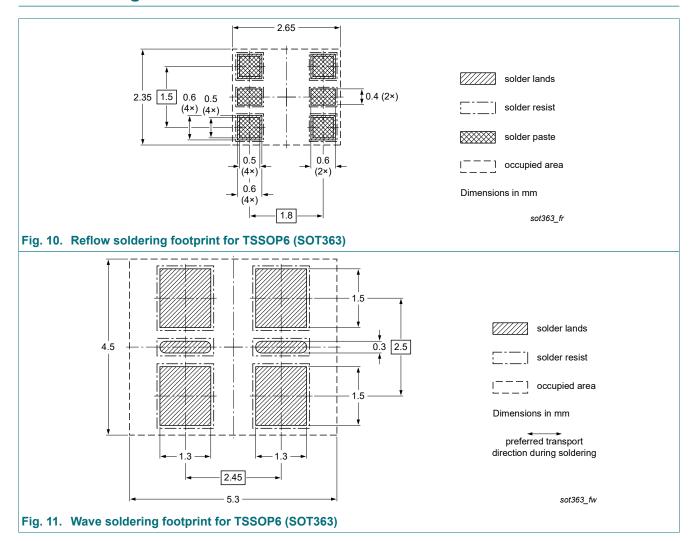
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

### 40 V, 200 mA PNP/PNP general-purpose double transistor

# 12. Package outline



### 13. Soldering



### 40 V, 200 mA PNP/PNP general-purpose double transistor

# 14. Revision history

#### **Table 8. Revision history**

Table of Iteriologic inc	,						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMBT3906YS v.3	20240115	Product data sheet	-	PMBT3906YS v.2			
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Packing information removed</li> </ul>						
PMBT3906YS v.2	20090513	Product data sheet	-	PMBT3906YS v.1			
PMBT3906YS v.1	20080306	Product data sheet	-	-			

#### 40 V, 200 mA PNP/PNP general-purpose double transistor

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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PMBT3906YS

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For more information, please visit: http://www.nexperia.com For sales office addresses, please send an email to: salesaddresses@nexperia.com Date of release: 15 January 2024

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