1. General description

Double NPN switching transistor in a very small SOT363 (TSSOP6) Surface-Mounted Device (SMD) plastic package.
Double PNP complement: PMBT2907AYS

2. Features and benefits

- Double general-purpose switching transistor
- High current (max. 600 mA)
- Voltage max. 40 V
- AEC-Q101 qualified

3. Applications

- Switching and linear amplification

4. Quick reference data

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>V_{CEO}</td>
<td>collector-emitter voltage</td>
<td>open base</td>
<td>-</td>
<td>-</td>
<td>40</td>
<td>V</td>
</tr>
<tr>
<td>I_C</td>
<td>collector current</td>
<td></td>
<td>-</td>
<td>-</td>
<td>600</td>
<td>mA</td>
</tr>
<tr>
<td>h_{FE}</td>
<td>DC current gain</td>
<td>V_{CE} = 10 V; I_C = 150 mA; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C</td>
<td>100</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>V_{CE} = 10 V; I_C = 500 mA; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C</td>
<td>40</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
</tbody>
</table>
5. Pinning information

Table 2. Pinning information

<table>
<thead>
<tr>
<th>Pin</th>
<th>Symbol</th>
<th>Description</th>
<th>Simplified outline</th>
<th>Graphic symbol</th>
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</thead>
<tbody>
<tr>
<td>1</td>
<td>E</td>
<td>emitter TR1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>B</td>
<td>base TR1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>C</td>
<td>collector TR2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>E</td>
<td>emitter TR2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>B</td>
<td>base TR2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>C</td>
<td>collector TR1</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

6. Ordering information

Table 3. Ordering information

<table>
<thead>
<tr>
<th>Type number</th>
<th>Package</th>
<th>Name</th>
<th>Description</th>
<th>Version</th>
</tr>
</thead>
<tbody>
<tr>
<td>PMBT2222AYS</td>
<td>TSSOP6</td>
<td>plastic</td>
<td>surface-mounted package; 6 leads</td>
<td>SOT363</td>
</tr>
</tbody>
</table>

7. Marking

Table 4. Marking codes

<table>
<thead>
<tr>
<th>Type number</th>
<th>Marking code</th>
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<tbody>
<tr>
<td>PMBT2222AYS</td>
<td>BF%</td>
</tr>
</tbody>
</table>

[1] placeholder for manufacturing site code
8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Per transistor</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>V_{CBO}</td>
<td>collector-base voltage</td>
<td>open emitter</td>
<td>-</td>
<td>75</td>
<td>V</td>
</tr>
<tr>
<td>V_{CEO}</td>
<td>collector-emitter voltage</td>
<td>open base</td>
<td>-</td>
<td>40</td>
<td>V</td>
</tr>
<tr>
<td>V_{EBO}</td>
<td>emitter-base voltage</td>
<td>open collector</td>
<td>-</td>
<td>6</td>
<td>V</td>
</tr>
<tr>
<td>I_C</td>
<td>collector current</td>
<td></td>
<td>-</td>
<td>600</td>
<td>mA</td>
</tr>
<tr>
<td>I_{CM}</td>
<td>peak collector current</td>
<td>single pulse; ( t_p \leq 1 \text{ ms} )</td>
<td>-</td>
<td>800</td>
<td>mA</td>
</tr>
<tr>
<td>I_{BM}</td>
<td>peak base current</td>
<td></td>
<td>-</td>
<td>200</td>
<td>mA</td>
</tr>
<tr>
<td>P_{tot}</td>
<td>total power dissipation</td>
<td>( T_{amb} \leq 25 \text{ °C} )</td>
<td>[1]</td>
<td>-250</td>
<td>mW</td>
</tr>
<tr>
<td></td>
<td>Per device</td>
<td></td>
<td>[2]</td>
<td>-300</td>
<td>mW</td>
</tr>
<tr>
<td>P_{tot}</td>
<td>total power dissipation</td>
<td>( T_{amb} \leq 25 \text{ °C} )</td>
<td>[1]</td>
<td>-400</td>
<td>mW</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[2]</td>
<td>-550</td>
<td>mW</td>
</tr>
<tr>
<td>T_j</td>
<td>junction temperature</td>
<td></td>
<td>-</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>T_{amb}</td>
<td>ambient temperature</td>
<td></td>
<td>-55</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>T_{stg}</td>
<td>storage temperature</td>
<td></td>
<td>-65</td>
<td>150</td>
<td>°C</td>
</tr>
</tbody>
</table>

[1] Device mounted on an FR4 Printed-Circuit Board (PCB); single-sided copper; tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB; single-sided copper; tin-plated and mounting pad for collector 1 cm².

Fig. 1. Per device: Power derating curves SOT363 (SC-88)
9. Thermal characteristics

Table 6. Thermal characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$R_{th(j-a)}$</td>
<td>thermal resistance from junction to ambient</td>
<td>in free air</td>
<td>[1]</td>
<td>-</td>
<td>-</td>
<td>500 K/W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[2]</td>
<td>-</td>
<td>-</td>
<td>417 K/W</td>
</tr>
</tbody>
</table>

Per device

| $R_{th(j-a)}$ | thermal resistance from junction to ambient | in free air         | [1] | -   | -   | 313 K/W |
|              |                                              |                     | [2] | -   | -   | 227 K/W |

[2] Device mounted on an FR4 PCB; single-sided copper; tin-plated and mounting pad for collector 1 cm².

**Fig. 2.** Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values
FR4 PCB, mounting pad for collector 1 cm²

Fig. 3. Per Transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values
### Table 7. Characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>( I_{\text{CBO}} )</td>
<td>collector-base cut-off current</td>
<td>( V_{\text{CB}} = 60 , \text{V}; I_{\text{E}} = 0 , \text{A}; T_{\text{amb}} = 25 , \text{°C} )</td>
<td>-</td>
<td>-</td>
<td>10</td>
<td>nA</td>
</tr>
<tr>
<td>( I_{\text{EBO}} )</td>
<td>emitter-base cut-off current</td>
<td>( V_{\text{CB}} = 60 , \text{V}; I_{\text{E}} = 0 , \text{A}; T_{\text{j}} = 125 , \text{°C} )</td>
<td>-</td>
<td>-</td>
<td>10</td>
<td>µA</td>
</tr>
<tr>
<td>( V_{\text{CE}} )</td>
<td>collector-emitter saturation voltage</td>
<td>( I_{\text{C}} = 150 , \text{mA}; I_{\text{B}} = 15 , \text{mA}; \delta \leq 0.02; T_{\text{amb}} = 25 , \text{°C} )</td>
<td>-</td>
<td>-</td>
<td>300</td>
<td>mV</td>
</tr>
<tr>
<td>( V_{\text{BE}} )</td>
<td>base-emitter saturation voltage</td>
<td>( I_{\text{C}} = 500 , \text{mA}; I_{\text{B}} = 50 , \text{mA}; \delta \leq 0.02; T_{\text{amb}} = 25 , \text{°C} )</td>
<td>-</td>
<td>-</td>
<td>1</td>
<td>V</td>
</tr>
<tr>
<td>( t_{\text{d}} )</td>
<td>delay time</td>
<td>( I_{\text{C}} = 150 , \text{mA}; I_{\text{B}} = 15 , \text{mA}; I_{\text{B}} = 15 , \text{mA}; T_{\text{amb}} = 25 , \text{°C} )</td>
<td>-</td>
<td>-</td>
<td>10</td>
<td>ns</td>
</tr>
<tr>
<td>( t_{\text{rise}} )</td>
<td>rise time</td>
<td>( I_{\text{C}} = 150 , \text{mA}; I_{\text{B}} = 15 , \text{mA}; I_{\text{B}} = -15 , \text{mA}; T_{\text{amb}} = 25 , \text{°C} )</td>
<td>-</td>
<td>-</td>
<td>25</td>
<td>ns</td>
</tr>
<tr>
<td>( t_{\text{on}} )</td>
<td>turn-on time</td>
<td>( I_{\text{C}} = 500 , \text{mA}; I_{\text{B}} = 50 , \text{mA}; T_{\text{amb}} = 25 , \text{°C} )</td>
<td>-</td>
<td>-</td>
<td>35</td>
<td>ns</td>
</tr>
<tr>
<td>( t_{\text{st}} )</td>
<td>storage time</td>
<td>( I_{\text{C}} = 150 , \text{mA}; I_{\text{B}} = 15 , \text{mA}; T_{\text{amb}} = 25 , \text{°C} )</td>
<td>-</td>
<td>-</td>
<td>200</td>
<td>ns</td>
</tr>
<tr>
<td>( t_{\text{f}} )</td>
<td>fall time</td>
<td>( I_{\text{C}} = 150 , \text{mA}; I_{\text{B}} = 15 , \text{mA}; T_{\text{amb}} = 25 , \text{°C} )</td>
<td>-</td>
<td>-</td>
<td>60</td>
<td>ns</td>
</tr>
<tr>
<td>( t_{\text{off}} )</td>
<td>turn-off time</td>
<td>( I_{\text{C}} = 150 , \text{mA}; I_{\text{B}} = -15 , \text{mA}; T_{\text{amb}} = 25 , \text{°C} )</td>
<td>-</td>
<td>-</td>
<td>250</td>
<td>ns</td>
</tr>
<tr>
<td>( C_{\text{C}} )</td>
<td>collector capacitance</td>
<td>( V_{\text{CB}} = 10 , \text{V}; I_{\text{B}} = 0 , \text{A}; I_{\text{E}} = 0 , \text{A}; f = 1 , \text{MHz}; T_{\text{amb}} = 25 , \text{°C} )</td>
<td>-</td>
<td>-</td>
<td>8</td>
<td>pF</td>
</tr>
<tr>
<td>( C_{\text{E}} )</td>
<td>emitter capacitance</td>
<td>( V_{\text{EB}} = 500 , \text{mV}; I_{\text{C}} = 0 , \text{A}; f = 1 , \text{MHz}; T_{\text{amb}} = 25 , \text{°C} )</td>
<td>-</td>
<td>-</td>
<td>25</td>
<td>pF</td>
</tr>
<tr>
<td>( f_{\text{T}} )</td>
<td>transition frequency</td>
<td>( V_{\text{CE}} = 20 , \text{V}; I_{\text{C}} = 20 , \text{mA}; f = 100 , \text{MHz}; T_{\text{amb}} = 25 , \text{°C} )</td>
<td>300</td>
<td>-</td>
<td>-</td>
<td>MHz</td>
</tr>
</tbody>
</table>
40 V, 600 mA, double NPN switching transistor

### Symbol | Parameter | Conditions | Min | Typ | Max | Unit
--- | --- | --- | --- | --- | --- | ---
NF | noise figure | $V_{CE} = 5 \text{ V}; \quad I_C = 100 \mu\text{A}; \quad R_S = 1 \Omega; \quad f = 1 \text{ kHz}$ | - | - | 4 | dB

**Fig. 4.** DC current gain as a function of collector current; typical values

$V_{CE} = 10 \text{ V}$
1. $T_{amb} = 100 ^\circ \text{C}$
2. $T_{amb} = 25 ^\circ \text{C}$
3. $T_{amb} = -55 ^\circ \text{C}$

**Fig. 5.** Collector current as a function of collector-emitter voltage; typical values

**Fig. 6.** Base-emitter voltage as a function of collector current; typical values

$V_{CE} = 10 \text{ V}$
1. $T_{amb} = -55 ^\circ \text{C}$
2. $T_{amb} = 25 ^\circ \text{C}$
3. $T_{amb} = 150 ^\circ \text{C}$

**Fig. 7.** Base-emitter saturation voltage as a function of collector current; typical values

$I_C/I_B = 10$
1. $T_{amb} = -55 ^\circ \text{C}$
2. $T_{amb} = 25 ^\circ \text{C}$
3. $T_{amb} = 150 ^\circ \text{C}$
**Fig. 8.** Collector-emitter saturation voltage as a function of collector current; typical values

\[
\begin{align*}
I_C/I_B &= 20 \\
(1) \ T_{amb} &= 150 \ ^\circ C \\
(2) \ T_{amb} &= 25 \ ^\circ C \\
(3) \ T_{amb} &= -55 \ ^\circ C
\end{align*}
\]

**Fig. 9.** Collector-emitter saturation voltage as a function of collector current; typical values

\[
\begin{align*}
T_{amb} &= 25 \ ^\circ C \\
(1) \ I_C/I_B &= 100 \\
(2) \ I_C/I_B &= 50 \\
(3) \ I_C/I_B &= 10
\end{align*}
\]
11. Test information

**Fig. 10. BISS transistor switching time definition**

**Fig. 11. Test circuit for switching times**

11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.
12. Package outline

Fig. 12. Package outline TSSOP6 (SOT363)

13. Soldering

Fig. 13. Reflow soldering footprint for TSSOP6 (SOT363)
Fig. 14. Wave soldering footprint for TSSOP6 (SOT363)
## 14. Revision history

<table>
<thead>
<tr>
<th>Data sheet ID</th>
<th>Release date</th>
<th>Data sheet status</th>
<th>Change notice</th>
<th>Supersedes</th>
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<tbody>
<tr>
<td>PMBT2222AYS v.1</td>
<td>20150624</td>
<td>Product data sheet</td>
<td>-</td>
<td>-</td>
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</tbody>
</table>
15. Legal information

15.1 Data sheet status

<table>
<thead>
<tr>
<th></th>
<th></th>
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<tbody>
<tr>
<td>Objective [short] data sheet</td>
<td>Development</td>
<td>This document contains data from the objective specification for product development.</td>
</tr>
<tr>
<td>Preliminary [short] data sheet</td>
<td>Qualification</td>
<td>This document contains data from the preliminary specification.</td>
</tr>
<tr>
<td>Product [short] data sheet</td>
<td>Production</td>
<td>This document contains the product specification.</td>
</tr>
</tbody>
</table>

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term "short data sheet" is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

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PMBT2222AYS

40 V, 600 mA, double NPN switching transistor

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Product data sheet 24 June 2015
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