

# **PMBS3904-Q**

40 V, 100 mA NPN general-purpose transistor

7 October 2022

**Product data sheet** 

### 1. General description

NPN transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package. PNP complement: PMBS3906

### 2. Features and benefits

- Low current (max. 100 mA)
- Low voltage (max. 40 V)
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

### 3. Applications

- General-purpose switching and amplification
- Telephony and professional communication equipment

### 4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	40	V
I <sub>C</sub>	collector current		-	-	100	mA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 1 V; I <sub>C</sub> = 10 mA; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	100	-	300	

# 5. Pinning information

Table 2.	Pinning info	rmation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	С
2	E	emitter		Ĵ
3	С	collector		вК
				E
				aaa-027673
			SOT23	



### 6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PMBS3904-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	<u>SOT23</u>			

### 7. Marking

Table 4. Marking codes	
Type number	Marking code[1]
PMBS3904-Q	%O4

[1] % = placeholder for manufacturing site code

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter	-	60	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	40	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	6	V
I <sub>C</sub>	collector current		-	100	mA
I <sub>CM</sub>	peak collector current		-	200	mA
I <sub>BM</sub>	peak base current		-	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	-	250	mW
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-65	150	°C
T <sub>stg</sub>	storage temperature		-65	150	°C

### 9. Thermal characteristics

Table 6. Therma Symbol	al characteristics Parameter	Conditions		Min	Тур	Мах	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient		[1]	-	-	500	K/W

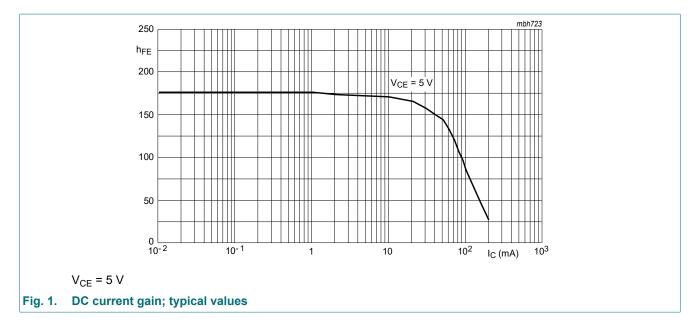
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

# **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off current	$V_{CB} = 30 \text{ V}; \text{ I}_{E} = 0 \text{ A}; \text{ T}_{amb} = 25 \text{ °C}$	-	-	50	nA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	50	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 1 V; I <sub>C</sub> = 0.1 mA; t <sub>p</sub> ≤ 300 µs; $\delta$ ≤ 0.02; T <sub>amb</sub> = 25 °C	40	-	-	
		$\label{eq:Vce} \begin{array}{l} V_{CE} = 1 \; V; \; I_{C} = 1 \; mA; \; t_{p} \leq \; 300 \; \mu s; \\ \delta \leq 0.02; \; T_{amb} = 25 \; ^{\circ} C \end{array}$	70	-	-	
		$V_{CE}$ = 1 V; I <sub>C</sub> = 10 mA; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	100	-	300	
		$V_{CE}$ = 1 V; I <sub>C</sub> = 50 mA; t <sub>p</sub> ≤ 300 µs; $\delta$ ≤ 0.02; T <sub>amb</sub> = 25 °C	60	-	-	
		$V_{CE} = 1 \text{ V}; I_C = 100 \text{ mA}; t_p \le 300 \mu\text{s}; \\ \delta \le 0.02; T_{amb} = 25 ^\circ\text{C}$	30	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 1 mA; T <sub>amb</sub> = 25 °C	-	-	200	mV
		I <sub>C</sub> = 50 mA; I <sub>B</sub> = 5 mA; T <sub>amb</sub> = 25 °C	-	-	300	mV
V <sub>BEsat</sub>	/ <sub>BEsat</sub> base-emitter saturation	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 1 mA; T <sub>amb</sub> = 25 °C	650	-	850	mV
	voltage	I <sub>C</sub> = 50 mA; I <sub>B</sub> = 5 mA; T <sub>amb</sub> = 25 °C	-	-	950	mV
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 5 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	-	4	pF
C <sub>e</sub>	emitter capacitance	$V_{EB} = 0.5 \text{ V}; \text{ I}_{C} = 0 \text{ A}; \text{ i}_{c} = 0 \text{ A};$ f = 1 MHz; $T_{amb} = 25 \text{ °C}$	-	-	12	pF
f <sub>T</sub>	transition frequency	$V_{CE} = 20 \text{ V}; \text{ I}_{C} = 10 \text{ mA}; \text{ f} = 100 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	180	-	-	MHz
NF	noise figure	$V_{CE}$ = 5 V; I <sub>C</sub> = 100 μA; R <sub>S</sub> = 1 kΩ; f = 10 Hz to 15.7 kHz; T <sub>amb</sub> = 25 °C	-	-	5	dB
Switching t	imes (between 10% and 90	% levels);				
t <sub>d</sub>	delay time	I <sub>C</sub> = 10 mA; I <sub>Bon</sub> = 1 mA; I <sub>Boff</sub> = -1 mA;	-	-	50	ns
t <sub>r</sub>	rise time	V <sub>CC</sub> = 3 V; T <sub>amb</sub> = 25 °C; V <sub>BB</sub> = -1.9 V	-	-	60	ns
t <sub>on</sub>	turn-on time		-	-	110	ns
t <sub>s</sub>	storage time	1 – – – – – – – – – – – – – – – – – – –	-	-	1000	ns
t <sub>f</sub>	fall time		-	-	200	ns
t <sub>off</sub>	turn-off time	1	-	-	1200	ns

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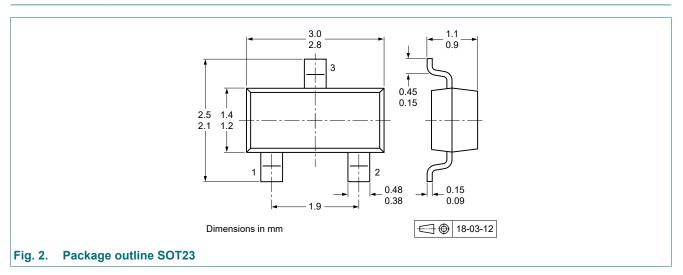


### **11. Test information**

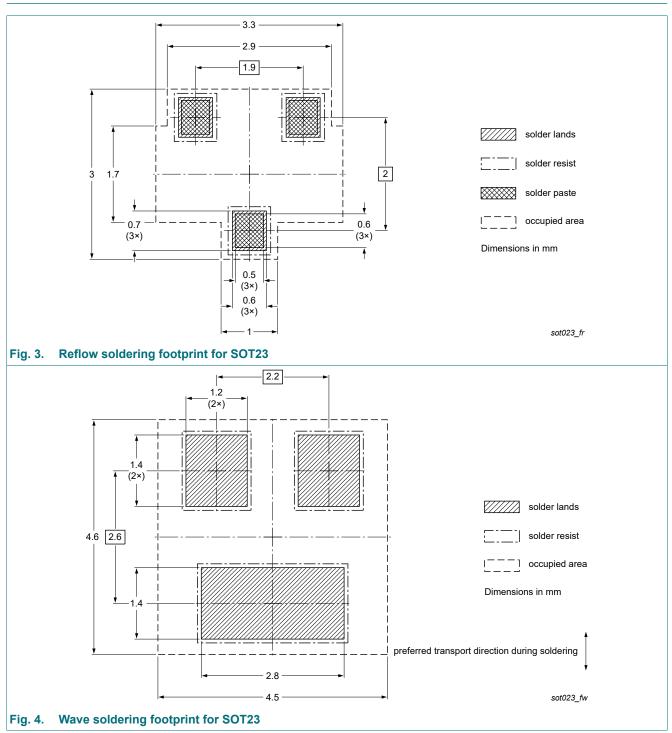
### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

### **12.** Package outline



### 13. Soldering



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# 14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMBS3904-Q v.1	20221007	Product data sheet	-	-		

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# 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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