

PMBS3904-Q

40 V, 100 mA NPN general-purpose transistor

7 October 2022

Product data sheet

1. General description

NPN transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package. PNP complement: PMBS3906

2. Features and benefits

- Low current (max. 100 mA)
- Low voltage (max. 40 V)
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- General-purpose switching and amplification
- Telephony and professional communication equipment

4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	40	V
I _C	collector current		-	-	100	mA
h _{FE}	DC current gain	V_{CE} = 1 V; I _C = 10 mA; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	100	-	300	

5. Pinning information

Table 2.	Pinning info	rmation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	С
2	E	emitter		Ĵ
3	С	collector		вК
				E
				aaa-027673
			SOT23	



6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PMBS3904-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	<u>SOT23</u>			

7. Marking

Table 4. Marking codes	
Type number	Marking code[1]
PMBS3904-Q	%O4

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter	-	60	V
V _{CEO}	collector-emitter voltage	open base	-	40	V
V _{EBO}	emitter-base voltage	open collector	-	6	V
I _C	collector current		-	100	mA
I _{CM}	peak collector current		-	200	mA
I _{BM}	peak base current		-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	-	250	mW
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-65	150	°C
T _{stg}	storage temperature		-65	150	°C

9. Thermal characteristics

Table 6. Therma Symbol	al characteristics Parameter	Conditions		Min	Тур	Мах	Unit
R _{th(j-a)}	thermal resistance from junction to ambient		[1]	-	-	500	K/W

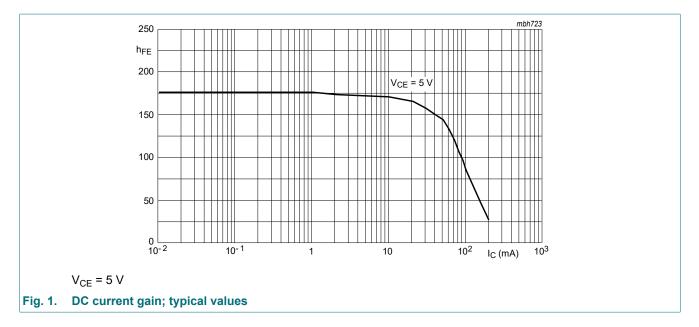
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	$V_{CB} = 30 \text{ V}; \text{ I}_{E} = 0 \text{ A}; \text{ T}_{amb} = 25 \text{ °C}$	-	-	50	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	50	nA
h _{FE}	DC current gain	V_{CE} = 1 V; I _C = 0.1 mA; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C	40	-	-	
		$\label{eq:Vce} \begin{array}{l} V_{CE} = 1 \; V; \; I_{C} = 1 \; mA; \; t_{p} \leq \; 300 \; \mu s; \\ \delta \leq 0.02; \; T_{amb} = 25 \; ^{\circ} C \end{array}$	70	-	-	
		V_{CE} = 1 V; I _C = 10 mA; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	100	-	300	
		V_{CE} = 1 V; I _C = 50 mA; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C	60	-	-	
		$V_{CE} = 1 \text{ V}; I_C = 100 \text{ mA}; t_p \le 300 \mu\text{s}; \\ \delta \le 0.02; T_{amb} = 25 ^\circ\text{C}$	30	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 10 mA; I _B = 1 mA; T _{amb} = 25 °C	-	-	200	mV
		I _C = 50 mA; I _B = 5 mA; T _{amb} = 25 °C	-	-	300	mV
V _{BEsat}	/ _{BEsat} base-emitter saturation	I _C = 10 mA; I _B = 1 mA; T _{amb} = 25 °C	650	-	850	mV
	voltage	I _C = 50 mA; I _B = 5 mA; T _{amb} = 25 °C	-	-	950	mV
C _c	collector capacitance	V _{CB} = 5 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	4	pF
C _e	emitter capacitance	$V_{EB} = 0.5 \text{ V}; \text{ I}_{C} = 0 \text{ A}; \text{ i}_{c} = 0 \text{ A};$ f = 1 MHz; $T_{amb} = 25 \text{ °C}$	-	-	12	pF
f _T	transition frequency	$V_{CE} = 20 \text{ V}; \text{ I}_{C} = 10 \text{ mA}; \text{ f} = 100 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	180	-	-	MHz
NF	noise figure	V_{CE} = 5 V; I _C = 100 μA; R _S = 1 kΩ; f = 10 Hz to 15.7 kHz; T _{amb} = 25 °C	-	-	5	dB
Switching t	imes (between 10% and 90	% levels);				
t _d	delay time	I _C = 10 mA; I _{Bon} = 1 mA; I _{Boff} = -1 mA;	-	-	50	ns
t _r	rise time	V _{CC} = 3 V; T _{amb} = 25 °C; V _{BB} = -1.9 V	-	-	60	ns
t _{on}	turn-on time		-	-	110	ns
t _s	storage time	1 – – – – – – – – – – – – – – – – – – –	-	-	1000	ns
t _f	fall time		-	-	200	ns
t _{off}	turn-off time	1	-	-	1200	ns

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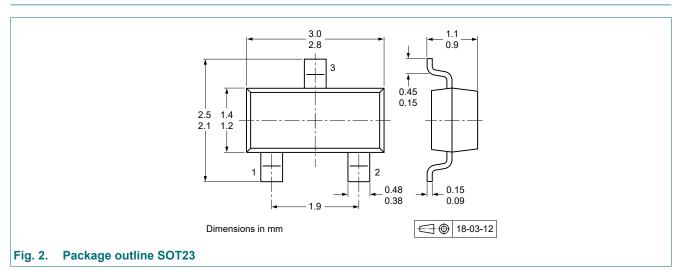


11. Test information

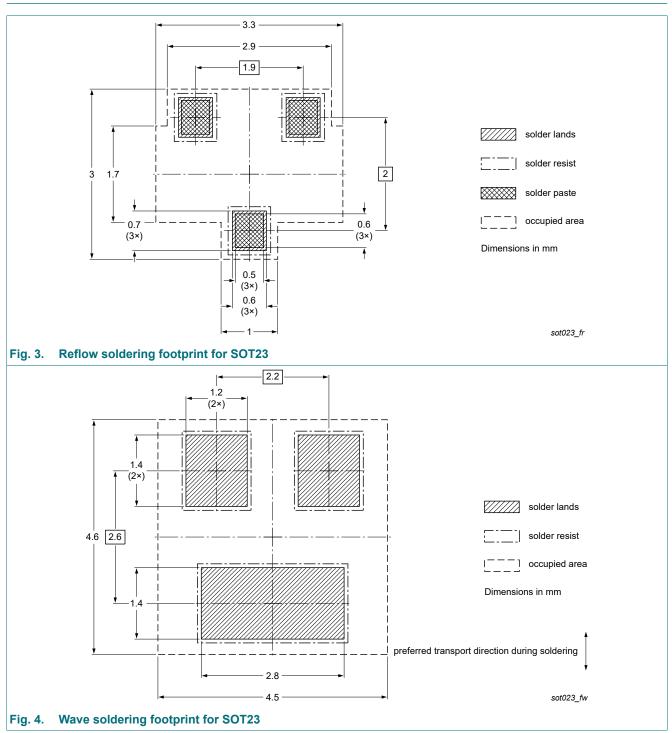
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMBS3904-Q v.1	20221007	Product data sheet	-	-		

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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