1. General description

Very low capacitance bidirectional ElectroStatic Discharge (ESD) protection diode in an SOD882 leadless ultra-small Surface-Mounted Device (SMD) plastic package designed to protect one signal line from the damage caused by ESD and other transients.

2. Features and benefits

- Bidirectional ESD protection of one line
- Very low diode capacitance: $C_d = 11\ pF$
- Max. peak pulse power: $P_{PPM} = 45\ W$
- Low clamping voltage: $V_{CL} = 12.5\ V$
- Ultra low leakage current: $I_{RM} < 1\ nA$
- ESD protection up to 30 kV
- IEC 61000-4-2; level 4 (ESD)
- IEC 61000-4-5 (surge); $I_{PPM} = 4.8\ A$
- AEC-Q101 qualified

3. Applications

- Computers and peripherals
- Audio and video equipment
- Cellular handsets and accessories
- SIM card protection
- Communication systems
- Portable electronics
- 10/100 Mbit/s Ethernet

4. Quick reference data

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{RWM}$</td>
<td>reverse standoff voltage</td>
<td>$T_{amb} = 25\ ^\circ C$</td>
<td>-</td>
<td>-</td>
<td>5</td>
<td>V</td>
</tr>
<tr>
<td>$C_d$</td>
<td>diode capacitance</td>
<td>$f = 1\ MHz; V_R = 0\ V; T_{amb} = 25\ ^\circ C$</td>
<td>-</td>
<td>11</td>
<td>13</td>
<td>pF</td>
</tr>
</tbody>
</table>
5. Pinning information

Table 2. Pinning information

<table>
<thead>
<tr>
<th>Pin</th>
<th>Symbol</th>
<th>Description</th>
<th>Simplified outline</th>
<th>Graphic symbol</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>K1</td>
<td>cathode (diode 1)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>K2</td>
<td>cathode (diode 2)</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

6. Ordering information

Table 3. Ordering information

<table>
<thead>
<tr>
<th>Type number</th>
<th>Package</th>
<th>Description</th>
<th>Version</th>
</tr>
</thead>
<tbody>
<tr>
<td>PESD5V0V1BL</td>
<td>DFN1006-2</td>
<td>plastic, leadless ultra small package; 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.48 mm body</td>
<td>SOD882</td>
</tr>
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</table>

7. Marking

Table 4. Marking codes

<table>
<thead>
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<th>Type number</th>
<th>Marking code</th>
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<tbody>
<tr>
<td>PESD5V0V1BL</td>
<td>X1</td>
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</table>
8. Limiting values

Table 5. Limiting values  
*In accordance with the Absolute Maximum Rating System (IEC 60134).*

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>P_{PPM}</td>
<td>rated peak pulse power</td>
<td>( t_p = 8/20 \mu s )</td>
<td>-</td>
<td>45</td>
<td>W</td>
</tr>
<tr>
<td>I_{PPM}</td>
<td>rated peak pulse current</td>
<td></td>
<td>-</td>
<td>4.8</td>
<td>A</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>T_j</td>
<td>junction temperature</td>
<td></td>
<td>-</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>T_{amb}</td>
<td>ambient temperature</td>
<td></td>
<td>-55</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>T_{stg}</td>
<td>storage temperature</td>
<td></td>
<td>-65</td>
<td>150</td>
<td>°C</td>
</tr>
</tbody>
</table>

**ESD maximum ratings**

<table>
<thead>
<tr>
<th>( V_{ESD} )</th>
<th>electrostatic discharge voltage</th>
<th>IEC 61000-4-2 (contact discharge)</th>
<th>[2]</th>
<th>30</th>
<th>kV</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>machine model</td>
<td></td>
<td>-</td>
<td>2</td>
<td>kV</td>
</tr>
<tr>
<td></td>
<td>MIL-STD-883 (human body model)</td>
<td></td>
<td>-</td>
<td>16</td>
<td>kV</td>
</tr>
</tbody>
</table>

[1] Non-repetitive current pulse 8/20 µs exponentially decaying waveform according to IEC 61000-4-5


---

**Fig. 1.** 8/20 µs pulse waveform according to IEC 61000-4-5

**Fig. 2.** ESD pulse waveform according to IEC 61000-4-2
9. Characteristics

Table 6. Characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
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<th>Typ</th>
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<tr>
<td>$V_{RWM}$</td>
<td>reverse standoff voltage</td>
<td>$T_{amb} = 25 \ ^\circ C$</td>
<td>-</td>
<td>-</td>
<td>5</td>
<td>V</td>
</tr>
<tr>
<td>$V_{BR}$</td>
<td>breakdown voltage</td>
<td></td>
<td>5.8</td>
<td>6.8</td>
<td>7.8</td>
<td>V</td>
</tr>
<tr>
<td>$I_{RM}$</td>
<td>reverse leakage current</td>
<td>$V_{RWM} = 5 \ V; T_{amb} = 25 \ ^\circ C$</td>
<td>-</td>
<td>1</td>
<td>10</td>
<td>nA</td>
</tr>
<tr>
<td>$C_d$</td>
<td>diode capacitance</td>
<td>$f = 1 \ \text{MHz}; V_R = 0 \ \text{V}; T_{amb} = 25 \ ^\circ C$</td>
<td>-</td>
<td>11</td>
<td>13</td>
<td>pF</td>
</tr>
<tr>
<td>$V_{CL}$</td>
<td>clamping voltage</td>
<td>$I_{PP} = 4.8 \ \text{A}; T_{amb} = 25 \ ^\circ C$</td>
<td>-</td>
<td>-</td>
<td>12.5</td>
<td>V</td>
</tr>
<tr>
<td>$R_{dyn}$</td>
<td>dynamic resistance</td>
<td>$I_R = 10 \ \text{A}; T_{amb} = 25 \ ^\circ C$</td>
<td>-</td>
<td>0.2</td>
<td>-</td>
<td>Ω</td>
</tr>
<tr>
<td>$r_{dif}$</td>
<td>differential resistance</td>
<td>$I_R = 5 \ \text{mA}; T_{amb} = 25 \ ^\circ C$</td>
<td>-</td>
<td>-</td>
<td>35</td>
<td>Ω</td>
</tr>
</tbody>
</table>

[1] Non-repetitive current pulse 8/20 μs exponential decay waveform according to IEC 61000-4-5.
[2] Non-repetitive current pulse, Transmission Line Pulse (TLP) $t_p = 100 \ \text{ns}$; square pulse; ANSI/ESD STM5.5.1-2008

Fig. 3. Peak pulse power as a function of exponential pulse duration; typical values

Fig. 4. Relative variation of peak pulse power as a function of junction temperature; typical values
Very low capacitance bidirectional ESD protection diode

**Fig. 5.** Diode capacitance as a function of reverse voltage; typical values

\[ C_d (\text{pF}) \]

\[ f = 1 \text{ MHz}; \; T_{\text{amb}} = 25 \; ^\circ \text{C} \]

**Fig. 6.** Relative variation of reverse leakage current as a function of junction temperature; typical values

\[ I_{\text{RM}} \]

\[ I_{\text{RM}}(25^\circ \text{C}) \]

\[ -100 \quad -50 \quad 0 \quad 50 \quad 100 \quad 150 \]

\[ T_j (^\circ \text{C}) \]

**Fig. 7.** V-I characteristics for a bidirectional ESD protection diode
Fig. 8. ESD clamping test setup and waveforms
10. Application information

The device is designed for the protection of one bidirectional data or signal line from the damage caused by ESD and/or other surge pulses. The device may be used on lines where the signal polarities are both, positive and negative with respect to ground. It provides a surge capability of 45 W per line for an 8/20 μs waveform.

Fig. 9. Application diagram

Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Avoid running protected conductors in parallel with unprotected conductors.
4. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
5. Minimize the length of the transient return path to ground.
6. Avoid using shared transient return paths to a common ground point.
7. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.
12. Package outline

Fig. 10. Package outline DFN1006-2 (SOD882)

13. Soldering

Fig. 11. Reflow soldering footprint for DFN1006-2 (SOD882)
14. Revision history

Table 7. Revision history

<table>
<thead>
<tr>
<th>Data sheet ID</th>
<th>Release date</th>
<th>Data sheet status</th>
<th>Change notice</th>
<th>Supersedes</th>
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<tr>
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<td>20180705</td>
<td>Product data sheet</td>
<td>-</td>
<td>PESD5V0V1BA_BB_BL v.2</td>
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Modifications:
- The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia
- Legal texts have been adapted to the new company name where appropriate
Product data sheet

5 July 2018

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Very low capacitance bidirectional ESD protection diode

Nexperia

15. Legal information

Data sheet status

<table>
<thead>
<tr>
<th>Document status</th>
<th>Product status</th>
<th>Definition</th>
</tr>
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<tbody>
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<td>Objective [short] data</td>
<td>Development</td>
<td>This document contains data from the objective specification for product</td>
</tr>
<tr>
<td>Preliminary [short]</td>
<td>Qualification</td>
<td>This document contains data from the preliminary specification.</td>
</tr>
<tr>
<td>Product [short] data</td>
<td>Production</td>
<td>This document contains the product specification.</td>
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