1. General description

Low capacitance bidirectional ElectroStatic Discharge (ESD) protection diode in an SOD882 leadless ultra-small plastic package, designed to protect one signal line from the damage caused by ESD and other transients.

2. Features and benefits

• Bidirectional ESD protection of one line
• Low diode capacitance: \( C_d = 2.9 \text{ pF} \)
• Ultra low leakage current: \( I_{RM} = 5 \text{ nA} \)
• ESD protection of up to 10 kV
• IEC 61000-4-2, level 4 (ESD)
• Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

• Computers and peripherals
• Audio and video equipment
• Cellular handsets and accessories
• 10/100/1000 Ethernet
• Local Area Network (LAN) equipment
• Communication systems
• Portable electronics
• SIM card protection
• High-speed data lines

4. Quick reference data

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_{RWM} )</td>
<td>reverse standoff voltage</td>
<td>( T_{amb} = 25 \text{ °C} )</td>
<td>-</td>
<td>-</td>
<td>5</td>
<td>V</td>
</tr>
<tr>
<td>( C_d )</td>
<td>diode capacitance</td>
<td>( f = 1 \text{ MHz}; V_R = 0 \text{ V}; T_{amb} = 25 \text{ °C} )</td>
<td>-</td>
<td>2.9</td>
<td>3.5</td>
<td>pF</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( f = 1 \text{ MHz}; V_R = 5 \text{ V}; T_{amb} = 25 \text{ °C} )</td>
<td>-</td>
<td>1.9</td>
<td>-</td>
<td>pF</td>
</tr>
</tbody>
</table>
5. Pinning information

Table 2. Pinning information

<table>
<thead>
<tr>
<th>Pin</th>
<th>Symbol</th>
<th>Description</th>
<th>Simplified outline</th>
<th>Graphic symbol</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>K1</td>
<td>cathode (diode 1)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>K2</td>
<td>cathode (diode 2)</td>
<td></td>
<td>DFN1006-2 (SOD882)</td>
</tr>
</tbody>
</table>

6. Ordering information

Table 3. Ordering information

<table>
<thead>
<tr>
<th>Type number</th>
<th>Package</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>PESD5V0U1BL-Q</td>
<td>DFN1006-2</td>
<td>plastic, leadless ultra small package; 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.48 mm body</td>
</tr>
</tbody>
</table>

7. Marking

Table 4. Marking codes

<table>
<thead>
<tr>
<th>Type number</th>
<th>Marking code</th>
</tr>
</thead>
<tbody>
<tr>
<td>PESD5V0U1BL-Q</td>
<td>AN</td>
</tr>
</tbody>
</table>
8. Limiting values

Table 5. Limiting values

*In accordance with the Absolute Maximum Rating System (IEC 60134).*

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$T_j$</td>
<td>junction temperature</td>
<td>-</td>
<td>150</td>
<td>°C</td>
<td></td>
</tr>
<tr>
<td>$T_{amb}$</td>
<td>ambient temperature</td>
<td>-55</td>
<td>150</td>
<td>°C</td>
<td></td>
</tr>
<tr>
<td>$T_{stg}$</td>
<td>storage temperature</td>
<td>-65</td>
<td>150</td>
<td>°C</td>
<td></td>
</tr>
</tbody>
</table>

**ESD maximum ratings**

<table>
<thead>
<tr>
<th>$V_{ESD}$</th>
<th>electrostatic discharge voltage</th>
<th>IEC 61000-4-2; contact discharge</th>
<th>[1] [2]</th>
<th>-</th>
<th>10</th>
<th>kV</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>MIL-STD-883; human body model (HBM)</td>
<td>-</td>
<td>8</td>
<td>kV</td>
<td></td>
</tr>
</tbody>
</table>


---

**Fig. 1.** ESD pulse waveform according to IEC 61000-4-2
## 9. Characteristics

### Table 6. Characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{RWM}$</td>
<td>reverse standoff voltage</td>
<td>$T_{amb} = 25 , ^\circ C$</td>
<td></td>
<td></td>
<td>5</td>
<td>V</td>
</tr>
<tr>
<td>$V_{BR}$</td>
<td>breakdown voltage</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>$I_{RM}$</td>
<td>reverse leakage current</td>
<td>$V_{RWM} = 5 , V; , T_{amb} = 25 , ^\circ C$</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>$C_d$</td>
<td>diode capacitance</td>
<td>$f = 1 , MHz; , V_R = 0 , V; , T_{amb} = 25 , ^\circ C$</td>
<td></td>
<td>2.9</td>
<td>3.5</td>
<td>pF</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$f = 1 , MHz; , V_R = 5 , V; , T_{amb} = 25 , ^\circ C$</td>
<td></td>
<td>1.9</td>
<td></td>
<td>pF</td>
</tr>
<tr>
<td>$R_{diff}$</td>
<td>differential resistance</td>
<td>$I_R = 1 , mA; , T_{amb} = 25 , ^\circ C$</td>
<td></td>
<td></td>
<td>100</td>
<td>Ω</td>
</tr>
</tbody>
</table>

![Fig. 2. Diode capacitance as a function of reverse voltage; typical values](image1)

![Fig. 3. V-I characteristics for a bidirectional ESD protection diode](image2)
Fig. 4. ESD clamping test setup and waveforms
10. Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.

![Application diagram](aaa-002737)

**Fig. 5. Application diagram**

Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Test information

**Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.
12. Package outline

DFN1006-2: Plastic, leadless ultra small package; 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.48 mm body

Dimensions (mm are the original dimensions)

<table>
<thead>
<tr>
<th>Unit</th>
<th>A(1)</th>
<th>A₁</th>
<th>b</th>
<th>D</th>
<th>e₁</th>
<th>L</th>
</tr>
</thead>
<tbody>
<tr>
<td>max</td>
<td>0.50</td>
<td>0.03</td>
<td>0.55</td>
<td>0.62</td>
<td>1.02</td>
<td>0.30</td>
</tr>
<tr>
<td>min</td>
<td>0.46</td>
<td>0.47</td>
<td>0.55</td>
<td>0.95</td>
<td>0.22</td>
<td></td>
</tr>
</tbody>
</table>

Note
1. Including plating thickness
2. The marking bar indicates the cathode (if applicable)

Fig. 6. Package outline DFN1006-2 (SOD882)
13. Soldering

![Reflow soldering footprint for DFN1006-2 (SOD882)](sod882.png)

Fig. 7. Reflow soldering footprint for DFN1006-2 (SOD882)
### 14. Revision history

Table 7. Revision history

<table>
<thead>
<tr>
<th>Data sheet ID</th>
<th>Release date</th>
<th>Data sheet status</th>
<th>Change notice</th>
<th>Supersedes</th>
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<tr>
<td>PESD5V0U1BL-Q v.1</td>
<td>20230607</td>
<td>Product data sheet</td>
<td>-</td>
<td>-</td>
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</tbody>
</table>
15. Legal information

Data sheet status

<table>
<thead>
<tr>
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<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Objective (short) data sheet</td>
<td>Development</td>
<td>This document contains data from the objective specification for product development.</td>
</tr>
<tr>
<td>Preliminary (short) data sheet</td>
<td>Qualification</td>
<td>This document contains data from the preliminary specification.</td>
</tr>
<tr>
<td>Product (short) data sheet</td>
<td>Production</td>
<td>This document contains the product specification.</td>
</tr>
</tbody>
</table>

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