1. General description

Bidirectional ElectroStatic Discharge (ESD) protection diode designed to protect one signal line from the damage caused by ESD and other transients.

The device is housed in a SOD882D leadless ultra small Surface-Mounted Device (SMD) plastic package with side-wettable flanks.

2. Features and benefits

- Bidirectional ESD protection of one line
- Ultra small SMD plastic package 1 x 0.6 x 0.37 mm
- Side-wettable flanks
- ESD protection up to 30 kV
- Very high surge robustness; $I_{PP} = 12$ A for 8/20 µs; average measured
- AEC-Q101 qualified

3. Applications

- ESD and surge protection for interface lines

4. Quick reference data

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{RWM}$</td>
<td>reverse standoff voltage</td>
<td>$T_{amb} = 25$ °C</td>
<td>-</td>
<td>-</td>
<td>3.3</td>
<td>V</td>
</tr>
<tr>
<td>$I_{PPM}$</td>
<td>rated peak pulse current</td>
<td>$t_p = 8/20$ µs</td>
<td>[1]</td>
<td>-</td>
<td>10</td>
<td>A</td>
</tr>
<tr>
<td>$V_{CL}$</td>
<td>clamping voltage</td>
<td>$I_{PPM} = 10$ A; $t_p = 8/20$ µs; $T_{amb} = 25$ °C</td>
<td>[1]</td>
<td>9.3</td>
<td>11</td>
<td>V</td>
</tr>
</tbody>
</table>

[1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.

5. Pinning information

<table>
<thead>
<tr>
<th>Pin</th>
<th>Symbol</th>
<th>Description</th>
<th>Simplified outline</th>
<th>Graphic symbol</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>K1</td>
<td>cathode 1[1]</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>K2</td>
<td>cathode 2</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

[1] The marking band indicates the cathode
6. Ordering information

Table 3. Ordering information

<table>
<thead>
<tr>
<th>Type number</th>
<th>Package</th>
<th>Name</th>
<th>Description</th>
<th>Version</th>
</tr>
</thead>
<tbody>
<tr>
<td>PESD3V3T1BLD</td>
<td>DFN1006D-2</td>
<td>leadless ultra small plastic package; 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.4 mm body</td>
<td>SOD882D</td>
<td></td>
</tr>
</tbody>
</table>

7. Marking

Table 4. Marking codes

<table>
<thead>
<tr>
<th>Type number</th>
<th>Marking code</th>
</tr>
</thead>
<tbody>
<tr>
<td>PESD3V3T1BLD</td>
<td>0110 0100</td>
</tr>
</tbody>
</table>

Fig. 1. SOD882D binary marking code description
8. Limiting values

Table 5. Limiting values

*In accordance with the Absolute Maximum Rating System (IEC 60134).*

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter Description</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>I&lt;sub&gt;PPM&lt;/sub&gt;</td>
<td>rated peak pulse current</td>
<td>I&lt;sub&gt;p&lt;/sub&gt; = 8/20 µs</td>
<td>[1] -</td>
<td>10</td>
<td>A</td>
</tr>
<tr>
<td>T&lt;sub&gt;j&lt;/sub&gt;</td>
<td>junction temperature</td>
<td>-</td>
<td>-55</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>T&lt;sub&gt;amb&lt;/sub&gt;</td>
<td>ambient temperature</td>
<td>-</td>
<td>-65</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>T&lt;sub&gt;stg&lt;/sub&gt;</td>
<td>storage temperature</td>
<td>-</td>
<td>-65</td>
<td>150</td>
<td>°C</td>
</tr>
</tbody>
</table>

**ESD maximum ratings**

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter Description</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>V&lt;sub&gt;ESD&lt;/sub&gt;</td>
<td>electrostatic discharge voltage</td>
<td>IEC 61000-4-2 (contact discharge)</td>
<td>[2] -</td>
<td>30</td>
<td>kV</td>
</tr>
</tbody>
</table>

[1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.

### 9. Characteristics

#### Table 6. Characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{RWM}$</td>
<td>reverse standoff voltage</td>
<td>$T_{amb} = 25 , ^\circ C$</td>
<td>-</td>
<td>-</td>
<td>3.3</td>
<td>V</td>
</tr>
<tr>
<td>$V_{BR}$</td>
<td>breakdown voltage</td>
<td>$I_R = 5 , mA; \ T_{amb} = 25 , ^\circ C$</td>
<td>4.7</td>
<td>5.5</td>
<td>8.7</td>
<td>V</td>
</tr>
<tr>
<td>$I_{RM}$</td>
<td>reverse leakage current</td>
<td>$V_{RWM} = 3.3 , V; \ T_{amb} = 25 , ^\circ C$</td>
<td>-</td>
<td>0.1</td>
<td>50</td>
<td>nA</td>
</tr>
<tr>
<td>$C_d$</td>
<td>diode capacitance</td>
<td>$f = 1 , MHz; \ V_R = 0 , V; \ T_{amb} = 25 , ^\circ C$</td>
<td>-</td>
<td>20</td>
<td>25</td>
<td>pF</td>
</tr>
<tr>
<td>$V_{CL}$</td>
<td>clamping voltage</td>
<td>$I_{pp} = 1 , A; \ t_p = 8/20 , \mu s; \ T_{amb} = 25 , ^\circ C$</td>
<td>[1]</td>
<td>6.5</td>
<td>-</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{PPM} = 10 , A; \ t_p = 8/20 , \mu s; \ T_{amb} = 25 , ^\circ C$</td>
<td>[1]</td>
<td>9.3</td>
<td>11</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{pp} = 16 , A; \ t_p = 100 , \text{ns}; \ T_{amb} = 25 , ^\circ C$</td>
<td>[2]</td>
<td>9.5</td>
<td>-</td>
<td>V</td>
</tr>
<tr>
<td>$R_{dyn}$</td>
<td>dynamic resistance</td>
<td>$I_R = 10 , A; \ T_{amb} = 25 , ^\circ C$</td>
<td>[2]</td>
<td>-</td>
<td>0.12</td>
<td>Ω</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_R = -10 , A; \ T_{amb} = 25 , ^\circ C$</td>
<td>[2]</td>
<td>-</td>
<td>0.21</td>
<td>Ω</td>
</tr>
</tbody>
</table>

[1] Non-repetitive current pulse 8/20 μs exponential decay waveform according to IEC 61000-4-5.
[2] Non-repetitive current pulse, Transmission Line Pulse (TLP) $t_p = 100 \, \text{ns}$; square pulse; ANSI/ESD STM5.5.1-2008.

![Fig. 4. V-I characteristics for a bidirectional ESD protection diode](image1)

![Fig. 5. Diode capacitance as a function of reverse voltage; typical values](image2)
**Bidirectional ESD protection diode**

**Fig. 6.** Positive clamping voltage (TLP); typical values

**Fig. 7.** Negative clamping voltage (TLP); typical values

**Fig. 8.** Positive clamping voltage (8/20 μs pulse); typical values

**Fig. 9.** Negative clamping voltage (8/20 μs pulse); typical values
Bidirectional ESD protection diode

IEC 61000-4-2 ed.2
$C_s = 150 \text{ pF; } R_d = 330 \Omega$

DUT (DEVICE UNDER TEST)

ESD TESTER

RG 223/U
50 Ω coax

4 GHz DIGITAL OSCILLOSCOPE

50 Ω

ATTENUATOR

$V_{CL}$ (V)

$V_{CL}$ at 30 nA = 8.1 V

$V_{CL}$ at 30 nA = -8.1 V

Fig. 10. ESD clamping test setup and waveforms

Fig. 11. Clamped +8 kV pulse waveform (IEC 61000-4-2 network)

Fig. 12. Clamped -8 kV pulse waveform (IEC 61000-4-2 network)
10. Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.

![Application diagram](image)

Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.
12. Package outline

DFN1006D-2: Leadless ultra small plastic package; 2 terminals; body 1 x 0.6 x 0.4 mm

SOD882D

---

<table>
<thead>
<tr>
<th>Dimensions</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Unit</td>
<td>A ((1))</td>
</tr>
<tr>
<td>mm max</td>
<td>0.4</td>
</tr>
<tr>
<td>mm nom</td>
<td>0.50</td>
</tr>
<tr>
<td>mm min</td>
<td>0.45</td>
</tr>
</tbody>
</table>

**Note:**
1. Dimension including plating thickness.
2. The marking bar indicates the cathode (if applicable).

---

**Fig. 14. Package outline DFN1006D-2 (SOD882D)**

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13. Soldering

Fig. 15. Reflow soldering footprint for DFN1006D-2 (SOD882D)
14. Revision history

Table 7. Revision history

<table>
<thead>
<tr>
<th>Data sheet ID</th>
<th>Release date</th>
<th>Data sheet status</th>
<th>Change notice</th>
<th>Supersedes</th>
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<tr>
<td>PESD3V3T1BLD v.1</td>
<td>20181121</td>
<td>Product data sheet</td>
<td>-</td>
<td>-</td>
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</tbody>
</table>
15. Legal information

Data sheet status

<table>
<thead>
<tr>
<th>Document status</th>
<th>Product status</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>Preliminary [short] data sheet</td>
<td>Qualification</td>
<td>This document contains data from the preliminary specification.</td>
</tr>
<tr>
<td>Objective [short] data sheet</td>
<td>Development</td>
<td>This document contains data from the objective specification for product development.</td>
</tr>
<tr>
<td>Product [short] data sheet</td>
<td>Production</td>
<td>This document contains the product specification.</td>
</tr>
</tbody>
</table>

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term “short data sheet” is explained in section “Definitions”.
[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com.

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