

Fivefold ESD protection diode array

15 May 2023

## 1. General description

Fivefold ElectroStatic Discharge (ESD) protection diode array in a SOT457 (SC-74) small Surface-Mounted Device (SMD) plastic package, designed to protect up to five signal lines from the damage caused by ESD and other transients.

## 2. Features and benefits

- · ESD protection of up to five lines
- Max peak pulse power: P<sub>PPM</sub> = 200 W
- Ultra low leakage current: I<sub>RM</sub> = 300 nA
- Low clamping voltage: V<sub>CL</sub> = 12 V at I<sub>PPM</sub> = 20 A
- ESD protection up to 30 kV
- IEC 61000-4-2; level 4 (ESD)
- IEC 61000-4-5 (surge); I<sub>PPM</sub> = 20 A
- Qualified according to AEC-Q101 and recommended for use in automotive applications

## 3. Applications

- Computers and peripherals
- Audio and video equipment
- Cellular handsets and accessories
- Communication systems
- Portable electronics
- SIM card protection

## 4. Quick reference data

| Table 1. Quick reference data |                             |   |  |     |     |     |      |
|-------------------------------|-----------------------------|---|--|-----|-----|-----|------|
| Symbol                        | Parameter                   | Conditions  |  | Min | Тур | Max | Unit |
| V <sub>RWM</sub>              | reverse standoff<br>voltage | T <sub>amb</sub> = 25 °C                                  |  | -   | -   | 3.3 | V    |
| C <sub>d</sub>                | diode capacitance           | f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C |  | -   | 215 | 300 | pF   |

# nexperia

# 5. Pinning information

| Pin | Symbol | Description  | Simplified outline    | Graphic symbol |
|-----|--------|--------------|-----------------------|----------------|
| 1   | K1     | cathode 1    |                       |                |
| 2   | CA     | common anode |                       | К1 [] К5       |
| 3   | K2     | cathode 2    |                       | СА К4          |
| 4   | K3     | cathode 3    |                       | K2 K3 K3       |
| 5   | K4     | cathode 4    | SC-74; TSOP6 (SOT457) | 006aaa159      |
| 6   | K5     | cathode 5    |                       |                |

# 6. Ordering information

## Table 3. Ordering information

| Type number   | Package      |   |         |  |
|---------------|--------------|---|---------|--|
|               | Name         | Description   | Version |  |
| PESD3V3S5UD-Q | SC-74; TSOP6 | plastic, surface-mounted package (SC-74; TSOP6); 6<br>leads | SOT457  |  |

## 7. Marking

# Table 4. Marking codes

| Type number   | Marking code |
|---------------|--------------|
| PESD3V3S5UD-Q | E1           |

# 8. Limiting values

#### Table 5. Limiting values

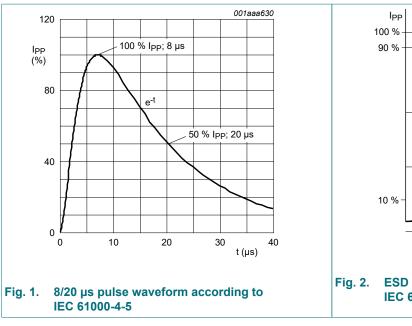
In accordance with the Absolute Maximum Rating System (IEC 60134).

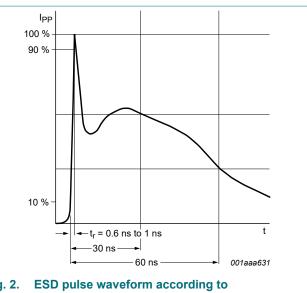
| Symbol           | Parameter                | Conditions                          |         | Min | Max | Unit |
|------------------|--------------------------|-------------------------------------|---------|-----|-----|------|
| P <sub>PPM</sub> | rated peak pulse power   | t <sub>p</sub> = 8/20 μs            | [1] [2] | -   | 200 | W    |
| I <sub>PPM</sub> | rated peak pulse current |                                     | [1] [2] | -   | 20  | А    |
| Tj               | junction temperature     |                                     |         | -   | 150 | °C   |
| T <sub>amb</sub> | ambient temperature      |                                     |         | -65 | 150 | °C   |
| T <sub>stg</sub> | storage temperature      |                                     |         | -65 | 150 | °C   |
| ESD maximum      | ratings                  |                                     |         |     |     |      |
| V <sub>ESD</sub> | electrostatic discharge  | IEC 61000-4-2; contact discharge    | [3] [2] | -   | 30  | kV   |
|                  | voltage                  | IEC 61000-4-2; air discharge        |         | -   | 15  | kV   |
|                  |                          | MIL-STD-883; human body model (HBM) |         | -   | 10  | kV   |

[1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.

[2] Measured from pin 1,3,4,5 or 6 to pin 2.

[3] Device stressed with ten non-repetitive ESD pulses.





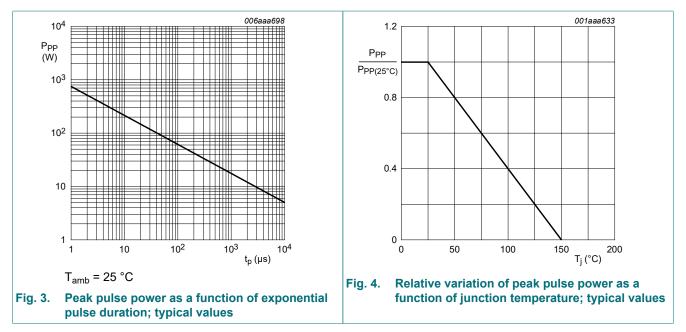
g. 2. ESD pulse waveform according IEC 61000-4-2

# 9. Characteristics

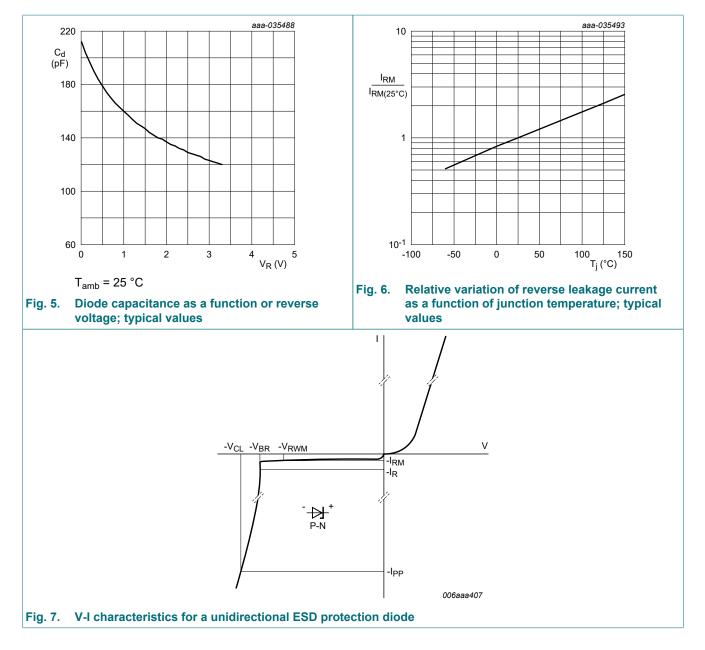
| Symbol            | Parameter                   | Conditions  |         | Min | Тур | Max | Unit |
|-------------------|-----------------------------|---|---------|-----|-----|-----|------|
| V <sub>RWM</sub>  | reverse standoff<br>voltage | T <sub>amb</sub> = 25 °C                                  |         | -   | -   | 3.3 | V    |
| V <sub>BR</sub>   | breakdown voltage           | I <sub>R</sub> = 1 mA; T <sub>amb</sub> = 25 °C           |         | 5.3 | 5.6 | 5.9 | V    |
| I <sub>RM</sub>   | reverse leakage current     | V <sub>RWM</sub> = 3.3 V; T <sub>amb</sub> = 25 °C        |         | -   | 300 | 800 | nA   |
| C <sub>d</sub>    | diode capacitance           | f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C |         | -   | 215 | 300 | pF   |
| V <sub>CL</sub>   | clamping voltage            | I <sub>PP</sub> = 1 A; T <sub>amb</sub> = 25 °C           | [1] [2] | -   | -   | 8   | V    |
|                   |                             | I <sub>PPM</sub> = 20 A; T <sub>amb</sub> = 25 °C         | [1] [2] | -   | -   | 12  | V    |
| R <sub>diff</sub> | differential resistance     | I <sub>R</sub> = 5 mA; T <sub>amb</sub> = 25 °C           |         | -   | -   | 25  | Ω    |

[1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.

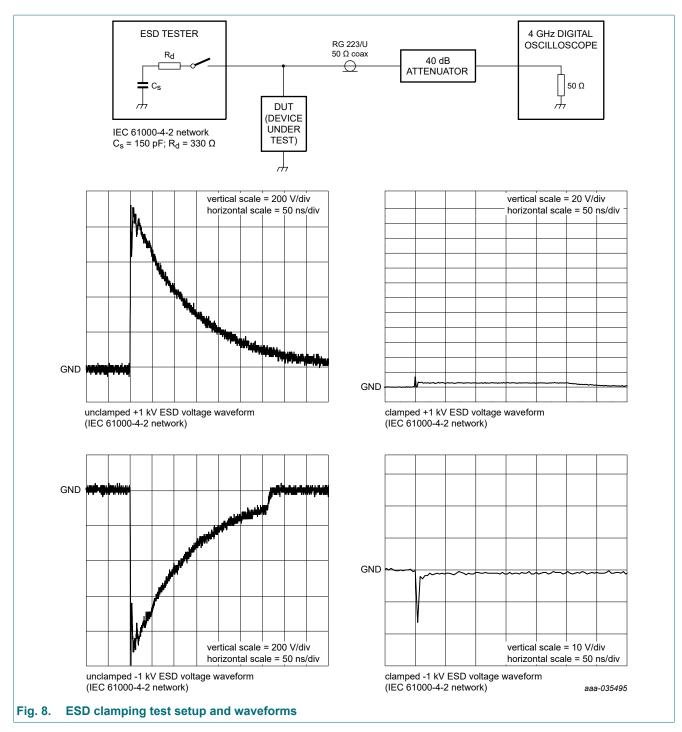
[2] Measured from pin 1,3,4,5 or 6 to pin 2.



## Fivefold ESD protection diode array

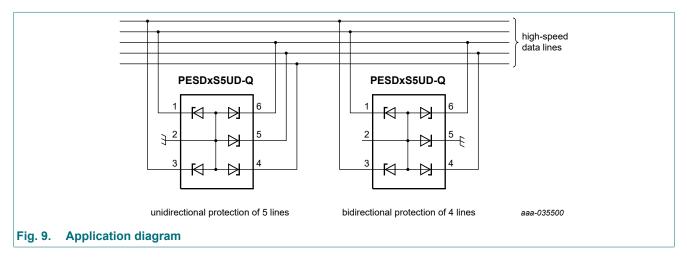


## Fivefold ESD protection diode array



# **10.** Application information

This device is designed for the protection of up to five unidirectional data lines from the damage caused by ESD and surge pulses. The device may be used on lines where the signal polarities are both, positive and negative with respect to ground.



### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- **3.** Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

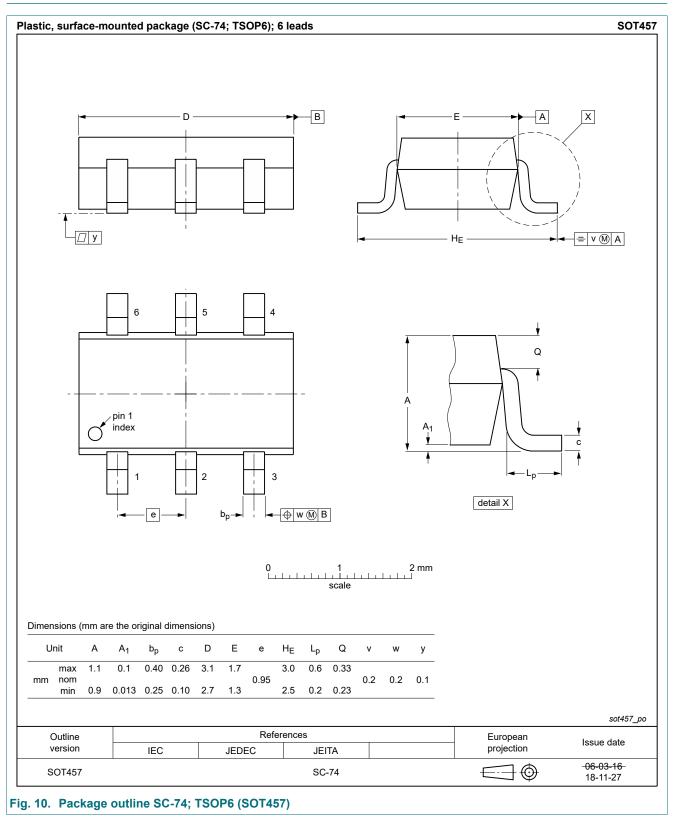
## 11. Test information

## **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

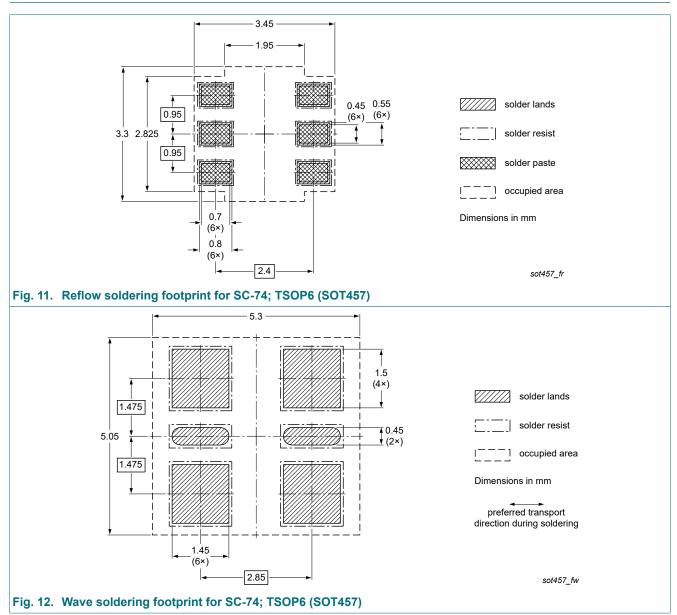
PESD3V3S5UD-Q

# 12. Package outline



## Fivefold ESD protection diode array

# 13. Soldering



## Fivefold ESD protection diode array

# 14. Revision history

| Table 7. Revision history |              |                    |               |            |  |  |
|---------------------------|--------------|--------------------|---------------|------------|--|--|
| Data sheet ID             | Release date | Data sheet status  | Change notice | Supersedes |  |  |
| PESD3V3S5UD-Q v.1         | 20230515     | Product data sheet | -             | -          |  |  |

PESD3V3S5UD-Q

# 15. Legal information

#### Data sheet status

| Document status<br>[1][2]         | Product<br>status [3] | Definition  |
|-----------------------------------|-----------------------|---|
| Objective [short]<br>data sheet   | Development           | This document contains data from<br>the objective specification for<br>product development. |
| Preliminary [short]<br>data sheet | Qualification         | This document contains data from the preliminary specification.                             |
| Product [short]<br>data sheet     | Production            | This document contains the product specification.   |

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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