1. General description

Uni-directional double ESD protection diode in a SOT23 plastic package. Designed to protect up to two transmission or data lines from ElectroStatic Discharge (ESD) damage.

2. Features and benefits

- Uni-directional ESD protection of up to two lines
- Max. peak pulse power: \( P_{pp} = 330 \text{ W} \) at \( t_p = 8/20 \text{ us} \)
- Low clamping voltage: \( V_{CL,IR} = 20 \text{ V} \) at \( I_{pp} = 18 \text{ A} \)
- Ultra-low reverse leakage current: \( I_{RM} < 700 \text{ nA} \)
- ESD protection > 30 kV
- IEC 61000-4-2; level 4 (ESD)
- IEC 61000-4-5 (surge); \( I_{pp} = 18 \text{ A} \) at \( t_p = 8/20 \text{ us.} \)
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Computers and peripherals
- Communication systems
- Audio and video equipment
- High speed data lines
- Parallel ports

4. Quick reference data

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_{RWM} )</td>
<td>reverse standoff voltage</td>
<td>( T_{amb} = 25 \text{ °C} )</td>
<td>[1]</td>
<td>-</td>
<td>3.3</td>
<td>V</td>
</tr>
<tr>
<td>( C_d )</td>
<td>diode capacitance</td>
<td>( f = 1 \text{ MHz}; V_R = 0 \text{ V}; T_{amb} = 25 \text{ °C} )</td>
<td>[1]</td>
<td>-</td>
<td>207</td>
<td>300 pF</td>
</tr>
</tbody>
</table>

[1] Measured from pin 1 or 2 to pin 3.
5. Pinning information

Table 2. Pinning information

<table>
<thead>
<tr>
<th>Pin</th>
<th>Symbol</th>
<th>Description</th>
<th>Simplified outline</th>
<th>Graphic symbol</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>K1</td>
<td>cathode (diode 1)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>K2</td>
<td>cathode (diode 2)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>A</td>
<td>common anode</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

6. Ordering information

Table 3. Ordering information

<table>
<thead>
<tr>
<th>Type number</th>
<th>Package</th>
<th>Name</th>
<th>Description</th>
<th>Version</th>
</tr>
</thead>
<tbody>
<tr>
<td>PESD3V3S2UT-Q</td>
<td>SOT23</td>
<td>plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body</td>
<td>SOT23</td>
<td></td>
</tr>
</tbody>
</table>

7. Marking

Table 4. Marking codes

<table>
<thead>
<tr>
<th>Type number</th>
<th>Marking code[1]</th>
</tr>
</thead>
<tbody>
<tr>
<td>PESD3V3S2UT-Q</td>
<td>%U9</td>
</tr>
</tbody>
</table>

[1] % = placeholder for manufacturing site code
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$P_{PPM}$</td>
<td>rated peak pulse power</td>
<td>$I_p = 8/20 \mu s$</td>
<td>[1] [2]</td>
<td>-</td>
<td>330 W</td>
</tr>
<tr>
<td>$I_{PPM}$</td>
<td>rated peak pulse current</td>
<td>[1] [2]</td>
<td>-</td>
<td>18 A</td>
<td></td>
</tr>
<tr>
<td>$T_j$</td>
<td>junction temperature</td>
<td>-</td>
<td>150 °C</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$T_{amb}$</td>
<td>ambient temperature</td>
<td>-65</td>
<td>150 °C</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$T_{stg}$</td>
<td>storage temperature</td>
<td>-65</td>
<td>150 °C</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

ESD maximum ratings

<table>
<thead>
<tr>
<th>$V_{ESD}$</th>
<th>electrostatic discharge voltage</th>
<th>IEC 61000-4-2 (contact discharge)</th>
<th>[3] [2]</th>
<th>-</th>
<th>30 kV</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>IEC 61000-4-2 (air discharge)</td>
<td>[3] [2]</td>
<td>-</td>
<td>15 kV</td>
<td></td>
</tr>
<tr>
<td></td>
<td>MIL-STD-883 (human body model)</td>
<td>-</td>
<td>10 kV</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

[1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.
[2] Measured from pin 1 or 2 to pin 3.

Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

Fig. 2. ESD pulse waveform according to IEC 61000-4-2
9. Characteristics

Table 6. Characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>V_{RWM}</td>
<td>reverse standoff voltage</td>
<td>[1]</td>
<td>-</td>
<td>-</td>
<td>3.3  V</td>
</tr>
<tr>
<td>V_{BR}</td>
<td>breakdown voltage</td>
<td>I_R = 5 mA; T_{amb} = 25 °C</td>
<td>[1]</td>
<td>5.2</td>
<td>5.6</td>
<td>6    V</td>
</tr>
<tr>
<td>I_{RM}</td>
<td>reverse leakage current</td>
<td>V_{RWM} = 3.3 V; T_{amb} = 25 °C</td>
<td>[1]</td>
<td>-</td>
<td>0.7</td>
<td>2    µA</td>
</tr>
<tr>
<td>C_d</td>
<td>diode capacitance</td>
<td>f = 1 MHz; V_R = 0 V; T_{amb} = 25 °C</td>
<td>[1]</td>
<td>-</td>
<td>207</td>
<td>300  pF</td>
</tr>
<tr>
<td>V_{CL}</td>
<td>clamping voltage</td>
<td>I_{PP} = 1 A; T_{amb} = 25 °C</td>
<td>[1]</td>
<td>-</td>
<td>-</td>
<td>7    V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>I_{PP} = 18 A; T_{amb} = 25 °C</td>
<td>[1]</td>
<td>-</td>
<td>-</td>
<td>20   V</td>
</tr>
<tr>
<td>R_{diff}</td>
<td>differential resistance</td>
<td>I_R = 1 mA; T_{amb} = 25 °C</td>
<td>[1]</td>
<td>-</td>
<td>-</td>
<td>400  Ω</td>
</tr>
</tbody>
</table>

[1] Measured from pin 1 or 2 to pin 3.
[2] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.

Fig. 3. Peak pulse power dissipation as a function of pulse time; typical values

Fig. 4. Relative variation of peak pulse power as a function of junction temperature; typical values
Fig. 5. Diode capacitance as a function of reverse voltage; typical values

\[ C_d = f(V_R) \]

\[ f = 1 \text{ MHz}; \ T_{\text{amb}} = 25 \degree \text{C} \]

Fig. 6. Relative variation of reverse leakage current as a function of junction temperature; typical values

\[ \frac{I_R}{I_R(25\degree \text{C})} \]

\[ -100 \quad -50 \quad 0 \quad 50 \quad 100 \quad 150 \]

\[ T_J (\degree \text{C}) \]
Double ESD protection diode

Note 1: IEC61000-4-2 network

$C_Z = 150 \text{ pF}; R_Z = 330 \Omega$

Fig. 7. ESD clamping test set-up and waveforms
10. Application information

The device is designed for the protection of up to two unidirectional data or signal lines from the damage caused by ESD and surge pulses. The devices may be used on lines where the signal polarities are either positive or negative with respect to ground.

Fig. 8. Application diagram

Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.
12. Package outline

Plastic surface-mounted package; 3 leads

Dimensions (mm are the original dimensions)

<table>
<thead>
<tr>
<th>Unit</th>
<th>A</th>
<th>A₁</th>
<th>bₚ</th>
<th>c</th>
<th>D</th>
<th>E</th>
<th>e₁</th>
<th>Hₑ</th>
<th>Lₚ</th>
<th>Q</th>
<th>v</th>
<th>w</th>
</tr>
</thead>
<tbody>
<tr>
<td>max</td>
<td>1.1</td>
<td>0.1</td>
<td>0.48</td>
<td>0.15</td>
<td>3.0</td>
<td>1.4</td>
<td>1.9</td>
<td>0.95</td>
<td>2.5</td>
<td>0.45</td>
<td>0.55</td>
<td></td>
</tr>
<tr>
<td>min</td>
<td>0.9</td>
<td>0.38</td>
<td>0.09</td>
<td>2.8</td>
<td>1.2</td>
<td>1.9</td>
<td>0.95</td>
<td>2.1</td>
<td>0.15</td>
<td>0.45</td>
<td>0.2</td>
<td>0.1</td>
</tr>
</tbody>
</table>

Fig. 9. Package outline SOT23
13. Soldering

Fig. 10. Reflow soldering footprint for SOT23

Fig. 11. Wave soldering footprint for SOT23
14. Revision history

Table 7. Revision history

<table>
<thead>
<tr>
<th>Data sheet ID</th>
<th>Release date</th>
<th>Data sheet status</th>
<th>Change notice</th>
<th>Supersedes</th>
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</thead>
<tbody>
<tr>
<td>PESD3V3S2UT-Q v.1</td>
<td>20220610</td>
<td>Product data sheet</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

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15. Legal information

Data sheet status

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Objective [short] data sheet</td>
<td>Development</td>
<td>This document contains data from the objective specification for product development.</td>
</tr>
<tr>
<td>Preliminary [short] data sheet</td>
<td>Qualification</td>
<td>This document contains data from the preliminary specification.</td>
</tr>
<tr>
<td>Product [short] data sheet</td>
<td>Production</td>
<td>This document contains the product specification.</td>
</tr>
</tbody>
</table>

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term “short data sheet” is explained in section “Definitions”.

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com.

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For sales office addresses, please send an email to: salesaddresses@nexperia.com
Date of release: 10 June 2022

Nexperia

PESD3V3S2UT-Q
Double ESD protection diode

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