1. General description

Bidirectional double ElectroStatic Discharge (ESD) protection diode in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package designed to protect two data lines from the damage caused by ESD and other transients.

2. Features and benefits

- Reverse stand-off voltage: $V_{RWM} = 27$ V
- Low clamping voltage: $V_{CL} = 36$ V at $I_{PP} = 3$ A
- ESD protection up to 30 kV (IEC 61000-4-2)
- Ultra low leakage current: $I_{RM} < 1$ nA
- AEC-Q101 qualified

3. Applications

ESD protection for low-speed interfaces in automotive, communication, consumer and computing devices.

4. Quick reference data

Table 1. Quick reference data

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{RWM}$</td>
<td>reverse standoff voltage</td>
<td>$T_{amb} = 25$ °C</td>
<td>-</td>
<td>-</td>
<td>27</td>
<td>V</td>
</tr>
<tr>
<td>$C_d$</td>
<td>diode capacitance</td>
<td>$f = 1$ MHz; $V_R = 0$ V; $T_{amb} = 25$ °C</td>
<td>[1]</td>
<td>13</td>
<td>17</td>
<td>pF</td>
</tr>
</tbody>
</table>

[1] Measured from pin 1 or 2 to pin 3.
5. Pinning information

Table 2. Pinning information

<table>
<thead>
<tr>
<th>Pin</th>
<th>Symbol</th>
<th>Description</th>
<th>Simplified outline</th>
<th>Graphic symbol</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>K1</td>
<td>cathode (diode 1)</td>
<td>SOT23 had</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>K2</td>
<td>cathode (diode 2)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>CC</td>
<td>common cathode</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

6. Ordering information

Table 3. Ordering information

<table>
<thead>
<tr>
<th>Type number</th>
<th>Package</th>
<th>Description</th>
<th>Version</th>
</tr>
</thead>
<tbody>
<tr>
<td>PESD27VV2BT</td>
<td>SOT23</td>
<td>plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body</td>
<td>SOT23</td>
</tr>
</tbody>
</table>

7. Marking

Table 4. Marking codes

<table>
<thead>
<tr>
<th>Type number</th>
<th>Marking code[1]</th>
</tr>
</thead>
<tbody>
<tr>
<td>PESD27VV2BT</td>
<td>FM%</td>
</tr>
</tbody>
</table>

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

*In accordance with the Absolute Maximum Rating System (IEC 60134).*

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$I_{PPM}$</td>
<td>rated peak pulse current</td>
<td>$t_p = 8/20 \mu s$</td>
<td>[1]</td>
<td>[2]</td>
<td>A</td>
</tr>
<tr>
<td>$T_j$</td>
<td>junction temperature</td>
<td></td>
<td>-150</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>$T_{amb}$</td>
<td>ambient temperature</td>
<td></td>
<td>-55</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>$T_{stg}$</td>
<td>storage temperature</td>
<td></td>
<td>-65</td>
<td>150</td>
<td>°C</td>
</tr>
</tbody>
</table>

**ESD maximum ratings**

<table>
<thead>
<tr>
<th>$V_{ESD}$</th>
<th>electrostatic discharge voltage</th>
<th>IEC 61000-4-2; contact discharge</th>
<th>[2]</th>
<th>[3]</th>
<th>30</th>
<th>kV</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>IEC 61000-4-2; air discharge</td>
<td>[2]</td>
<td>[3]</td>
<td>30</td>
<td>kV</td>
</tr>
</tbody>
</table>

[1] Device stressed with 8/20 μs exponential decay waveform according to IEC 61000-4-5.
[2] Measured from pin 1 or 2 to pin 3.
Bidirectional double ESD protection diode

9. Characteristics

<table>
<thead>
<tr>
<th>Table 6. Characteristics</th>
</tr>
</thead>
<tbody>
<tr>
<td>Symbol</td>
</tr>
<tr>
<td>V_{RWM}</td>
</tr>
<tr>
<td>V_{BR}</td>
</tr>
<tr>
<td>I_{RM}</td>
</tr>
<tr>
<td>C_d</td>
</tr>
<tr>
<td>V_{CL}</td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td>R_{dyn}</td>
</tr>
</tbody>
</table>

[1] Measured from pin 1 or 2 to pin 3.
[2] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
[3] Non-repetitive current pulse; Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008
Bidirectional double ESD protection diode

**Fig. 3.** V-I characteristics for a bidirectional ESD protection diode

**Fig. 4.** Diode capacitance as a function of reverse voltage; typical values

**Fig. 5.** Positive clamping voltage (TLP); typical values

**Fig. 6.** Negative clamping voltage (TLP); typical values

**Fig. 7.** Peak pulse power as a function of exponential pulse duration; typical values

**Fig. 8.** Relative variation of peak pulse power as a function of junction temperature; typical values

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Bidirectional double ESD protection diode

IEC 61000-4-2 ed.2
$C_s = 150 \, \text{pF}; \ R_d = 330 \, \Omega$

DUT (DEVICE UNDER TEST)

RG 223/U
50 $\Omega$ coax

4 GHz DIGITAL OSCILLOSCOPE

ATTENUATOR

50 $\Omega$

undamped +8 kV ESD pulse waveform
(IEC 61000-4-2 network)

unclamped -8 kV ESD pulse waveform
(IEC 61000-4-2 network)

Fig. 9. ESD clamping test setup and waveforms

Fig. 10. Clamped +8 kV pulse waveform (IEC 61000-4-2 network)

Fig. 11. Clamped -8 kV pulse waveform (IEC 61000-4-2 network)
10. Application information

The device is designed for the protection of up to two bidirectional data lines from surge pulses and ESD damage.

![Application diagram](image)

**Fig. 12. Application diagram**

**Circuit board layout and protection device placement**

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Test information

**Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.
12. Package outline

Plastic surface-mounted package; 3 leads

Dimensions (mm are the original dimensions)

<table>
<thead>
<tr>
<th>Unit</th>
<th>A</th>
<th>A₁</th>
<th>b₁p</th>
<th>c</th>
<th>D</th>
<th>E</th>
<th>e₁</th>
<th>Hₑ</th>
<th>L_p</th>
<th>Q</th>
<th>v</th>
<th>w</th>
</tr>
</thead>
<tbody>
<tr>
<td>mm</td>
<td>max</td>
<td>1.1</td>
<td>0.1</td>
<td>0.15</td>
<td>3.0</td>
<td>1.4</td>
<td>1.9</td>
<td>0.95</td>
<td>2.5</td>
<td>0.45</td>
<td>0.55</td>
<td></td>
</tr>
<tr>
<td>min</td>
<td>0.9</td>
<td>0.15</td>
<td>0.08</td>
<td>2.8</td>
<td>1.2</td>
<td>1.9</td>
<td>0.95</td>
<td>2.1</td>
<td>0.15</td>
<td>0.45</td>
<td>0.2</td>
<td>0.1</td>
</tr>
</tbody>
</table>

Fig. 13. Package outline SOT23
13. Soldering

Fig. 14. Reflow soldering footprint for SOT23

Fig. 15. Wave soldering footprint for SOT23
14. Revision history

Table 7. Revision history

<table>
<thead>
<tr>
<th>Data sheet ID</th>
<th>Release date</th>
<th>Data sheet status</th>
<th>Change notice</th>
<th>Supersedes</th>
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</thead>
<tbody>
<tr>
<td>PESD27VV2BT v.1</td>
<td>20190625</td>
<td>Product data sheet</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>
Bidirectional double ESD protection diode

PESD27VV2BT

15. Legal information

Data sheet status

<table>
<thead>
<tr>
<th>Document status</th>
<th>Product status</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>Preliminary [short]</td>
<td>Development</td>
<td>This document contains data from the objective specification for product development.</td>
</tr>
<tr>
<td>Preliminary [short]</td>
<td>Qualification</td>
<td>This document contains data from the preliminary specification.</td>
</tr>
<tr>
<td>Product [short]</td>
<td>Production</td>
<td>This document contains the product specification.</td>
</tr>
</tbody>
</table>

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