1. General description

Bidirectional double ElectroStatic Discharge (ESD) protection diode in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package designed to protect two data lines from the damage caused by ESD and other transients.

2. Features and benefits

- Reverse stand-off voltage: $V_{RWM} = 24 \text{ V}$
- Low clamping voltage: $V_{CL} = 33 \text{ V}$ at $I_{PP} = 3.5 \text{ A}$
- ESD protection up to 30 kV (IEC 61000-4-2)
- Ultra low leakage current: $I_{RM} < 1 \text{ nA}$
- AEC-Q101 qualified

3. Applications

ESD protection for low-speed interfaces in automotive, communication, consumer and computing devices.

4. Quick reference data

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{RWM}$</td>
<td>reverse standoff voltage</td>
<td>$T_{amb} = 25 \text{ °C}$</td>
<td>-</td>
<td>-</td>
<td>24</td>
<td>V</td>
</tr>
<tr>
<td>$C_d$</td>
<td>diode capacitance</td>
<td>$f = 1 \text{ MHz}; V_R = 0 \text{ V}; T_{amb} = 25 \text{ °C}$ [1]</td>
<td>-</td>
<td>14</td>
<td>17</td>
<td>pF</td>
</tr>
</tbody>
</table>

[1] Measured from pin 1 or 2 to pin 3.
5. Pinning information

Table 2. Pinning information

<table>
<thead>
<tr>
<th>Pin</th>
<th>Symbol</th>
<th>Description</th>
<th>Simplified outline</th>
<th>Graphic symbol</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>K1</td>
<td>cathode (diode 1)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>K2</td>
<td>cathode (diode 2)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>K</td>
<td>common cathode</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

6. Ordering information

Table 3. Ordering information

<table>
<thead>
<tr>
<th>Type number</th>
<th>Package</th>
<th>Name</th>
<th>Description</th>
<th>Version</th>
</tr>
</thead>
<tbody>
<tr>
<td>PESD24VV2BT</td>
<td>SOT23</td>
<td>plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body</td>
<td>SOT23</td>
<td></td>
</tr>
</tbody>
</table>

7. Marking

Table 4. Marking codes

<table>
<thead>
<tr>
<th>Type number</th>
<th>Marking code[1]</th>
</tr>
</thead>
<tbody>
<tr>
<td>PESD24VV2BT</td>
<td>FL%</td>
</tr>
</tbody>
</table>

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>( I_{\text{PPM}} )</td>
<td>rated peak pulse current</td>
<td>( t_p = 8/20 \mu s )</td>
<td>[1]</td>
<td>3.5</td>
<td>A</td>
</tr>
<tr>
<td>( T_j )</td>
<td>junction temperature</td>
<td></td>
<td>-</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>( T_{\text{amb}} )</td>
<td>ambient temperature</td>
<td></td>
<td>-55</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>( T_{\text{stg}} )</td>
<td>storage temperature</td>
<td></td>
<td>-65</td>
<td>150</td>
<td>°C</td>
</tr>
</tbody>
</table>

**ESD maximum ratings**

| \( V_{\text{ESD}} \) | electrostatic discharge voltage | IEC 61000-4-2; contact discharge | [2]  | 30   | kV   |
|                     |                                | IEC 61000-4-2; air discharge     | [2]  | 30   | kV   |

[2] Measured from pin 1 or 2 to pin 3.
### 9. Characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{RWM}$</td>
<td>reverse standoff voltage</td>
<td>$T_{amb} = 25 \degree C$</td>
<td>-</td>
<td>-</td>
<td>24</td>
<td>V</td>
</tr>
<tr>
<td>$V_{BR}$</td>
<td>breakdown voltage</td>
<td>$I_R = 10 \ mA; T_{amb} = 25 \degree C$</td>
<td>[1] 25.5</td>
<td>30.5</td>
<td>35.5</td>
<td>V</td>
</tr>
<tr>
<td>$I_{RM}$</td>
<td>reverse leakage current</td>
<td>$V_{RWM} = 24 \ V; T_{amb} = 25 \degree C$</td>
<td>[1] -</td>
<td>1</td>
<td>50</td>
<td>nA</td>
</tr>
<tr>
<td>$C_d$</td>
<td>diode capacitance</td>
<td>$f = 1 \ MHz; V_R = 0 \ V; T_{amb} = 25 \degree C$</td>
<td>[1] -</td>
<td>14</td>
<td>17</td>
<td>pF</td>
</tr>
<tr>
<td>$V_{CL}$</td>
<td>clamping voltage</td>
<td>$I_{PPM} = 1 \ A; t_p = 8/20 \ mu s; T_{amb} = 25 \degree C$</td>
<td>[2] [1] -</td>
<td>31</td>
<td>40</td>
<td>V</td>
</tr>
<tr>
<td>$R_{dyn}$</td>
<td>dynamic resistance</td>
<td>$I_R = 10 \ A; T_{amb} = 25 \degree C$</td>
<td>[3] [1] -</td>
<td>2</td>
<td>-</td>
<td>V</td>
</tr>
</tbody>
</table>

[1] Measured from pin 1 or 2 to pin 3.
[2] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
[3] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008
Bidirectional double ESD protection diode

Fig. 3. V-I characteristics for a bidirectional ESD protection diode

Fig. 4. Diode capacitance as a function of reverse voltage; typical values

Fig. 5. Positive clamping voltage (TLP); typical values

Fig. 6. Negative clamping voltage (TLP); typical values

Fig. 7. Peak pulse power as a function of exponential pulse duration; typical values

Fig. 8. Relative variation of peak pulse power as a function of junction temperature; typical values
Fig. 9. ESD clamping test setup and waveforms

Fig. 10. Clamped +8 kV pulse waveform (IEC 61000-4-2 network)

Fig. 11. Clamped -8 kV pulse waveform (IEC 61000-4-2 network)
10. Application information

The device is designed for the protection of up to two bidirectional data lines from surge pulses and ESD damage.

![Application diagram](image)

**Fig. 12. Application diagram**

Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.
12. Package outline

Plastic surface-mounted package; 3 leads  SOT23

Fig. 13. Package outline SOT23
13. Soldering

Fig. 14. Reflow soldering footprint for SOT23

Fig. 15. Wave soldering footprint for SOT23
## 14. Revision history

<table>
<thead>
<tr>
<th>Data sheet ID</th>
<th>Release date</th>
<th>Data sheet status</th>
<th>Change notice</th>
<th>Supersedes</th>
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<tbody>
<tr>
<td>PESD24VV2BT v.1</td>
<td>20190625</td>
<td>Product data sheet</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>
15. Legal information

**Data sheet status**

<table>
<thead>
<tr>
<th>Document status</th>
<th>Product status</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>[1][2]</td>
<td>[3]</td>
<td>This document contains data from the objective specification for product development.</td>
</tr>
<tr>
<td>Preliminary [short] data sheet</td>
<td>Qualification</td>
<td>This document contains data from the preliminary specification.</td>
</tr>
<tr>
<td>Product [short] data sheet</td>
<td>Production</td>
<td>This document contains the product specification.</td>
</tr>
</tbody>
</table>

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term “short data sheet” is explained in section “Definitions”.
[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at [https://www.nexperia.com](https://www.nexperia.com).

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