## 1. General description

ESD protection device in a leadless ultra small DFN1006BD-2 (SOD882BD) Surface-Mounted Device (SMD) plastic package with side-wettable flanks, designed to protect automotive in-vehicle network bus lines from the damage caused by ElectroStatic Discharge (ESD) and other transients.

## 2. Features and benefits

- Reverse stand-off voltage: V<sub>RWM</sub> = 24 V
- Low clamping voltage: typical V<sub>CL</sub> = 31 V at I<sub>pp</sub> = 1 A
- ESD protection up to 30 kV (IEC 61000-4-2)
- ESD protection up to 30 kV (ISO 10605; C = 330 pF, R = 330 Ω)
- Low capacitance: C<sub>d</sub> = 10 pF
- Qualified according to AEC-Q101 and recommended for use in automotive applications

## 3. Applications

ESD protection for in-vehicle network lines in automotive environments

- 24 V board net / truck systems
- CAN / CAN-FD
- FlexRay
- SENT

## 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	24	V
I <sub>PPM</sub>	rated peak pulse current	$t_p = 8/20 \ \mu s$	[1]	-	-	3.8	А
V <sub>CL</sub>	clamping voltage	$I_{PP}$ = 16 A; $t_p$ = 100 ns; $T_{amb}$ = 25 °C	[2]	-	36	-	V

- [1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- [2] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008



# 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)		
2	K2	cathode (diode 2)	Transparent top view  DFN1006BD-2 (SOD882BD)	K1 K2 006aab041

# 6. Ordering information

## **Table 3. Ordering information**

Type number	Package				
	Name	Description	Version		
PESD1CANFD24LS-Q		Leadless ultra small plastic package with side-wettable flanks (SWF); 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.47 mm body	SOD882BD		

## 7. Marking

### Table 4. Marking codes

Type number	Marking code
PESD1CANFD24LS-Q	81

# 8. Limiting values

### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I <sub>PPM</sub>	rated peak pulse current	$t_p = 8/20 \ \mu s$	[1]	-	3.8	Α
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maximi	um ratings					
V <sub>ESD</sub>	electrostatic discharge	IEC 61000-4-2; contact discharge	[2]	-	30	kV
	voltage	ISO 10605; contact discharge; C = 330 pF, R = 330 $\Omega$	[2]	-	30	kV
		ISO 10605; contact discharge; C = 150 pF, R = 330 $\Omega$	[2]	-	30	kV

- [1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- [2] Device stressed with ten non-repetitive ESD pulses.

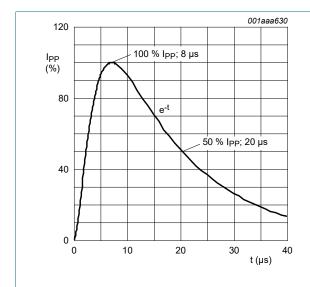


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

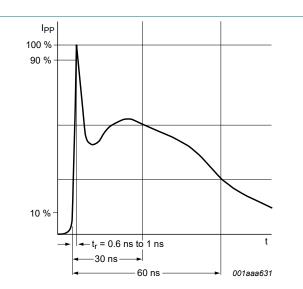


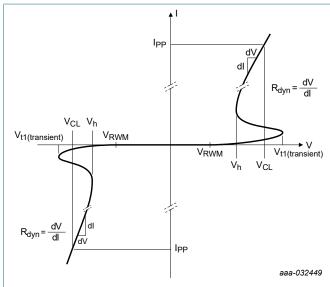
Fig. 2. ESD pulse waveform according to IEC 61000-4-2

## 9. Characteristics

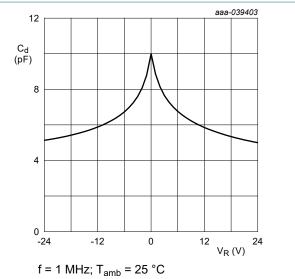
**Table 6. Characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	24	V
$V_{BR}$	breakdown voltage	I <sub>R</sub> = 10 mA; T <sub>amb</sub> = 25 °C		25.5	-	35.5	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 24 V; T <sub>amb</sub> = 25 °C		-	1	50	nA
C <sub>d</sub>	diode capacitance	$f = 1 \text{ MHz}; V_R = 0 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}$		-	10	11.5	pF
V <sub>CL</sub>	clamping voltage	$I_{PP}$ = 1 A; $t_p$ = 8/20 µs; $T_{amb}$ = 25 °C	[1]	-	31	-	V
		$I_{PP}$ = 16 A; $t_p$ = 100 ns; $T_{amb}$ = 25 °C	[2]	-	36	-	V
R <sub>dyn</sub>	dynamic resistance	$I_R = 10 \text{ A}; t_p = 100 \text{ ns}; T_{amb} = 25 ^{\circ}\text{C}$	[2]	-	0.5	-	Ω

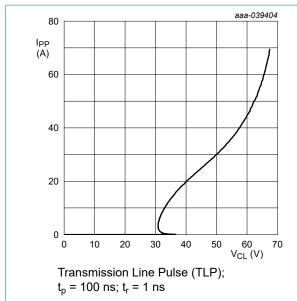
- Device stressed with 8/20  $\mu$ s exponential decay waveform according to IEC 61000-4-5. Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008

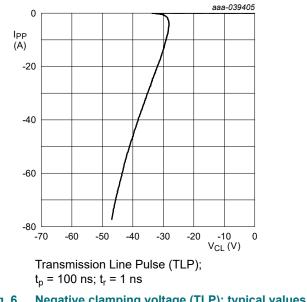


Transient characteristics for a bidirectional ESD Fig. 4. protection device



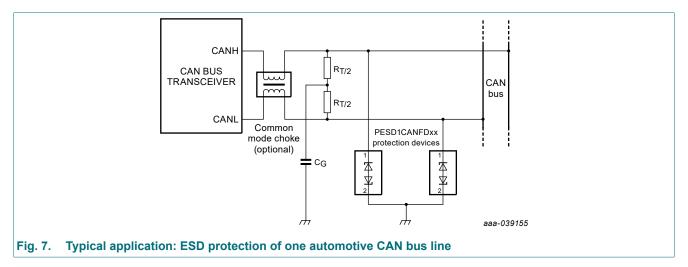
Diode capacitance as a function of reverse voltage; typical values





## 10. Application information

The device is designed for the protection of one automotive in-vehicle bus line, e.g. CAN (FD), from the damage caused by ESD and surge pulses.



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

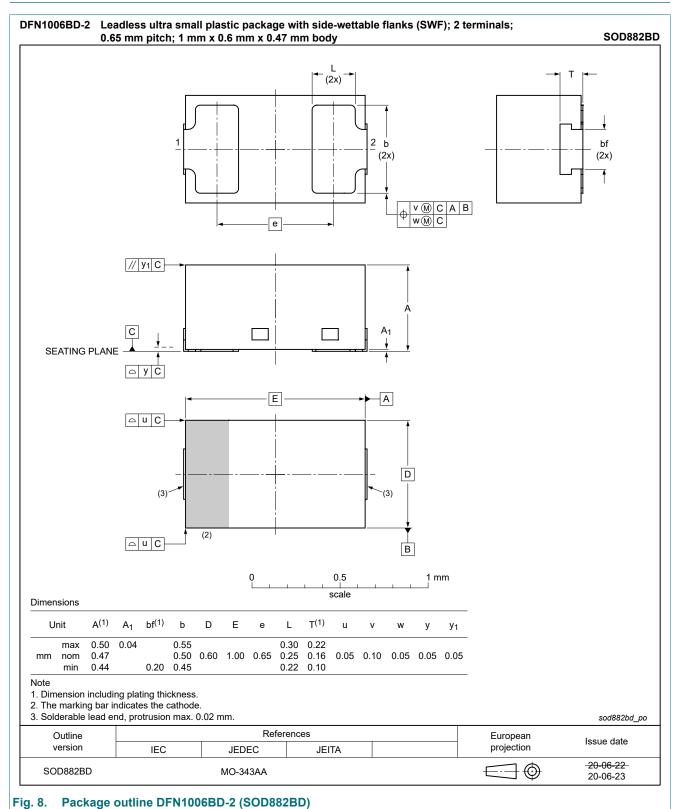
- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

## 11. Test information

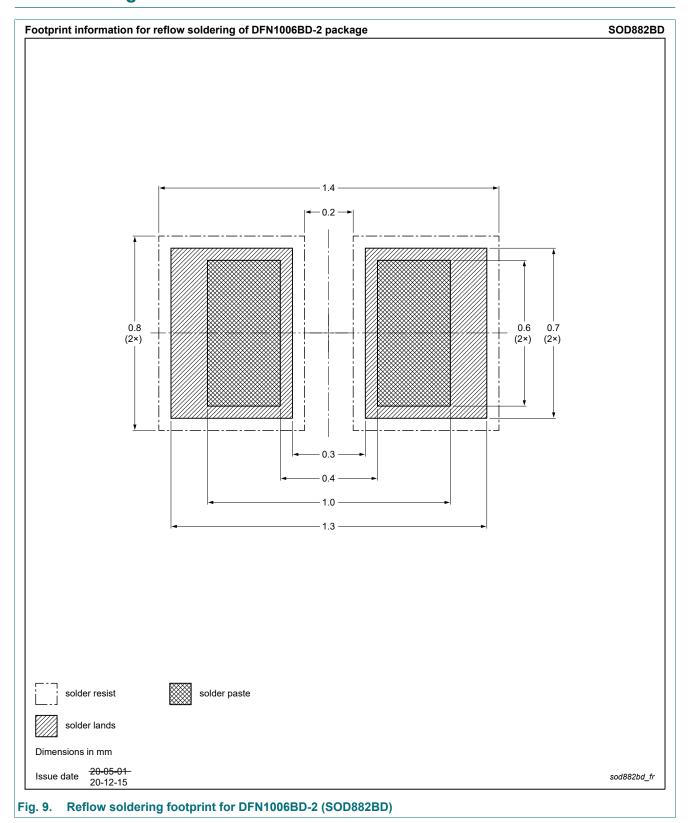
### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

# 12. Package outline



# 13. Soldering



# 14. Revision history

## **Table 7. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD1CANFD24LS-Q	20240517	Product data sheet	-	-
v.1				

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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