



PESD18VF1BBL

Bidirectional ESD protection diode

25 July 2023

Product data sheet

1. General description

Ultra low capacitance bidirectional ElectroStatic Discharge (ESD) protection diode in a DFN1006-2 (SOD882) leadless ultra small Surface-Mounted Device (SMD) plastic package, designed to protect one signal line from the damage caused by ESD and other transients.

2. Features and benefits

- Bidirectional ESD protection of one line
- High reverse standoff voltage $V_{RWM} = 18\text{ V}$
- Ultra-low diode capacitance $C_d = 0.31\text{ pF}$
- Ultra small plastic package $1.0 \times 0.6 \times 0.48\text{ mm}$
- ESD protection up to $\pm 10\text{ kV}$ contact discharge according to IEC 61000-4-2

3. Applications

- NFC antenna protection
- Protection of high-speed data lines

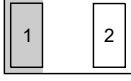
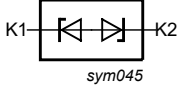
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{RWM}	reverse standoff voltage	$T_{amb} = 25\text{ °C}$	-	-	18	V
C_d	diode capacitance	$f = 1\text{ MHz}; V_R = 0\text{ V}; T_{amb} = 25\text{ °C}$	-	0.31	0.4	pF

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)	 <p>Transparent top view</p> <p>DFN1006-2 (SOD882)</p>	 <p><i>sym045</i></p>
2	K2	cathode (diode 2)		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PESD18VF1BBL	DFN1006-2	plastic, leadless ultra small package; 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.48 mm body	SOD882

7. Marking

Table 4. Marking codes

Type number	Marking code
PESD18VF1BBL	++

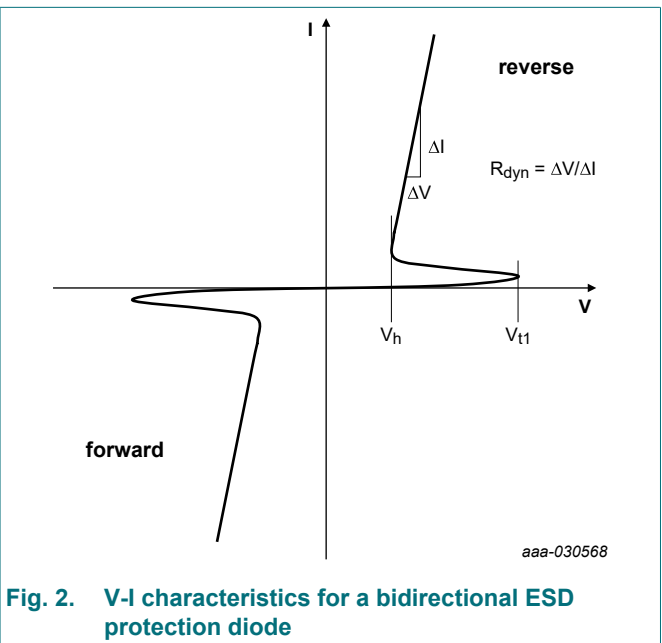
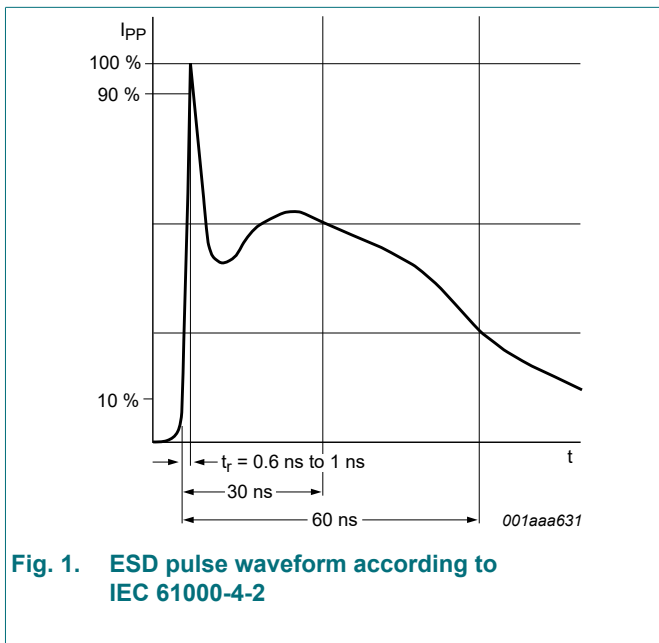
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-55	150	°C
T_{stg}	storage temperature			-65	150	°C
ESD maximum ratings						
V_{ESD}	electrostatic discharge voltage	IEC 61000-4-2; contact discharge	[1]	-	10	kV
		IEC 61000-4-2; air discharge	[1]	-	15	kV

[1] Device stressed with ten non-repetitive ESD pulses.

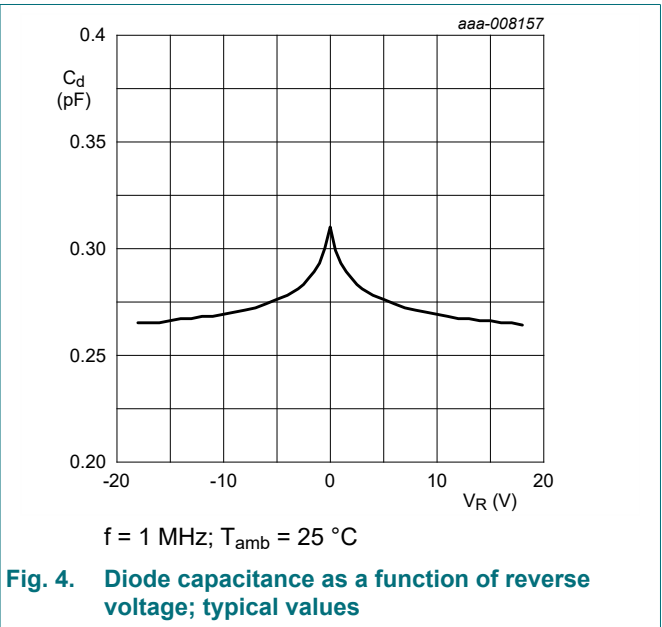
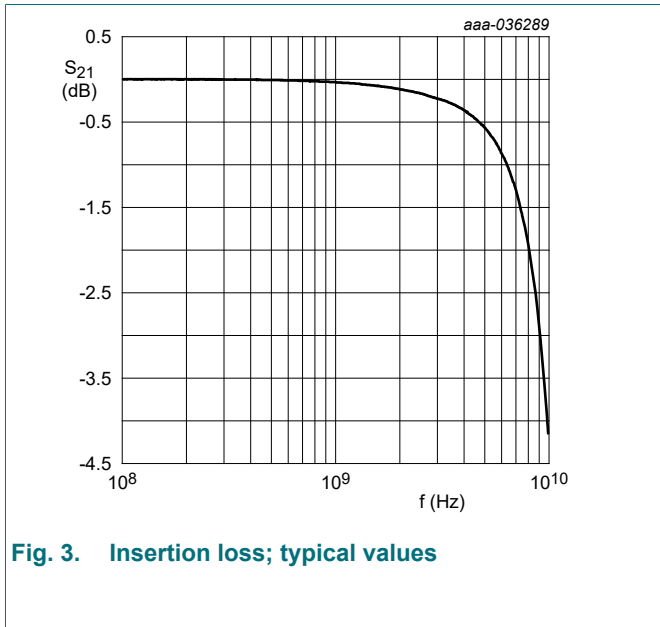


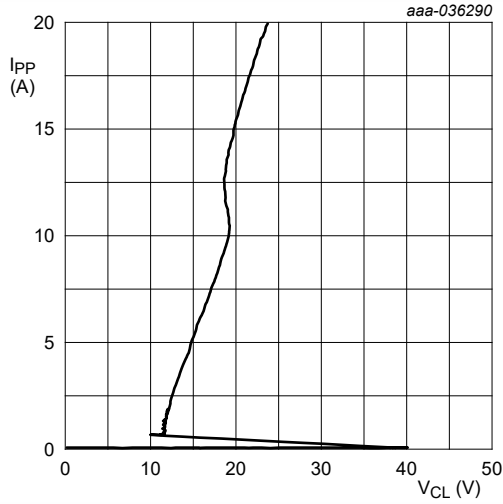
9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
V_{RWM}	reverse standoff voltage	$T_{amb} = 25\text{ °C}$	-	-	18	V	
V_{BR}	breakdown voltage	$I_R = 10\text{ mA}; T_{amb} = 25\text{ °C}$	19	24	-	V	
I_{RM}	reverse leakage current	$V_R = 18\text{ V}; T_{amb} = 25\text{ °C}$	-	1	30	nA	
C_d	diode capacitance	$f = 1\text{ MHz}; V_R = 0\text{ V}; T_{amb} = 25\text{ °C}$	-	0.31	0.4	pF	
V_{CL}	clamping voltage	$I_{TLP} = 8\text{ A}; t_p = 100\text{ ns}; T_{amb} = 25\text{ °C}$	[1]	-	17.5	-	V
		$I_{TLP} = 16\text{ A}; t_p = 100\text{ ns}; T_{amb} = 25\text{ °C}$	[1]	-	20	-	V
R_{dyn}	dynamic resistance	$I_R = 7.5\text{ A}; t_p = 100\text{ ns}; T_{amb} = 25\text{ °C}$	[1]	-	0.8	Ω	

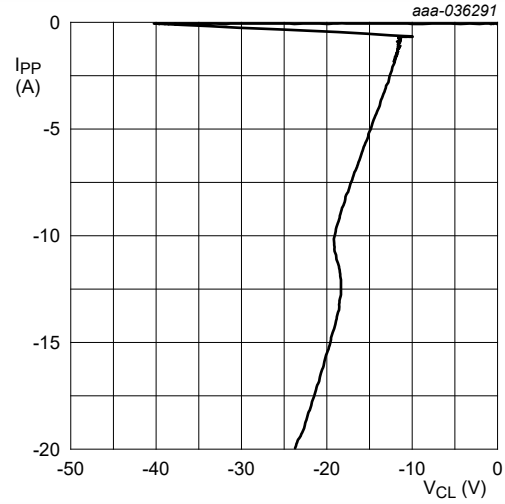
[1] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008.





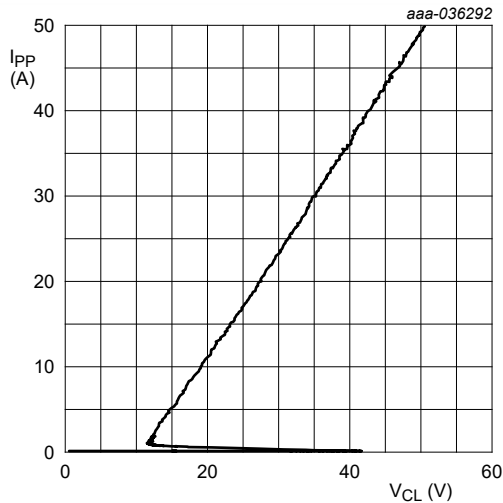
Transmission Line Pulse (TLP);
 $t_p = 100$ ns; $t_r = 1$ ns

Fig. 5. Dynamic resistance with positive clamping; typical values



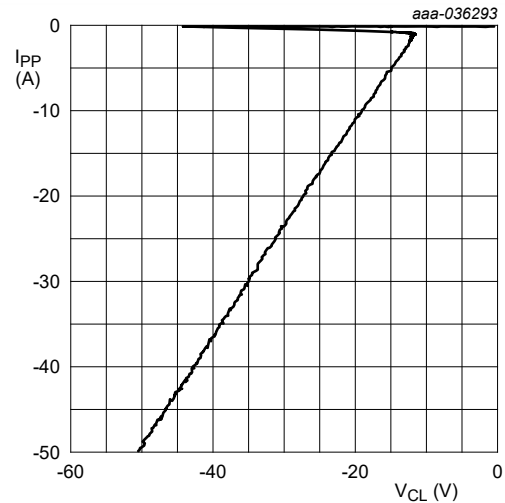
Transmission Line Pulse (TLP);
 $t_p = 100$ ns; $t_r = 1$ ns

Fig. 6. Dynamic resistance with negative clamping; typical values



Very-Fast Transmission Line Pulse (VF-TLP);
 $t_p = 5$ ns; $t_r = 600$ ps

Fig. 7. Dynamic resistance with positive clamping; typical values



Very-Fast Transmission Line Pulse (VF-TLP);
 $t_p = 5$ ns; $t_r = 600$ ps

Fig. 8. Dynamic resistance with negative clamping; typical values

10. Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.

The device uses an advanced clamping structure showing a negative dynamic resistance. This snap-back behavior strongly reduces the clamping voltage to the system behind the ESD protection during an ESD event. Do not connect unlimited DC current sources to the data lines to avoid keeping the ESD protection device in snap-back state after exceeding breakdown voltage (due to an ESD pulse for instance).

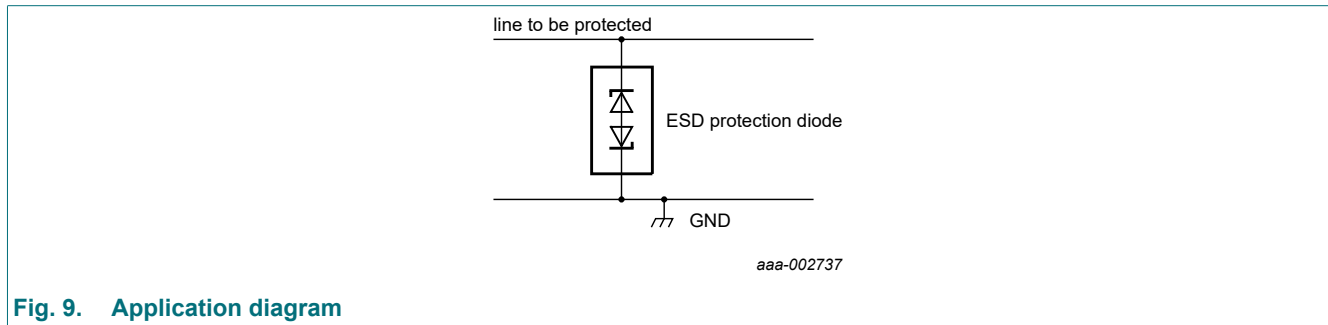


Fig. 9. Application diagram

Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Package outline

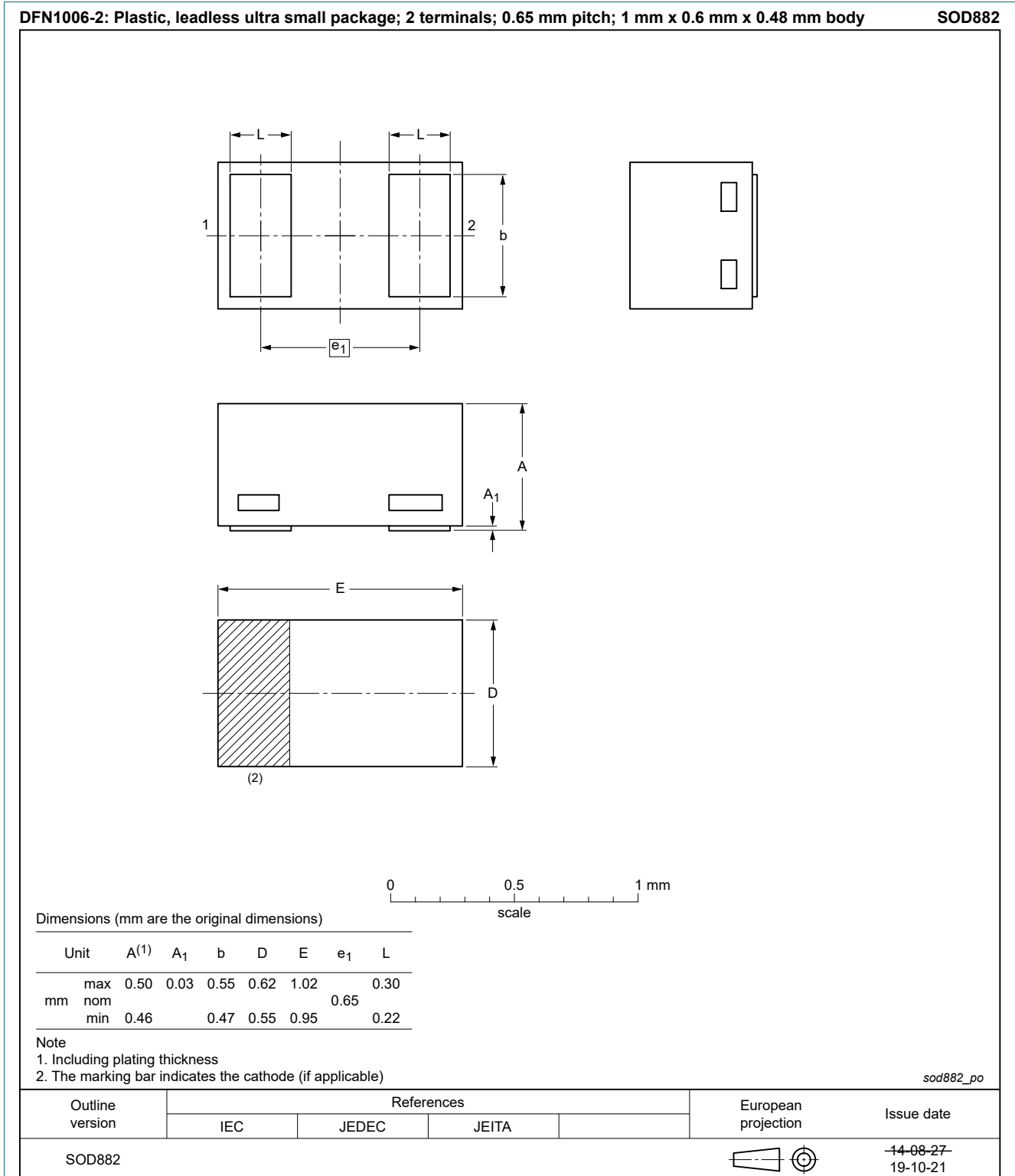
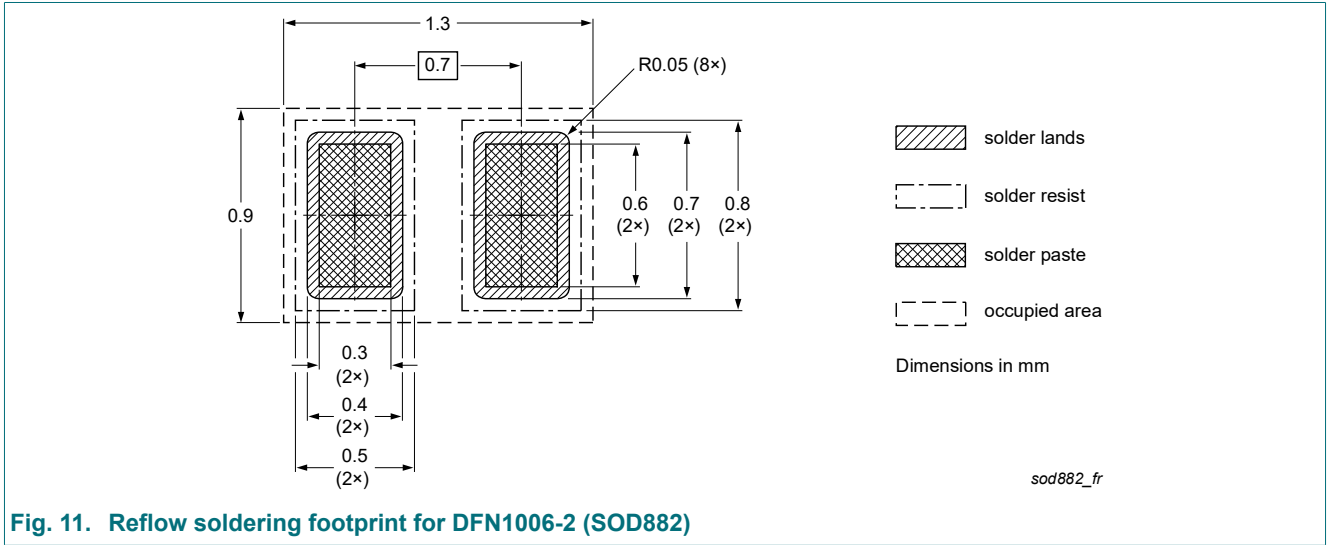


Fig. 10. Package outline DFN1006-2 (SOD882)

12. Soldering



13. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD18VF1BBL v.2	20230725	Product data sheet	-	PESD18VF1BBL v.1
Modifications:	<ul style="list-style-type: none">• Corrected the document to non-automotive qualification• Chapter "Limiting values": IEC 61000-4-2 air discharge revised• Chapter "Features and benefits": ESD information adjusted			
PESD18VF1BBL v.1	20230424	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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