

ESD protection diode in SOD523 package

18 August 2022

Product data sheet

1. General description

Unidirectional ElectroStatic Discharge (ESD) protection diode in a SOD523 plastic package designed to protect one transmission or data line from the damage caused by ESD and other transients.

2. Features and benefits

- Unidirectional ESD protection of one line
- Low clamping voltage: V_{CL} = 40 V at I_{PPM} = 5 A
- ESD protection > 30 kV
- IEC 61000-4-5 (surge); I_{PPM} = 5 A at t_p = 8/20 μs
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Application information

- Computers and peripherals
- Communication systems
- Audio and video equipment
- Data lines
- CAN bus protection

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C	-	-	15	V
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	-	32	70	pF



5. Pinning information

Table 2	Table 2. Pinning information						
Pin	Symbol	Description	Simplified outline	Graphic symbol			
1	К	cathode[1]					
2	A	anode	1 2	1 🛃 2			
				sym035			
			SC-79 (SOD523)				

[1] The marking bar indicates pin 1.

6. Ordering information

Type number Package				
	Name	Description	Version	
PESD15VS1UB-Q		plastic, surface-mounted package; 2 leads; 1.2 mm x 0.8 mm x 0.6 mm body	SOD523	

7. Marking

Table 4. Marking codes	
Type number	Marking code
PESD15VS1UB-Q	N4

8. Limiting values

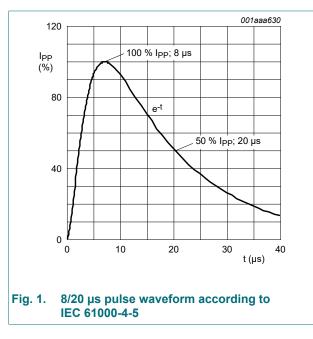
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P _{PPM}	rated peak pulse power	t _p = 8/20 μs	[1]	-	160	W
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[1]	-	5	А
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximu	m ratings					
V _{ESD}	electrostatic discharge	IEC 61000-4-2 (contact discharge)	[2]	-	30	kV
	voltage	HBM MIL-STD883		-	10	kV

[1] Non-repetitive current pulse 8/20 µs exponentially decay waveform.

[2] Device stressed with ten non-repetitive ESD pulses.



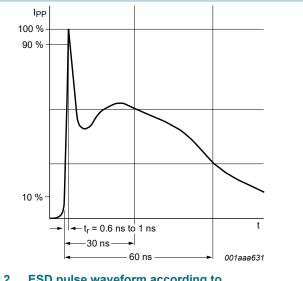
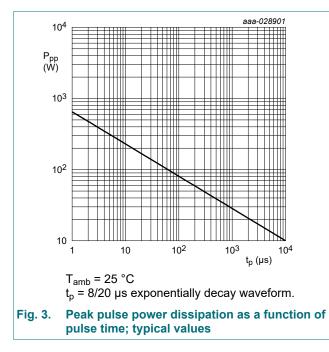


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

9. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	15	V
V _{BR}	breakdown voltage	I _R = 5 mA; T _{amb} = 25 °C		17.6	18	18.4	V
I _{RM}	reverse leakage current	V _{RWM} = 15 V; T _{amb} = 25 °C		-	1	50	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	32	70	pF
V _{CL}	clamping voltage	I _{PPM} = 1 A; T _{amb} = 25 °C	[1]	-	-	23	V
		I _{PPM} = 5 A; T _{amb} = 25 °C	[1]	-	-	40	V
R _{diff}	differential resistance	I _R = 1 mA; T _{amb} = 25 °C		-	-	225	Ω

[1] Non-repetitive current pulse 8/20 µs exponentially decay waveform.



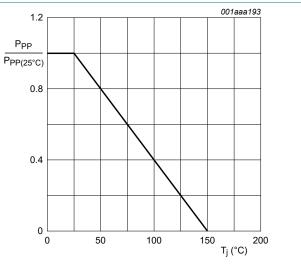
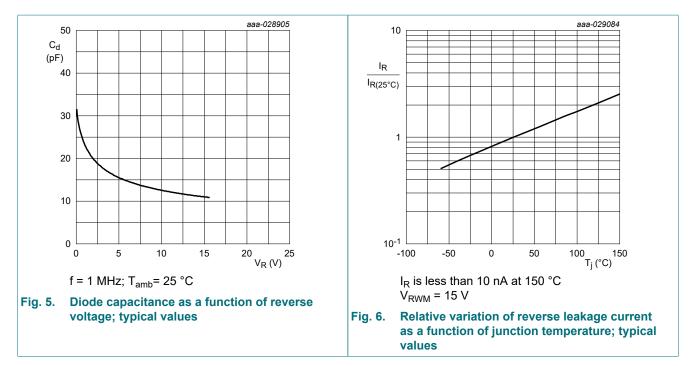


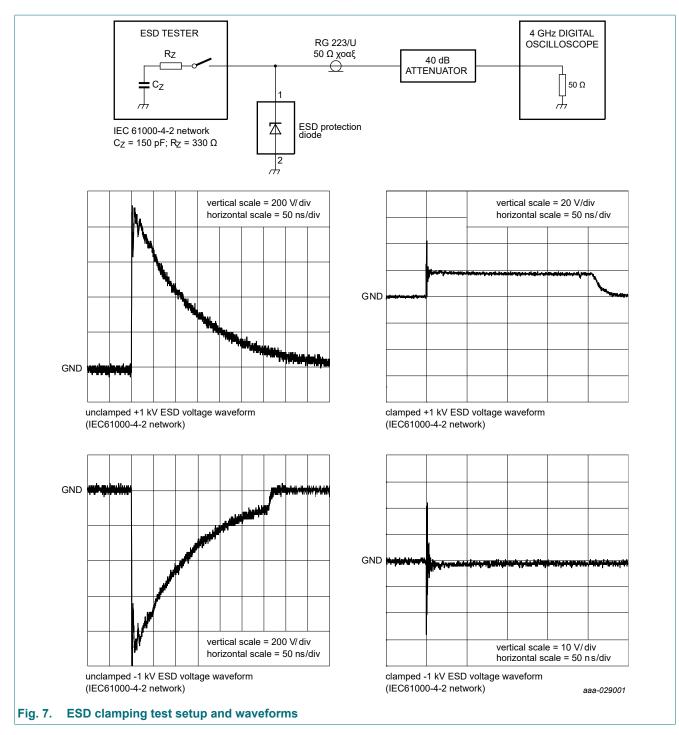
Fig. 4. Relative variation of peak pulse power as a function of junction temperature; typical values

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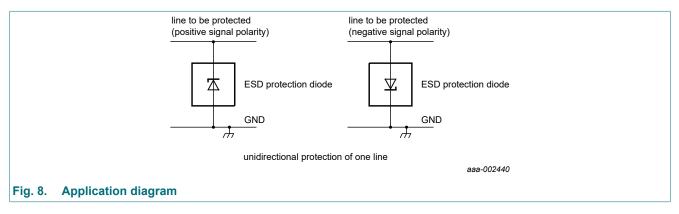
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10. Application information

The device is designed for protection of one unidirectional data or signal line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are either positive or negative with respect to ground.



Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

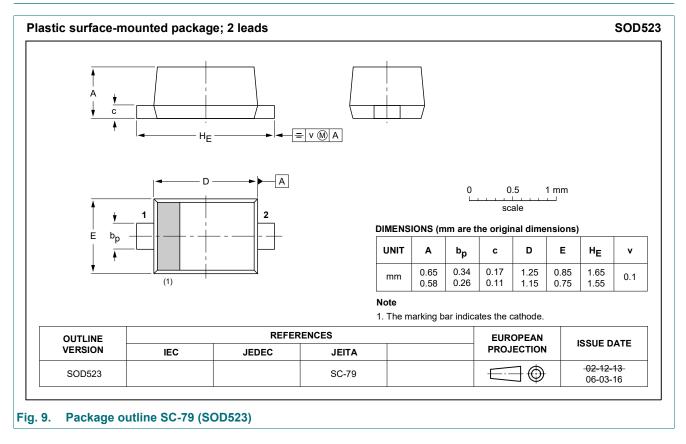
- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- **6.** Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

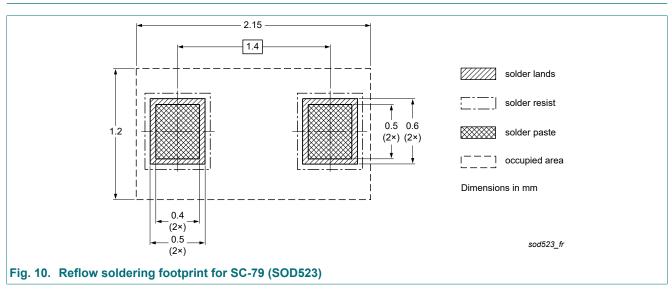
12. Package outline



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13. Soldering



14. Revision history

Table 7. Revision history					
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
PESD15VS1UB-Q v.1	20220818	Product data sheet	-	-	

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

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