1. General description

Unidirectional ElectroStatic Discharge (ESD) protection diode in a SOD523 plastic package designed to protect one transmission or data line from the damage caused by ESD and other transients.

2. Features and benefits

- Unidirectional ESD protection of one line
- Low clamping voltage: $V_{CL} = 35 \text{ V}$ at $I_{PPM} = 5 \text{ A}$
- ESD protection $> 30 \text{ kV}$
- IEC 61000-4-5 (surge); $I_{PPM} = 5 \text{ A}$ at $t_p = 8/20 \mu\text{s}$

3. Application information

- Computers and peripherals
- Communication systems
- Audio and video equipment
- Data lines
- CAN bus protection

4. Quick reference data

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{RWM}$</td>
<td>reverse standoff voltage</td>
<td>$T_{amb} = 25 \degree \text{C}$</td>
<td>-</td>
<td>-</td>
<td>12</td>
<td>V</td>
</tr>
<tr>
<td>$C_d$</td>
<td>diode capacitance</td>
<td>$f = 1 \text{ MHz}; V_R = 0 \text{ V}; T_{amb} = 25 \degree \text{C}$</td>
<td>-</td>
<td>38</td>
<td>75</td>
<td>pF</td>
</tr>
</tbody>
</table>
5. Pinning information

Table 2. Pinning information

<table>
<thead>
<tr>
<th>Pin</th>
<th>Symbol</th>
<th>Description</th>
<th>Simplified outline</th>
<th>Graphic symbol</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>K</td>
<td>cathode[1]</td>
<td>SOD523</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>A</td>
<td>anode</td>
<td></td>
<td></td>
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</tbody>
</table>


6. Ordering information

Table 3. Ordering information

<table>
<thead>
<tr>
<th>Type number</th>
<th>Package Name</th>
<th>Description</th>
<th>Version</th>
</tr>
</thead>
<tbody>
<tr>
<td>PESD12VS1UB</td>
<td>SOD523</td>
<td>plastic, surface-mounted package; 2 leads; 1.2 mm x 0.8 mm x 0.6 mm body</td>
<td>SOD523</td>
</tr>
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</table>

7. Marking

Table 4. Marking codes

<table>
<thead>
<tr>
<th>Type number</th>
<th>Marking code</th>
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<tbody>
<tr>
<td>PESD12VS1UB</td>
<td>N3</td>
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</table>
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$P_{PPM}$</td>
<td>rated peak pulse power</td>
<td>$t_p = 8/20 \mu s$</td>
<td>[1]</td>
<td>-</td>
<td>180</td>
</tr>
<tr>
<td>$I_{PPM}$</td>
<td>rated peak pulse current</td>
<td>$t_p = 8/20 \mu s$</td>
<td>[1]</td>
<td>-</td>
<td>5</td>
</tr>
<tr>
<td>$T_j$</td>
<td>junction temperature</td>
<td></td>
<td></td>
<td>-</td>
<td>150</td>
</tr>
<tr>
<td>$T_{amb}$</td>
<td>ambient temperature</td>
<td></td>
<td></td>
<td>-55</td>
<td>150</td>
</tr>
<tr>
<td>$T_{stg}$</td>
<td>storage temperature</td>
<td></td>
<td></td>
<td>-65</td>
<td>150</td>
</tr>
</tbody>
</table>

ESD maximum ratings

| $V_{ESD}$ | electrostatic discharge voltage | IEC 61000-4-2 (contact discharge) | [2] | - | 30 | kV |
| HBM MIL-STD883 | | | - | 10 | kV |


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

Fig. 2. ESD pulse waveform according to IEC 61000-4-2
9. Characteristics

Table 6. Characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{RWM}$</td>
<td>reverse standoff voltage</td>
<td>$T_{amb} = 25 , ^\circ C$</td>
<td>-</td>
<td>-</td>
<td>12</td>
<td>V</td>
</tr>
<tr>
<td>$V_{BR}$</td>
<td>breakdown voltage</td>
<td>$I_R = 5 , mA; , T_{amb} = 25 , ^\circ C$</td>
<td>14.7</td>
<td>15</td>
<td>15.3</td>
<td>V</td>
</tr>
<tr>
<td>$I_{RM}$</td>
<td>reverse leakage current</td>
<td>$V_{RWM} = 12 , V; , T_{amb} = 25 , ^\circ C$</td>
<td>-</td>
<td>1</td>
<td>50</td>
<td>nA</td>
</tr>
<tr>
<td>$C_d$</td>
<td>diode capacitance</td>
<td>$f = 1 , MHz; , V_R = 0 , V; , T_{amb} = 25 , ^\circ C$</td>
<td>-</td>
<td>38</td>
<td>75</td>
<td>pF</td>
</tr>
<tr>
<td>$V_{CL}$</td>
<td>clamping voltage</td>
<td>$I_{PPM} = 1 , A; , T_{amb} = 25 , ^\circ C$</td>
<td>[1]</td>
<td>-</td>
<td>-</td>
<td>V</td>
</tr>
<tr>
<td>$r_{dif}$</td>
<td>differential resistance</td>
<td>$I_R = 1 , mA; , T_{amb} = 25 , ^\circ C$</td>
<td>[1]</td>
<td>-</td>
<td>-</td>
<td>200</td>
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</tbody>
</table>


Fig. 3. Peak pulse power dissipation as a function of pulse time; typical values

Fig. 4. Relative variation of peak pulse power as a function of junction temperature; typical values
20

30

40

50

0

10

20

30

40

0

5

10

15

20

25

V_{R} (V)

C_{d} (pF)

Fig. 5. Diode capacitance as a function of reverse voltage; typical values

f = 1 MHz; T_{amb} = 25 °C

I_{R} is less than 10 nA at 150 °C

V_{RWM} = 12 V

Fig. 6. Relative variation of reverse leakage current as a function of junction temperature; typical values

I_{R}/I_{R(25°C)}

10

1

10^{-1}

-100

-50

0

50

100

150

T_{j} (°C)
ESD protection diode in SOD523 package

Fig. 7. ESD clamping test setup and waveforms
10. Application information

The device is designed for unidirectional protection of one single data line from the damage caused by ESD and surge pulses. The device may be used on lines where the signal polarity is above or below ground. It provides a surge capability of up to 180 W per line for a 8/20 µs waveform.

Fig. 8. Unidirectional protection of one line

Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Package outline

Fig. 9. Package outline SOD523
12. Soldering

Fig. 10. Reflow soldering footprint for SOD523
## 13. Revision history

<table>
<thead>
<tr>
<th>Data sheet ID</th>
<th>Release date</th>
<th>Data sheet status</th>
<th>Change notice</th>
<th>Supersedes</th>
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<td>20181129</td>
<td>Product data sheet</td>
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<td>PESDXS1UB_SERIES_2</td>
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<td>• The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</td>
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<td></td>
<td></td>
<td>• Legal texts have been adapted to the new company name where appropriate.</td>
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<td></td>
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<td></td>
<td>• Soldering section added.</td>
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<td>• Application information: updated.</td>
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<td>• Figure 9: updated.</td>
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<td>• $T_{\text{amb}}$ value updated from -65°C to -55°C</td>
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<table>
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<td>-</td>
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14. Legal information

Data sheet status

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<th>Product status</th>
<th>Definition</th>
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<td>This document contains data from the objective specification for product development.</td>
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<tr>
<td>Preliminary [short] data sheet</td>
<td>Qualification</td>
<td>This document contains data from the preliminary specification.</td>
</tr>
<tr>
<td>Product [short] data sheet</td>
<td>Production</td>
<td>This document contains the product specification.</td>
</tr>
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</table>

[1] Please consult the most recently issued document before initiating or completing a design. Specifications in this document are subject to change without notice. The reader is advised to confirm that the current version of this document is being used.

[2] The term "short data sheet" is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com/

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