



# PDTA124XT

50 V, 100 mA NPN resistor-equipped transistor;

R1 = 22 k $\Omega$ , R2 = 47 k $\Omega$

23 October 2024

Product data sheet

## 1. General description

NPN Resistor-Equipped Transistor (RET) in a small SOT23 Surface-Mounted Device (SMD) plastic package.

PNP complement: PDTA124XT

## 2. Features and benefits

- Built-in bias resistors
- Simplifies circuit design
- Reduces component count
- Reduces pick and place costs
- AEC-Q101 qualified

## 3. Applications

- General purpose switching and amplification
- Inverter and interface circuits
- Circuit driver

## 4. Quick reference data

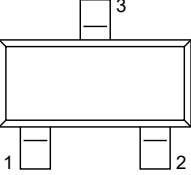
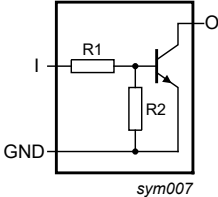
Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	50	V
I <sub>O</sub>	output current		-	-	100	mA
R1	bias resistor 1 (input)	T <sub>amb</sub> = 25 °C	15.4	22	28.6	k $\Omega$
R2/R1	bias resistor ratio		1.7	2.1	2.6	

50 V, 100 mA NPN resistor-equipped transistor; R1 = 22 k $\Omega$ , R2 = 47 k $\Omega$ 

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)	 <p style="text-align: center;"><b>SOT23</b></p>	 <p style="text-align: center;"><i>sym007</i></p>
2	GND	ground (emitter)		
3	O	output (collector)		

## 6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
<a href="#">PDTC124XT</a>	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	<a href="#">SOT23</a>

## 7. Marking

Table 4. Marking codes

Type number	Marking code <sup>[1]</sup>
PDTC124XT	%46

[1] % = placeholder for manufacturing site code

## 8. Limiting values

**Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	50	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	50	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	7	V
V <sub>I</sub>	input voltage			-7	40	V
I <sub>O</sub>	output current			-	100	mA
I <sub>CM</sub>	peak collector current	t <sub>p</sub> ≤ 1 ms; single pulse		-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

## 9. Thermal characteristics

**Table 6. Thermal characteristics**

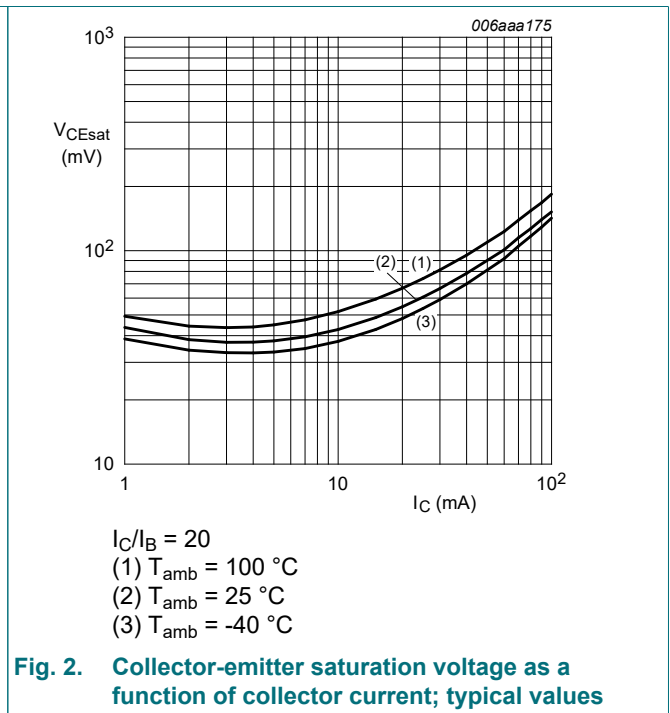
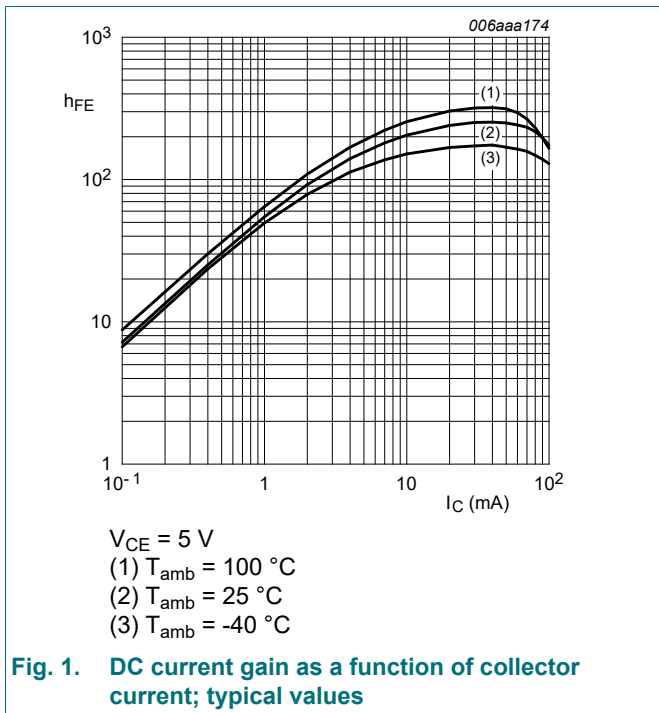
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

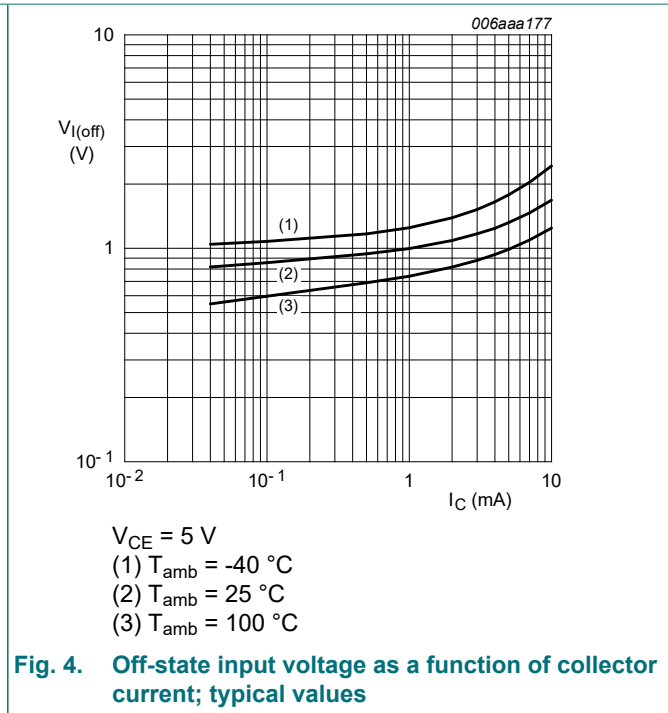
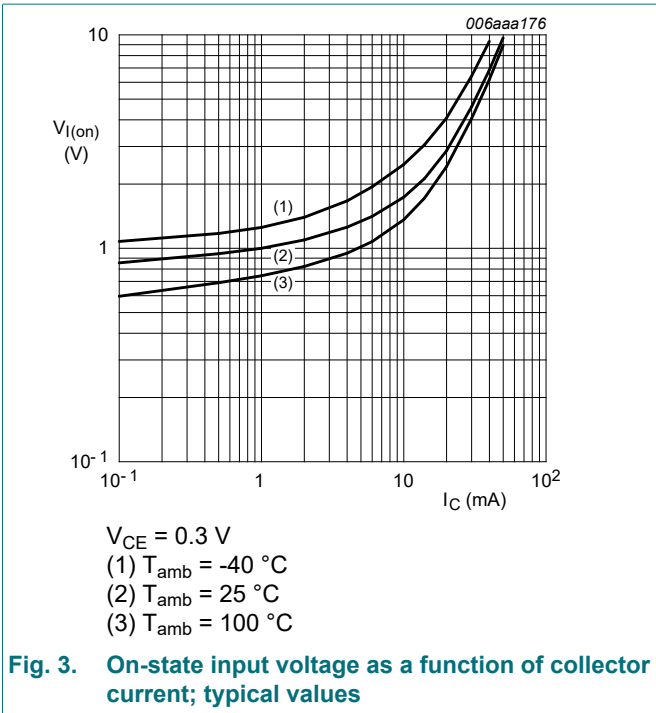
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

## 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$I_{CBO}$	collector-base cut-off current	$V_{CB} = 50\text{ V}; I_E = 0\text{ A}; T_{amb} = 25\text{ °C}$	-	-	100	nA
$I_{CEO}$	collector-emitter cut-off current	$V_{CE} = 30\text{ V}; I_B = 0\text{ A}; T_{amb} = 25\text{ °C}$	-	-	100	nA
		$V_{CE} = 30\text{ V}; I_B = 0\text{ A}; T_j = 150\text{ °C}$	-	-	5	μA
$I_{EBO}$	emitter-base cut-off current	$V_{EB} = 5\text{ V}; I_C = 0\text{ A}; T_{amb} = 25\text{ °C}$	-	-	120	μA
$h_{FE}$	DC current gain	$V_{CE} = 5\text{ V}; I_C = 5\text{ mA}; T_{amb} = 25\text{ °C}$	80	-	-	
$V_{CEsat}$	collector-emitter saturation voltage	$I_C = 10\text{ mA}; I_B = 0.5\text{ mA}; T_{amb} = 25\text{ °C}$	-	-	150	mV
$V_{I(off)}$	off-state input voltage	$V_{CE} = 5\text{ V}; I_C = 100\text{ μA}; T_{amb} = 25\text{ °C}$	-	0.8	0.5	V
$V_{I(on)}$	on-state input voltage	$V_{CE} = 300\text{ mV}; I_C = 2\text{ mA}; T_{amb} = 25\text{ °C}$	2	1.1	-	V
R1	bias resistor 1 (input)	$T_{amb} = 25\text{ °C}$	15.4	22	28.6	kΩ
R2/R1	bias resistor ratio		1.7	2.1	2.6	
$C_c$	collector capacitance	$V_{CB} = 10\text{ V}; I_E = 0\text{ A}; i_e = 0\text{ A}; f = 1\text{ MHz}; T_{amb} = 25\text{ °C}$	-	-	2.5	pF



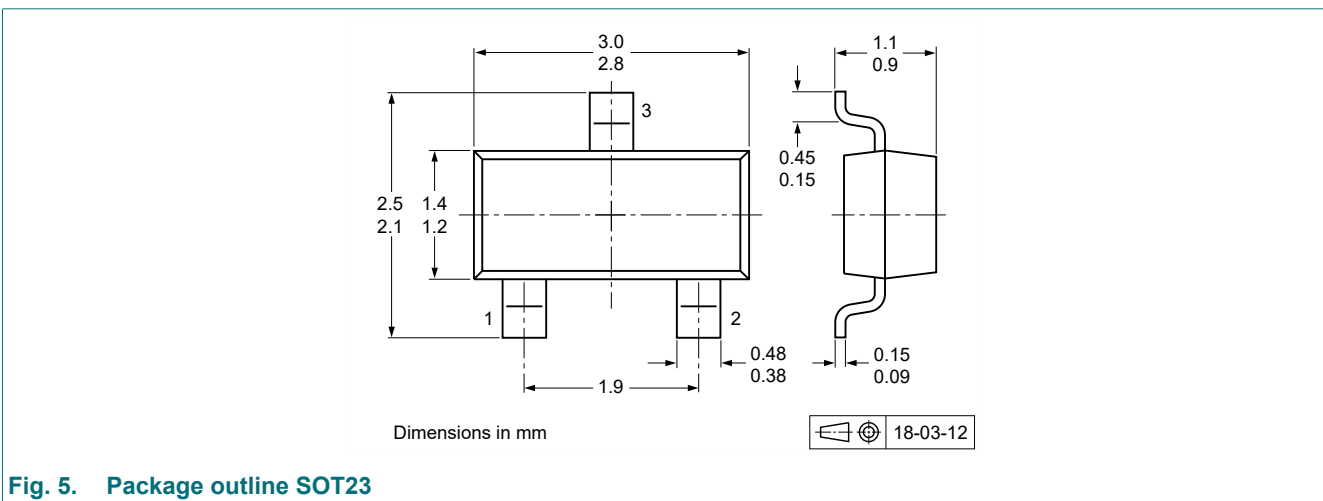


## 11. Test information

### Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline



### 13. Soldering

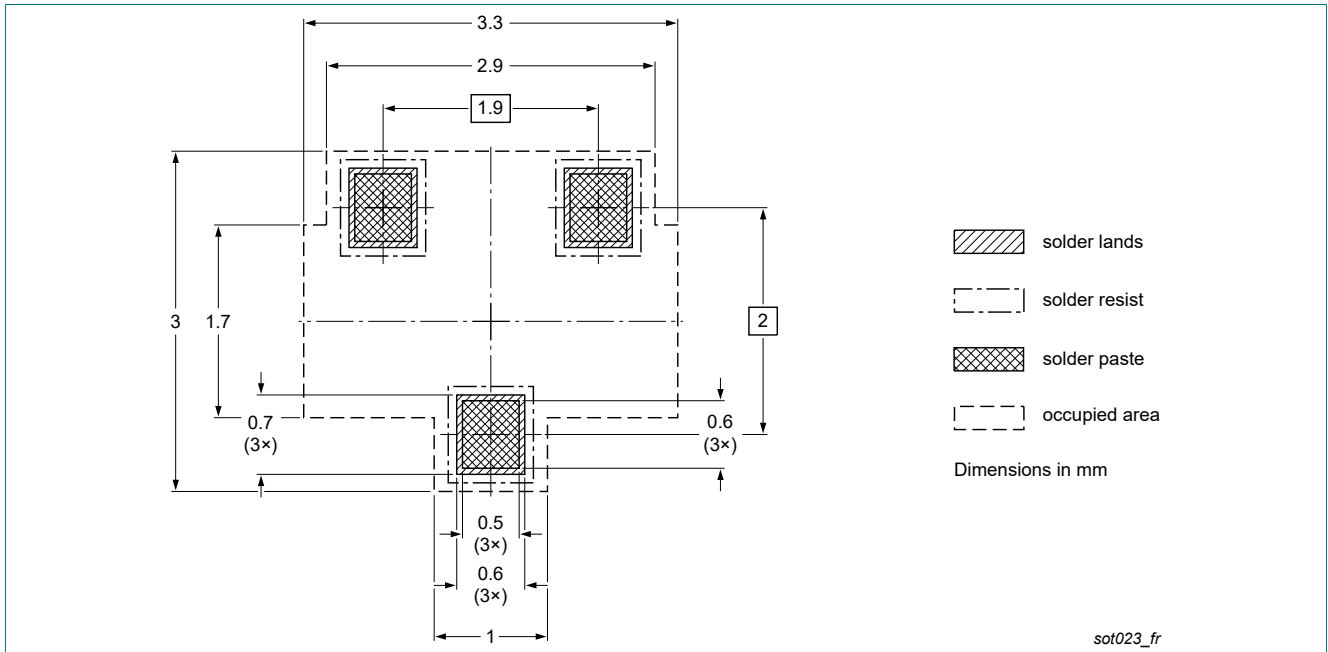


Fig. 6. Reflow soldering footprint for SOT23

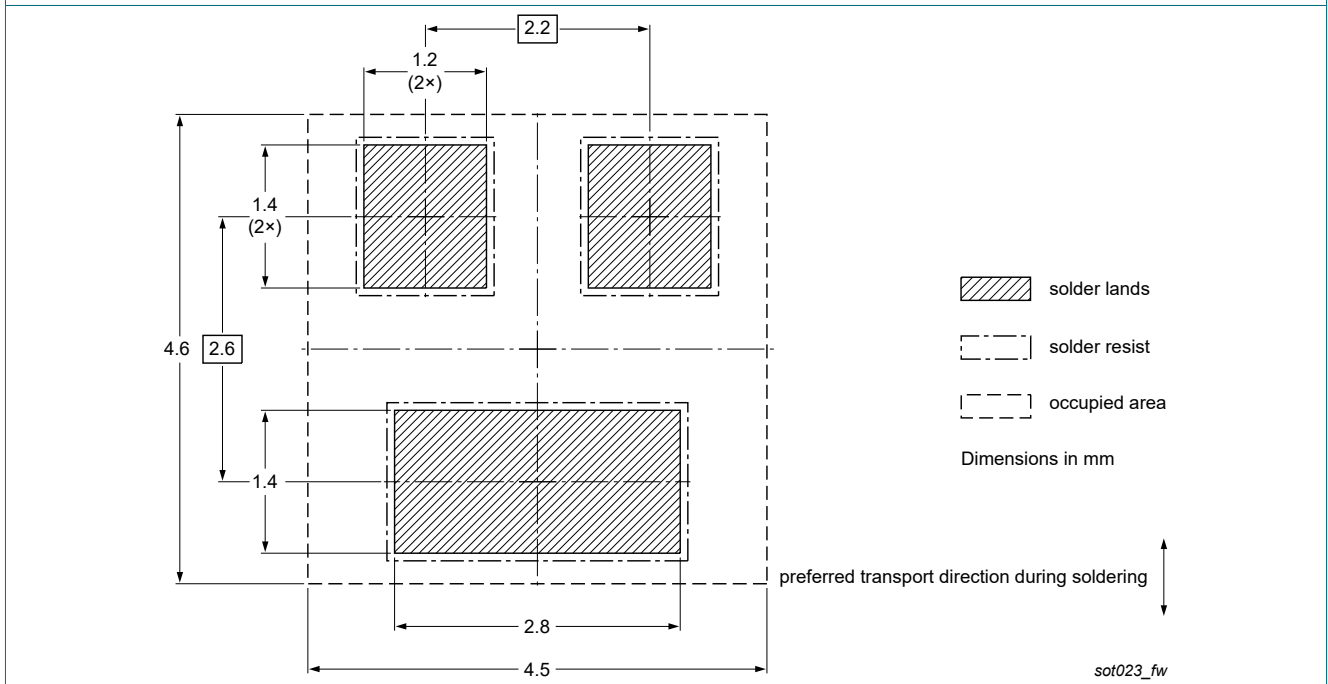


Fig. 7. Wave soldering footprint for SOT23

## 14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PDTC124XT v.8	20241023	Product data sheet	-	PDTC124X_SER_7
Modification:	<ul style="list-style-type: none"> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Family data sheet reduced to single type data sheet.</li> <li>Packing information removed.</li> </ul>			
PDTC124X_SER_7	20091116	Product data sheet	-	PDTC124X_SER_6
PDTC124X_SER_6	20050714	Product data sheet	-	PDTC124X_SERIES_5
PDTC124X_SERIES_5	20040813	Product specification	-	PDTC124X_SERIES_4
PDTC124X_SERIES_4	20030410	Product specification	-	PDTC124XEF_2 PDTC124XE_3
PDTC124XE_3	19990518	Product specification	-	PDTC124XE_2
PDTC124XE_2	19980921	Product specification	-	PDTC124XE_1
PDTC124XE_1	19971215	Product specification	-	-
PDTC124XEF_2	19990518	Preliminary specification	-	PDTC124XEF_1
PDTC124XEF_1	19981111	Preliminary specification	-	-

## 15. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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