

PDTA115EM

PNP resistor-equipped transistor; R1 = 100 kΩ, R2 = 100 kΩ28 March 2024Product data sheet

1. General description

PNP Resistor-Equipped Transistor (RET) in a ultra small SOT883 (SC-101) Surface-Mounted Device (SMD) plastic package.

NPN complement: PDTC115EM

2. Features and benefits

- Built-in bias resistors
- Simplifies circuit design
- Reduces component count
- Reduces pick and place costs
- AEC-Q101 qualified

3. Applications

- General purpose switching and amplification
- Inverter and interface circuits
- Circuit driver

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-50	V
lo	output current		-	-	-20	mA
R1	bias resistor 1 (input)	T _{amb} = 25 °C	70	100	130	kΩ
R2/R1	bias resistor ratio		0.8	1	1.2	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)		
2	G	GND (emitter)	3	
3	O	output (collector)	1 2 Transparent top view DFN1006-3 (SOT883)	R2 Sym003 GND

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6. Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
PDTA115EM		plastic, leadless ultra small package; 3 terminals; 0.35 mm pitch; 1 mm x 0.6 mm x 0.48 mm body	<u>SOT883</u>		

7. Marking

Table 4. Marking codes	
Type number	Marking code
PDTA115EM	F6

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-50	V
V _{CEO}	collector-emitter voltage	open base		-	-50	V
V _{EBO}	emitter-base voltage	open collector		-	-10	V
VI	input voltage			-40	10	V
I _O	output current			-	-20	mA
I _{CM}	peak collector current			-	-100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1] [2]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1]

Reflow soldering is the only recommended soldering method. Device mounted on an FR4 Printed-Circuit Board (PCB) with 60 μm copper strip line, tin-plated and standard footprint. [2]

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	T _{amb} ≤ 25 °C	[1] [2]	-	-	500	K/W

[1]

Reflow soldering is the only recommended soldering method. Device mounted on an FR4 Printed-Circuit Board (PCB) with 60 µm copper strip line, tin-plated and standard footprint. [2]

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = -50 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-100	nA
I _{CEO}	collector-emitter cut-off	V_{CE} = -30 V; I _B = 0 A; T _{amb} = 25 °C	-	-	-100	nA
	current	V _{CE} = -30 V; I _B = 0 A; T _j = 150 °C	-	-	-5	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; \text{ I}_{C} = 0 \text{ A}; \text{ T}_{amb} = 25 \text{ °C}$	-	-	-50	μA
h _{FE}	DC current gain	V _{CE} = -5 V; I _C = -5 mA; T _{amb} = 25 °C	80	-	-	
V _{CEsat}	collector-emitter saturation voltage	I_{C} = -5 mA; I_{B} = -0.25 mA; T_{amb} = 25 °C	-	-	-150	mV
V _{I(off)}	off-state input voltage	V_{CE} = -5 V; I _C = -100 µA; T _{amb} = 25 °C	-	-1.2	-0.5	V
V _{I(on)}	on-state input voltage	V_{CE} = -0.3 V; I _C = -1 mA; T _{amb} = 25 °C	-3	-1.6	-	V
R1	bias resistor 1 (input)	T _{amb} = 25 °C	70	100	130	kΩ
R2/R1	bias resistor ratio		0.8	1	1.2	
C _c	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	3	pF

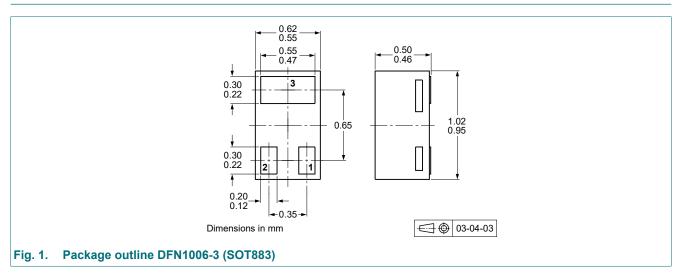
10. Characteristics

11. Test information

Quality information

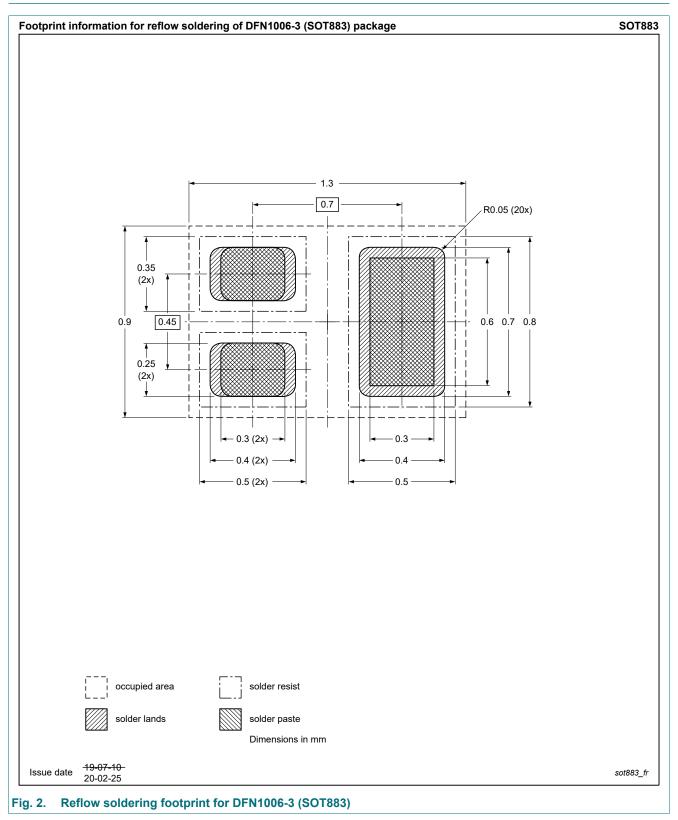
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



PNP resistor-equipped transistor; R1 = 100 k Ω , R2 = 100 k Ω

13. Soldering



14. Revision history

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Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PDTA115EM v.3	20240328	Product data sheet	-	PDTA115E series v.2
Modification:	of Nexperia. Legal texts I Family data 		w company nam	mply with the identity guidelines ne where appropriate.
PDTA115E series v.2	20040730	Product data sheet	-	PDTA115E series v.1
PDTA115E series v.1	20040505	Product specification	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

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