1. General description

PNP/PNP low V\text{CE\text{sat}} Breakthrough In Small Signal (BISS) transistor in a leadless medium power DFN2020-6 (SOT1118) Surface-Mounted Device (SMD) plastic package.

NPN/PNP complement: PBSS4160PANP. NPN/NPN complement: PBSS4160PAN.

2. Features and benefits

- Very low collector-emitter saturation voltage V\text{CE\text{sat}}
- High collector current capability I\text{C} and I\text{CM}
- High collector current gain h\text{FE} at high I\text{C}
- Reduced Printed-Circuit Board (PCB) requirements
- High energy efficiency due to less heat generation
- AEC-Q101 qualified

3. Applications

- Load switch
- Battery-driven devices
- Power management
- Charging circuits
- Power switches (e.g. motors, fans)

4. Quick reference data

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{CEO}$</td>
<td>collector-emitter voltage</td>
<td>open base</td>
<td>-</td>
<td>-</td>
<td>-60</td>
<td>V</td>
</tr>
<tr>
<td>$I_C$</td>
<td>collector current</td>
<td>-</td>
<td>-</td>
<td>-1</td>
<td>A</td>
<td></td>
</tr>
<tr>
<td>$I_{CM}$</td>
<td>peak collector current</td>
<td>single pulse; $t_p \leq 1$ ms</td>
<td>-</td>
<td>-</td>
<td>-1.5</td>
<td>A</td>
</tr>
<tr>
<td>$R_{CE\text{sat}}$</td>
<td>collector-emitter saturation resistance</td>
<td>$I_C = -0.5$ A; $I_B = -50$ mA; pulsed; $t_p \leq 300$ µs; $\delta \leq 0.02$ ; $T_{amb} = 25$ °C</td>
<td>-</td>
<td>-</td>
<td>360</td>
<td>mΩ</td>
</tr>
</tbody>
</table>
5. Pinning information

Table 2. Pinning information

<table>
<thead>
<tr>
<th>Pin</th>
<th>Symbol</th>
<th>Description</th>
<th>Simplified outline</th>
<th>Graphic symbol</th>
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<tbody>
<tr>
<td>1</td>
<td>E1</td>
<td>emitter TR1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>B1</td>
<td>base TR1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>C2</td>
<td>collector TR2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>E2</td>
<td>emitter TR2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>B2</td>
<td>base TR2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>C1</td>
<td>collector TR1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>C1</td>
<td>collector TR1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>8</td>
<td>C2</td>
<td>collector TR2</td>
<td></td>
<td></td>
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6. Ordering information

Table 3. Ordering information

<table>
<thead>
<tr>
<th>Type number</th>
<th>Package</th>
<th>Description</th>
<th>Version</th>
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</thead>
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<tr>
<td>PBSS5160PAP</td>
<td>DFN2020-6</td>
<td>plastic thermal enhanced ultra thin small outline package; no leads; 6 terminals; body 2 x 2 x 0.65 mm</td>
<td>SOT1118</td>
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7. Marking

Table 4. Marking codes

<table>
<thead>
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<th>Type number</th>
<th>Marking code</th>
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<tr>
<td>PBSS5160PAP</td>
<td>2L</td>
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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>(V_{CBO})</td>
<td>collector-base voltage</td>
<td>open emitter</td>
<td>-</td>
<td>-60</td>
<td>V</td>
</tr>
<tr>
<td>(V_{CEO})</td>
<td>collector-emitter voltage</td>
<td>open base</td>
<td>-</td>
<td>-60</td>
<td>V</td>
</tr>
<tr>
<td>(V_{EBO})</td>
<td>emitter-base voltage</td>
<td>open collector</td>
<td>-</td>
<td>-7</td>
<td>V</td>
</tr>
<tr>
<td>(I_C)</td>
<td>collector current</td>
<td>-</td>
<td>-1</td>
<td>A</td>
<td></td>
</tr>
<tr>
<td>(I_{CM})</td>
<td>peak collector current</td>
<td>single pulse; (t_p \leq 1) ms</td>
<td>-1.5</td>
<td>A</td>
<td></td>
</tr>
<tr>
<td>(I_B)</td>
<td>base current</td>
<td>-</td>
<td>-0.3</td>
<td>A</td>
<td></td>
</tr>
</tbody>
</table>
### Symbol | Parameter | Conditions | Min | Max | Unit
--- | --- | --- | --- | --- | ---
$I_{BM}$ | peak base current | single pulse; $t_p \leq 1$ ms | - | -1 | A

### $P_{tot}$ | total power dissipation | $T_{amb} \leq 25$ °C | [1] | - | 370 | mW
[2] | - | 570 | mW
[3] | - | 530 | mW
[4] | - | 700 | mW
[5] | - | 450 | mW
[6] | - | 760 | mW
[7] | - | 700 | mW
[8] | - | 1450 | mW

### Per device

### $P_{tot}$ | total power dissipation | $T_{amb} \leq 25$ °C | [1] | - | 510 | mW
[2] | - | 780 | mW
[3] | - | 730 | mW
[4] | - | 960 | mW
[5] | - | 620 | mW
[6] | - | 1040 | mW
[7] | - | 960 | mW
[8] | - | 2000 | mW

### $T_j$ | junction temperature | - | - | 150 | °C

### $T_{amb}$ | ambient temperature | -55 | 150 | °C

### $T_{stg}$ | storage temperature | -65 | 150 | °C

[3] Device mounted on 4-layer PCB 35 µm copper strip line, tin-plated and standard footprint.
[4] Device mounted on 4-layer PCB 35 µm copper strip line, tin-plated, mounting pad for collector 1 cm$^2$.
[7] Device mounted on 4-layer PCB 70 µm copper strip line, tin-plated and standard footprint.
[8] Device mounted on 4-layer PCB 70 µm copper strip line, tin-plated, mounting pad for collector 1 cm$^2$. 
9. Thermal characteristics

Table 6. Thermal characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>per transistor</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>$R_{th(j-a)}$</td>
<td>thermal resistance from junction to ambient</td>
<td>in free air</td>
<td>[1]</td>
<td>-</td>
<td>-</td>
<td>338 K/W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[2]</td>
<td>-</td>
<td>-</td>
<td>219 K/W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[3]</td>
<td>-</td>
<td>-</td>
<td>236 K/W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[4]</td>
<td>-</td>
<td>-</td>
<td>179 K/W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[5]</td>
<td>-</td>
<td>-</td>
<td>278 K/W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[6]</td>
<td>-</td>
<td>-</td>
<td>164 K/W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[7]</td>
<td>-</td>
<td>-</td>
<td>179 K/W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[8]</td>
<td>-</td>
<td>-</td>
<td>86  K/W</td>
</tr>
<tr>
<td>$R_{th(j-sp)}$</td>
<td>thermal resistance from junction to solder point</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>30  K/W</td>
<td></td>
</tr>
</tbody>
</table>

(1) 4-layer PCB 70 μm, mounting pad for collector 1 cm²
(2) FR4 PCB 70 μm, mounting pad for collector 1 cm²
(3) 4-layer PCB 70 μm, standard footprint
(4) 4-layer PCB 35 μm, mounting pad for collector 1 cm²
(5) FR4 PCB 35 μm, mounting pad for collector 1 cm²
(6) 4-layer PCB 35 μm, standard footprint
(7) FR4 PCB 70 μm, standard footprint
(8) FR4 PCB 35 μm, standard footprint

Fig. 1. Per transistor: power derating curves
### Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>$R_{th(j-a)}$</td>
<td>thermal resistance from junction to ambient</td>
<td>in free air</td>
<td>[1]</td>
<td>-</td>
<td>-</td>
<td>245 K/W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[2]</td>
<td>-</td>
<td>-</td>
<td>160 K/W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[3]</td>
<td>-</td>
<td>-</td>
<td>171 K/W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[4]</td>
<td>-</td>
<td>-</td>
<td>130 K/W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[6]</td>
<td>-</td>
<td>-</td>
<td>120 K/W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[7]</td>
<td>-</td>
<td>-</td>
<td>130 K/W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>[8]</td>
<td>-</td>
<td>-</td>
<td>63 K/W</td>
</tr>
</tbody>
</table>

- **[1]** Device mounted on an FR4 PCB, single-sided 35 µm copper strip line, tin-plated and standard footprint.
- **[2]** Device mounted on an FR4 PCB, single-sided 35 µm copper strip line, tin-plated, mounting pad for collector 1 cm².
- **[3]** Device mounted on 4-layer PCB 35 µm copper strip line, tin-plated and standard footprint.
- **[4]** Device mounted on 4-layer PCB 35 µm copper strip line, tin-plated, mounting pad for collector 1 cm².
- **[5]** Device mounted on an FR4 PCB, single-sided 70 µm copper strip line, tin-plated and standard footprint.
- **[6]** Device mounted on an FR4 PCB, single-sided 70 µm copper strip line, tin-plated, mounting pad for collector 1 cm².
- **[7]** Device mounted on 4-layer PCB 70 µm copper strip line, tin-plated and standard footprint.
- **[8]** Device mounted on 4-layer PCB 70 µm copper strip line, tin-plated, mounting pad for collector 1 cm².

**Fig. 2.** Per transistor: transient thermal impedance from junction to ambient as a function of pulse duration; typical values
Fig. 3. Per transistor: transient thermal impedance from junction to ambient as a function of pulse duration; typical values

Fig. 4. Per transistor: transient thermal impedance from junction to ambient as a function of pulse duration; typical values
Fig. 5. Per transistor: transient thermal impedance from junction to ambient as a function of pulse duration; typical values

4-layer PCB 35 µm, mounting pad for collector 1 cm²

FR4 PCB 70 µm, standard footprint

Fig. 6. Per transistor: transient thermal impedance from junction to ambient as a function of pulse duration; typical values
Fig. 7. Per transistor: transient thermal impedance from junction to ambient as a function of pulse duration; typical values

FR4 PCB 70 µm, mounting pad for collector 1 cm²

Fig. 8. Per transistor: transient thermal impedance from junction to ambient as a function of pulse duration; typical values

4-layer PCB 70 µm, standard footprint
10. Characteristics

Table 7. Characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Per transistor</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>I_CBO</td>
<td>collector-base cut-off</td>
<td>( V_{CB} = -48 , \text{V}; , I_E = 0 , \text{A}; , T_{amb} = 25 , \text{°C} )</td>
<td>-</td>
<td>-</td>
<td>-100</td>
<td>nA</td>
</tr>
<tr>
<td></td>
<td>current</td>
<td>( V_{CB} = -48 , \text{V}; , I_E = 0 , \text{A}; , T_j = 150 , \text{°C} )</td>
<td>-</td>
<td>-</td>
<td>-50</td>
<td>µA</td>
</tr>
<tr>
<td>I_EBO</td>
<td>emitter-base cut-off</td>
<td>( V_{EB} = -5 , \text{V}; , I_C = 0 , \text{A}; , T_{amb} = 25 , \text{°C} )</td>
<td>-</td>
<td>-</td>
<td>-100</td>
<td>nA</td>
</tr>
<tr>
<td>h_FE</td>
<td>DC current gain</td>
<td>( V_{CE} = -2 , \text{V}; , I_C = -100 , \text{mA}; \text{pulsed}; , t_p \leq 300 , \mu\text{s}; , \delta \leq 0.02 ); , T_{amb} = 25 , \text{°C} )</td>
<td>170</td>
<td>245</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>( V_{CE} = -2 , \text{V}; , I_C = -500 , \text{mA}; \text{pulsed}; , t_p \leq 300 , \mu\text{s}; , \delta \leq 0.02 ); , T_{amb} = 25 , \text{°C} )</td>
<td>120</td>
<td>170</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>( V_{CE} = -2 , \text{V}; , I_C = -1 , \text{A}; \text{pulsed}; , t_p \leq 300 , \mu\text{s}; , \delta \leq 0.02 ); , T_{amb} = 25 , \text{°C} )</td>
<td>70</td>
<td>100</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>V_CESat</td>
<td>collector-emitter</td>
<td>( I_C = -500 , \text{mA}; , I_B = -50 , \text{mA}; \text{pulsed}; , t_p \leq 300 , \mu\text{s}; , \delta \leq 0.02 ); , T_{amb} = 25 , \text{°C} )</td>
<td>-</td>
<td>-125</td>
<td>-180</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td>saturation voltage</td>
<td>( I_C = -1 , \text{A}; , I_B = -50 , \text{mA}; \text{pulsed}; , t_p \leq 300 , \mu\text{s}; , \delta \leq 0.02 ); , T_{amb} = 25 , \text{°C} )</td>
<td>-</td>
<td>-390</td>
<td>-550</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( I_C = -1 , \text{A}; , I_B = -100 , \text{mA}; \text{pulsed}; , t_p \leq 300 , \mu\text{s}; , \delta \leq 0.02 ); , T_{amb} = 25 , \text{°C} )</td>
<td>-</td>
<td>-240</td>
<td>-340</td>
<td>mV</td>
</tr>
<tr>
<td>R_CESat</td>
<td>collector-emitter</td>
<td>( I_C = -0.5 , \text{A}; , I_B = -50 , \text{mA}; \text{pulsed}; , t_p \leq 300 , \mu\text{s}; , \delta \leq 0.02 ); , T_{amb} = 25 , \text{°C} )</td>
<td>-</td>
<td>-</td>
<td>360</td>
<td>mΩ</td>
</tr>
</tbody>
</table>
### Symbol | Parameter | Conditions | Min | Typ | Max | Unit
--- | --- | --- | --- | --- | --- | ---
$V_{BE_{sat}}$ | base-emitter saturation voltage | $I_C = -500 \, mA; \, I_B = -50 \, mA; \, \text{pulsed;} \, \tau_p \leq 300 \, \mu s; \, \delta \leq 0.02; \, T_{amb} = 25 \, ^\circ C$ |  |  | -1 | V
$V_{BE_{sat}}$ | base-emitter saturation voltage | $I_C = -1 \, A; \, I_B = -50 \, mA; \, T_{amb} = 25 \, ^\circ C$ |  |  | -1 | V
$V_{BE_{sat}}$ | base-emitter saturation voltage | $I_C = -1 \, A; \, I_B = -100 \, mA; \, \text{pulsed;} \, \tau_p \leq 300 \, \mu s; \, \delta \leq 0.02; \, T_{amb} = 25 \, ^\circ C$ |  |  | -1.1 | V
$V_{BE_{on}}$ | base-emitter turn-on voltage | $V_{CE} = -2 \, V; \, I_C = -0.5 \, A; \, \text{pulsed;} \, \tau_p \leq 300 \, \mu s; \, \delta \leq 0.02; \, T_{amb} = 25 \, ^\circ C$ |  |  | -0.9 | V
$t_d$ | delay time | $V_{CC} = -10 \, V; \, I_C = -0.5 \, A; \, I_{B_{on}} = -25 \, mA; \, I_{B_{off}} = 25 \, mA; \, T_{amb} = 25 \, ^\circ C$ | - | 15 | - | ns
$t_r$ | rise time |  |  | 40 | - | ns
$t_{on}$ | turn-on time | - | 55 | - | ns
$t_s$ | storage time | - | 95 | - | ns
$t_f$ | fall time | - | 40 | - | ns
$t_{off}$ | turn-off time | - | 135 | - | ns
$f_T$ | transition frequency | $V_{CE} = -10 \, V; \, I_C = -50 \, mA; \, f = 100 \, MHz; \, T_{amb} = 25 \, ^\circ C$ | 65 | 125 | - | MHz
$C_C$ | collector capacitance | $V_{CB} = -10 \, V; \, I_C = 0 \, A; \, I_E = 0 \, A; \, f = 1 \, MHz; \, T_{amb} = 25 \, ^\circ C$ |  | 9.5 | 13 | pF

---

**Fig. 10.** DC current gain as a function of collector current; typical values

**Fig. 11.** Collector current as a function of collector-emitter voltage; typical values
**V_{CE} = -2 V**

1. \(T_{\text{amb}} = -55 \, ^{\circ}C\)
2. \(T_{\text{amb}} = 25 \, ^{\circ}C\)
3. \(T_{\text{amb}} = 100 \, ^{\circ}C\)

**Fig. 12. Base-emitter voltage as a function of collector current; typical values**

**I_{C}/I_{B} = 20**

1. \(T_{\text{amb}} = 100 \, ^{\circ}C\)
2. \(I_{C}/I_{B} = 50\)
3. \(I_{C}/I_{B} = 10\)

**Fig. 13. Base-emitter saturation voltage as a function of collector current; typical values**

**T_{\text{amb}} = 25 \, ^{\circ}C**

1. \(I_{C}/I_{B} = 100\)
2. \(I_{C}/I_{B} = 50\)
3. \(I_{C}/I_{B} = 10\)

**Fig. 14. Collector-emitter saturation voltage as a function of collector current; typical values**

**Fig. 15. Collector-emitter saturation voltage as a function of collector current; typical values**
Fig. 16. Collector-emitter saturation resistance as a function of collector current; typical values

- $I_C/I_B = 20$
- $T_{amb} = 100 \, ^\circ C$
- $T_{amb} = 25 \, ^\circ C$
- $T_{amb} = -55 \, ^\circ C$

Fig. 17. Collector-emitter saturation resistance as a function of collector current; typical values

- $T_{amb} = 25 \, ^\circ C$
- $I_C/I_B = 100$
- $I_C/I_B = 50$
- $I_C/I_B = 10$
11. Test information

Fig. 18. BISS transistor switching time definition

Fig. 19. Test circuit for switching times

11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.
12. Package outline

Fig. 20. Package outline DFN2020-6 (SOT1118)

13. Soldering

Fig. 21. Reflow soldering footprint for DFN2020-6 (SOT1118)

14. Revision history

Table 8. Revision history

<table>
<thead>
<tr>
<th>Data sheet ID</th>
<th>Release date</th>
<th>Data sheet status</th>
<th>Change notice</th>
<th>Supersedes</th>
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<tbody>
<tr>
<td>PBSS5160PAP v.1</td>
<td>20130123</td>
<td>Product data sheet</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>
15. Legal information

15.1 Data sheet status

<table>
<thead>
<tr>
<th>Document status</th>
<th>Product status</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>Objective [short] data sheet</td>
<td>Development</td>
<td>This document contains data from the objective specification for product development.</td>
</tr>
<tr>
<td>Preliminary [short] data sheet</td>
<td>Qualification</td>
<td>This document contains data from the preliminary specification.</td>
</tr>
<tr>
<td>Product [short] data sheet</td>
<td>Production</td>
<td>This document contains the product specification.</td>
</tr>
</tbody>
</table>

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term "short data sheet" is explained in section "Definitions".
[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

15.2 Definitions

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