



PBSS4250X-Q

50 V, 2 A NPN low V_{CEsat} transistor

8 July 2025

Product data sheet

1. General description

NPN low V_{CEsat} transistor in a SOT89 (SC-62) flat lead Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS5250X

2. Features and benefits

- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability: I_C and I_{CM}
- Higher efficiency leading to less heat generation
- Reduced printed-circuit board requirements
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Power management
 - DC/DC converters
 - Supply line switching
 - Battery charger
 - LCD backlighting
- Peripheral drivers
 - Driver in low supply voltage applications (e.g. lamps and LEDs)
 - Inductive load driver (e.g. relays, buzzers and motors)

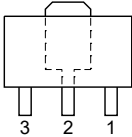
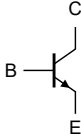
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	50	V
I _C	collector current		-	-	2	A
h _{FE}	DC current gain	V _{CE} = 2 V; I _C = 0.1 A; T _{amb} = 25 °C	300	-	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter	 SOT89	 sym123
2	C	collector		
3	B	base		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS4250X-Q	SOT89	plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	SOT89

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PBSS4250X-Q	%1M

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	50	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	2	A
I _{CM}	peak collector current	limited by T _{j(max)} ; single pulse; t _p ≤ 1 ms		-	5	A
I _B	base current			-	0.5	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	550	mW
			[2]	-	1	W
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	225	K/W
			[2]	-	-	125	K/W
			[3]	-	-	90	K/W
			[4]	-	-	80	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	16	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².
[4] Device mounted on a ceramic PCB, 7 cm², single-sided copper, tin-plated.

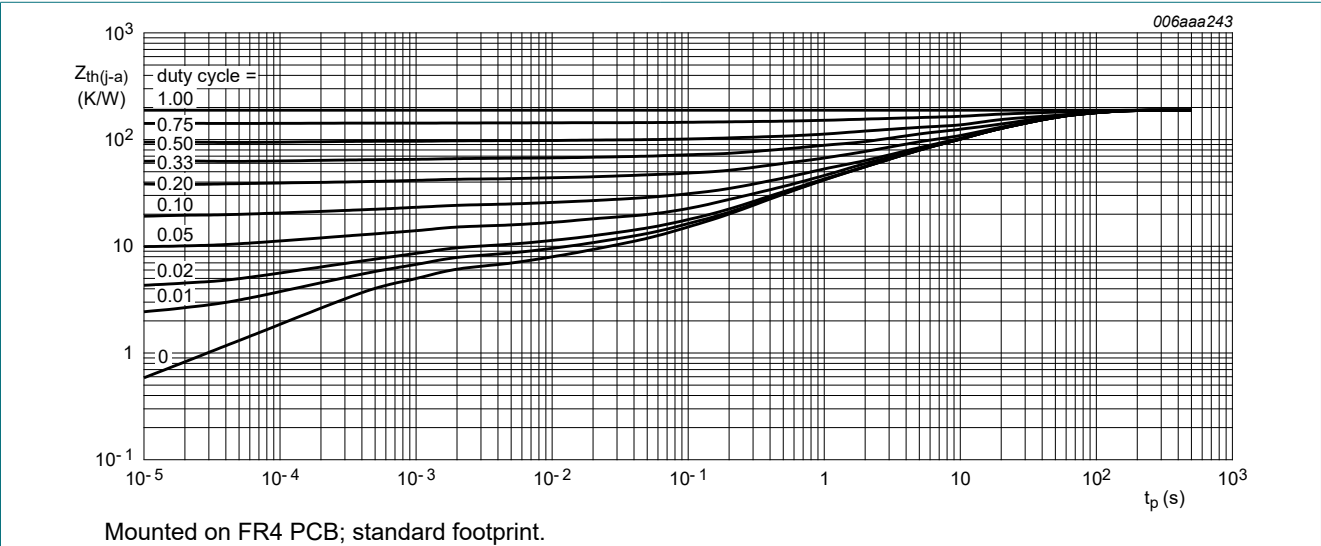


Fig. 1. Transient thermal impedance as a function of pulse duration; typical values

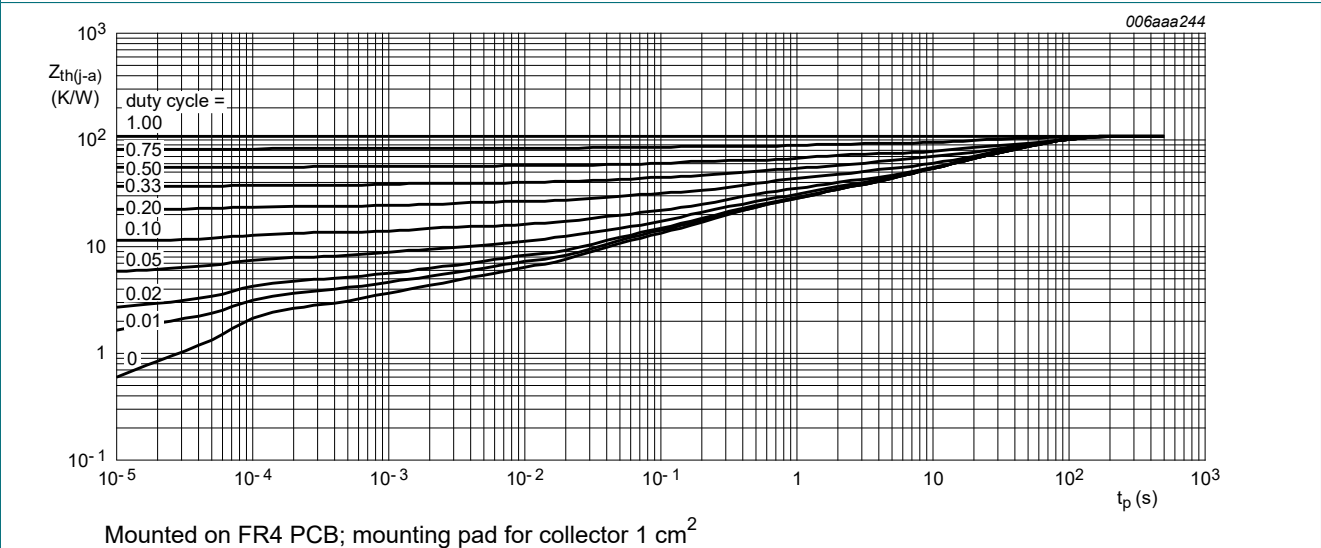
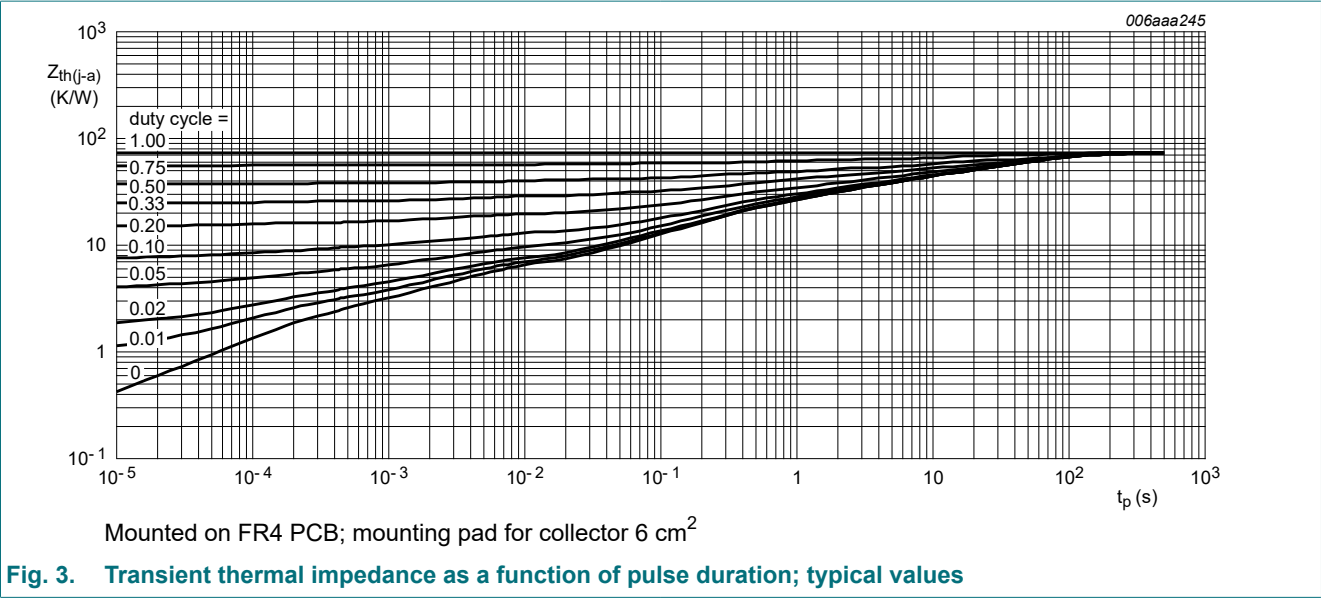


Fig. 2. Transient thermal impedance as a function of pulse duration; typical values



10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = 50 V; I _E = 0 A; T _{amb} = 25 °C	-	-	100	nA
		V _{CB} = 50 V; I _E = 0 A; T _J = 150 °C	-	-	50	μA
I _{CES}	collector-emitter cut-off current	V _{CE} = 50 V; V _{BE} = 0 V; T _{amb} = 25 °C	-	-	100	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 2 V; I _C = 0.1 A; T _{amb} = 25 °C	300	-	-	
		V _{CE} = 2 V; I _C = 0.5 A; T _{amb} = 25 °C	300	-	-	
		V _{CE} = 2 V; I _C = 1 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	300	-	-	
		V _{CE} = 2 V; I _C = 2 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	150	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 0.5 A; I _B = 50 mA; T _{amb} = 25 °C	-	-	90	mV
		I _C = 1 A; I _B = 50 mA; T _{amb} = 25 °C	-	-	250	mV
		I _C = 2 A; I _B = 100 mA; T _{amb} = 25 °C	-	-	380	mV
		I _C = 2 A; I _B = 200 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-	320	mV
R _{CEsat}	collector-emitter saturation resistance		-	-	160	mΩ
V _{BEsat}	base-emitter saturation voltage	I _C = 2 A; I _B = 100 mA; T _{amb} = 25 °C	-	-	1.1	V
V _{BEon}	base-emitter turn-on voltage	V _{CE} = 2 V; I _C = 1 A; T _{amb} = 25 °C	-	-	1.1	V
f _T	transition frequency	V _{CE} = 5 V; I _C = 100 mA; f = 100 MHz; T _{amb} = 25 °C	100	-	-	MHz
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	25	pF

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 -Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

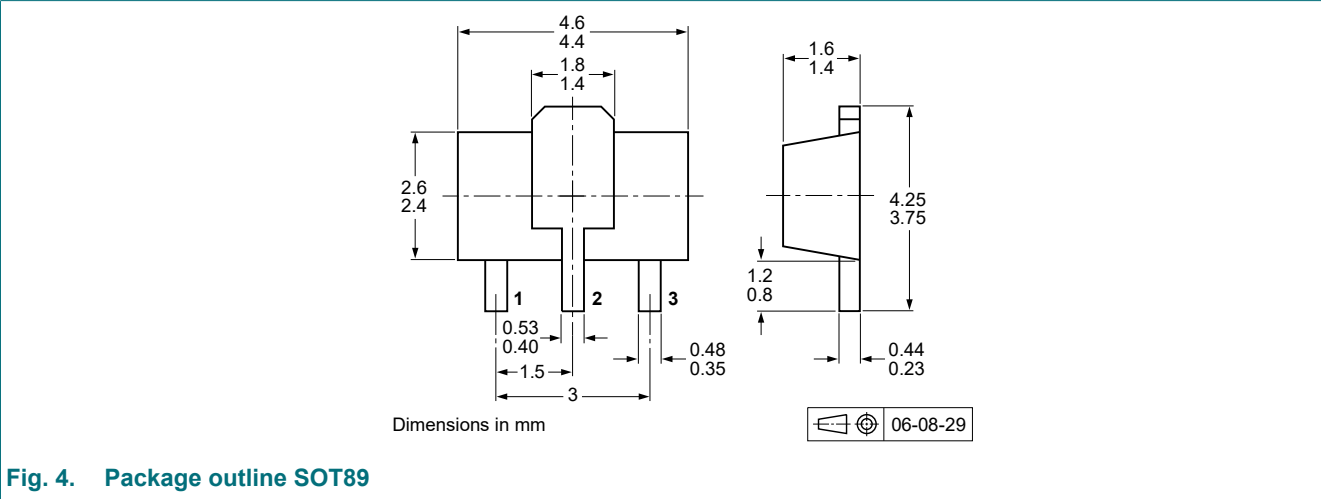


Fig. 4. Package outline SOT89

13. Soldering

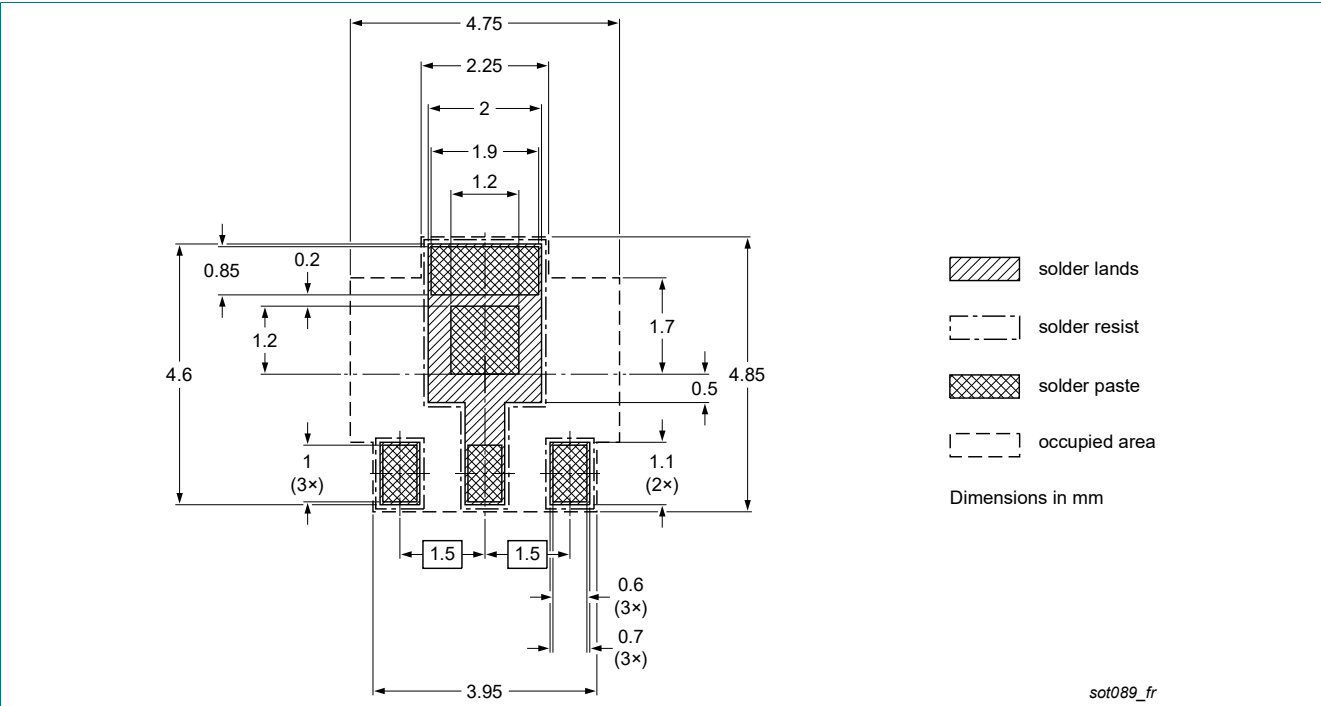


Fig. 5. Reflow soldering footprint for SOT89

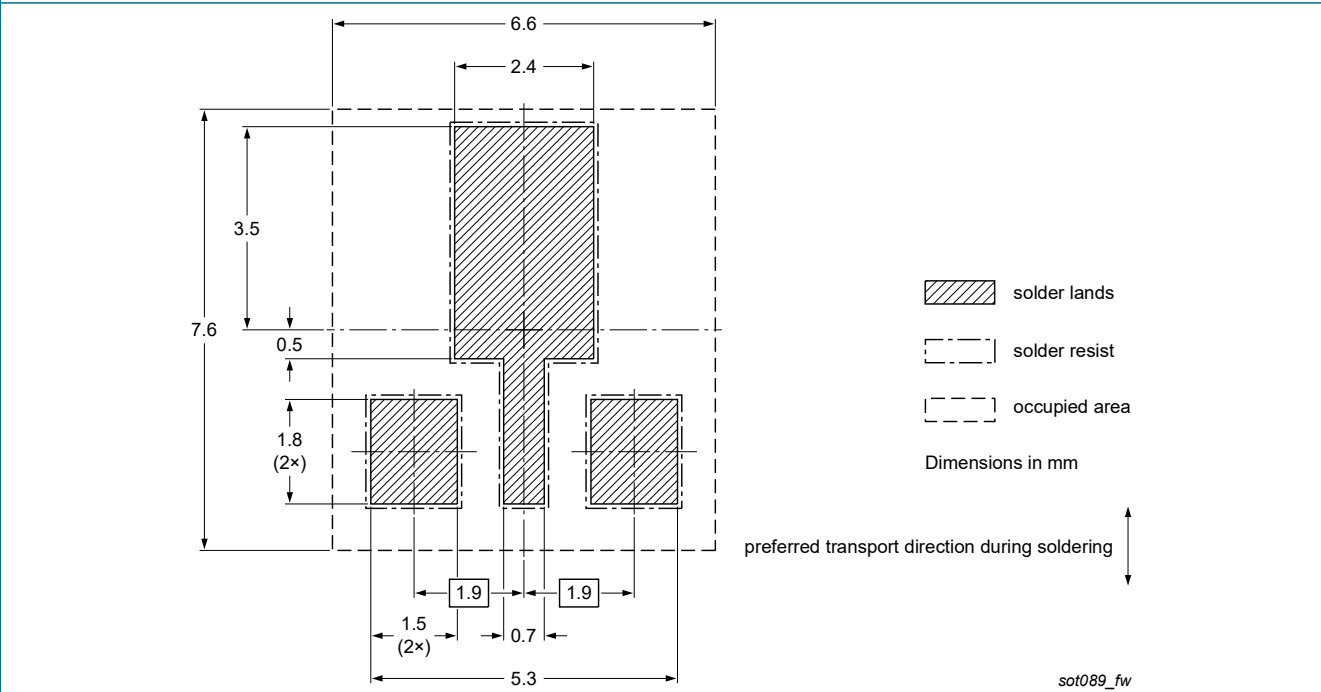


Fig. 6. Wave soldering footprint for SOT89

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4250X-Q v.1	20250708	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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