

PBSS4041PT

60 V, 2.7 A PNP low VCEsat transistor

15 September 2023

Product data sheet

1. General description

PNP low V_{CEsat} transistor in a SOT23 (TO-236AB) small Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS4041NT

2. Features and benefits

- Very low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- · High energy efficiency due to less heat generation
- · Smaller required Printed-Circuit Board (PCB) area than for conventional transistors
- AEC-Q101 qualified

3. Applications

- Loadswitch
- · Battery-driven devices
- · Power management
- Charging circuits
- · Power switches (e.g. motors, fans)

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-60	V
I _C	collector current		-	-	-2.7	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	-8	А
R _{CEsat}	collector-emitter saturation resistance	I_C = -3 A; I_B = -300 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	80	120	mΩ



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5. Pinning information

Table 2. Pinning information

Symbol	Description	Simplified outline	Graphic symbol
В	base	3	С
Е	emitter		j
С	collector		В — (
			 E
		1	sym013
	B E	B base E emitter	B base E emitter C collector

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PBSS4041PT	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23		

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PBSS4041PT	%BL

[1] % = placeholder for manufacturing site code

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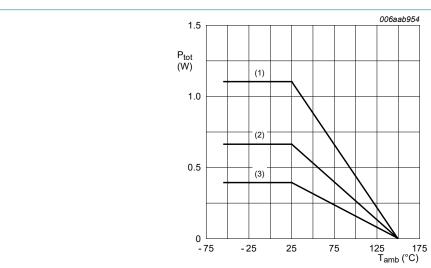
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-60	V
V _{CEO}	collector-emitter voltage	open base		-	-60	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-2.7	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-8	А
I _B	base current			-	-1	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	390	mW
			[2]	-	660	mW
			[3]	-	1.1	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².
- [3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.



- (1) Ceramic PCB, Al₂O₃, standard footprint
- (2) FR4 PCB, mounting pad for collector 1 cm²
- (3) FR4 PCB, standard footprint

Fig. 1. Power derating curves

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9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from		[1]	-	-	320	K/W
	junction to ambient		[2]	-	-	190	K/W
			[3]	-	-	115	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	62	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².
- [3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

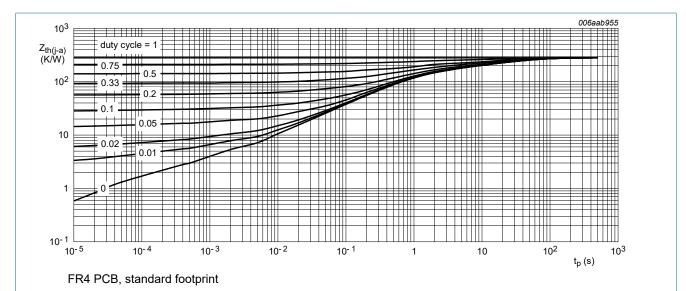


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

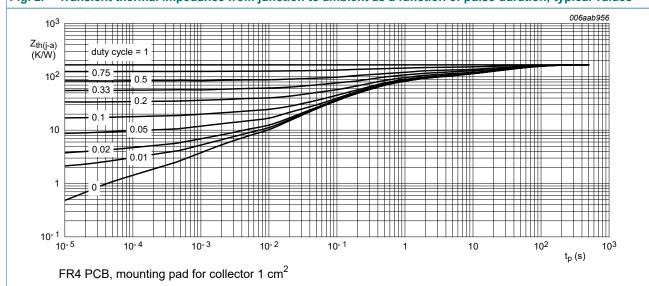
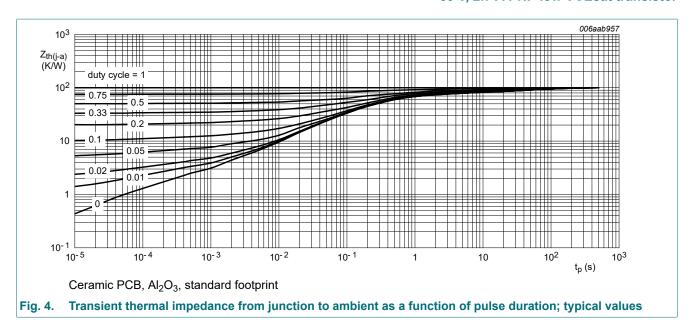


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{СВО}	collector-base cut-off	V _{CB} = -60 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-100	nA
	current	V _{CB} = -60 V; I _E = 0 A; T _j = 150 °C	-	-	-55	μΑ
ЕВО	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-100	nA
CES	collector-emitter cut-off current	V _{CE} = -48 V; V _{BE} = 0 V; T _{amb} = 25 °C	-	-	-100	nA
J ^E	DC current gain	V _{CE} = -2 V; I _C = -500 mA; T _{amb} = 25 °C	200	320	-	
		V_{CE} = -2 V; I_{C} = -1 A; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	150	270	-	
		V_{CE} = -2 V; I_{C} = -2 A; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	120	180	-	
		V_{CE} = -2 V; I_{C} = -4 A; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	35	55	-	
√ _{CEsat}	collector-emitter	I _C = -500 mA; I _B = -50 mA; T _{amb} = 25 °C	-	-49	-75	mV
	saturation voltage	I_C = -1 A; I_B = -50 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-100	-150	mV
		I_C = -1 A; I_B = -10 mA; pulsed; $t_p \le$ 300 μs; $\delta \le$ 0.02; T_{amb} = 25 °C	-	-260	-390	mV
		I_C = -2 A; I_B = -40 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-420	-600	mV
		I_C = -3 A; I_B = -300 mA; pulsed; $t_p \le$	-	-240	-360	mV
R _{CEsat}	collector-emitter saturation resistance	300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	80	120	mΩ
V _{BEsat}	base-emitter saturation voltage	I_C = -1 A; I_B = -100 mA; pulsed; $t_p \le$ 300 μs; $\delta \le$ 0.02; T_{amb} = 25 °C	-	-0.9	-1	V
		I_C = -3 A; I_B = -300 mA; pulsed; $t_p \le$ 300 μs; $\delta \le$ 0.02; T_{amb} = 25 °C	-	-1.04	-1.15	V
V _{BEon}	base-emitter turn-on voltage	V _{CE} = -2 V; I _C = -2 A; T _{amb} = 25 °C	-	-0.84	-0.9	V
d	delay time	V _{CC} = -12.5 V; I _C = -1 A; I _{Bon} = -0.05 A;	-	18	-	ns
·r	rise time	I _{Boff} = 0.05 A; T _{amb} = 25 °C	-	70	-	ns
on	turn-on time		-	88	-	ns
·s	storage time		-	350	-	ns
f	fall time		-	80	-	ns
· ·off	turn-off time		-	430	-	ns
fΤ	transition frequency	V_{CE} = -10 V; I_{C} = -100 mA; f = 100 MHz; T_{amb} = 25 °C	-	150	-	MHz
C _c	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	38	-	pF

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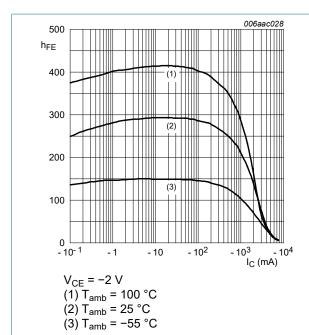


Fig. 5. DC current gain as a function of collector current; typical values

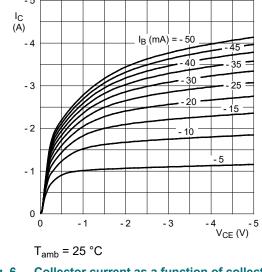


Fig. 6. Collector current as a function of collectoremitter voltage; typical values

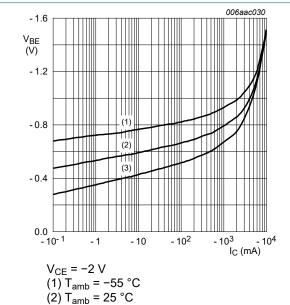
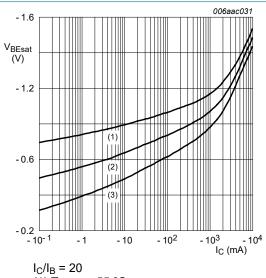


Fig. 7. Base-emitter voltage as a function of collector current; typical values

(3) $T_{amb} = 100 \, ^{\circ}C$



 $I_C/I_B = 20$ (1) $T_{amb} = -55 \,^{\circ}C$ (2) $T_{amb} = 25 \,^{\circ}C$ (3) $T_{amb} = 100 \,^{\circ}C$

Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values

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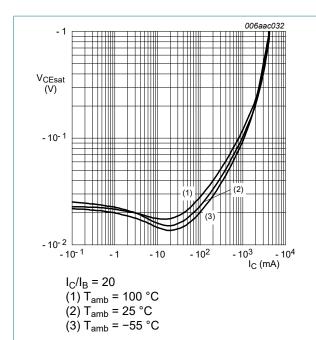


Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values

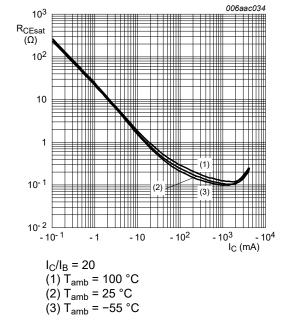


Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values

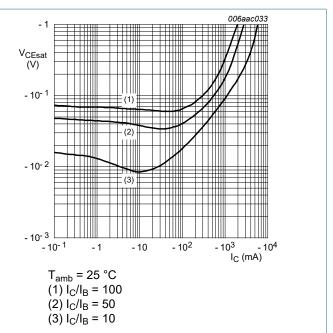


Fig. 10. Collector-emitter saturation voltage as a function of collector current; typical values

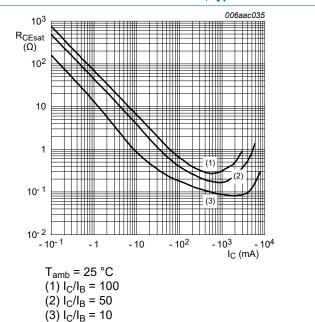
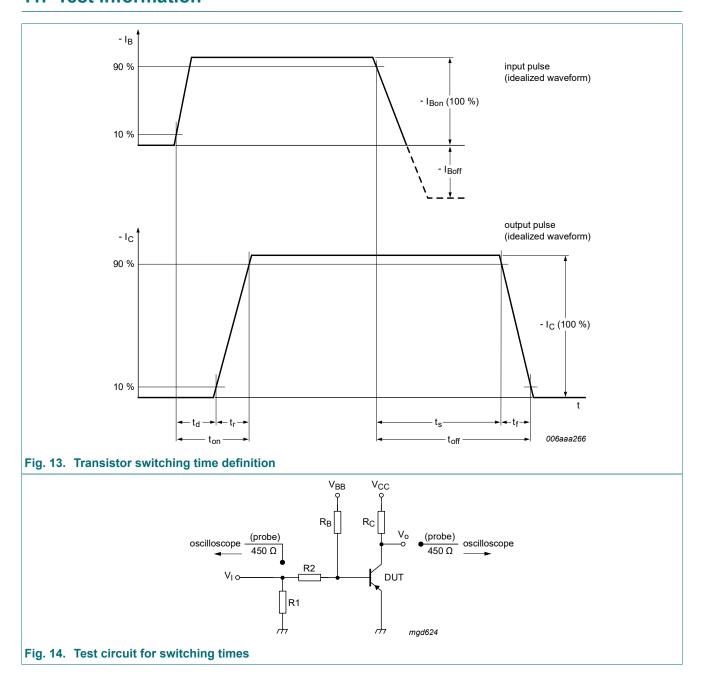


Fig. 12. Collector-emitter saturation resistance as a function of collector current; typical values

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11. Test information

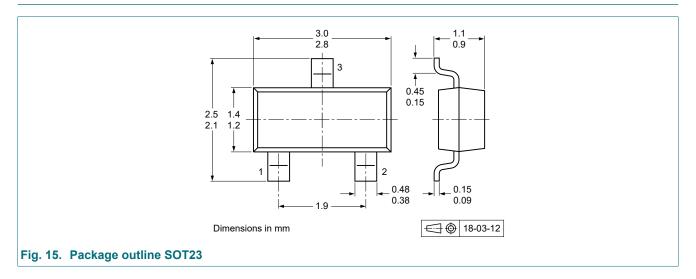


Quality information

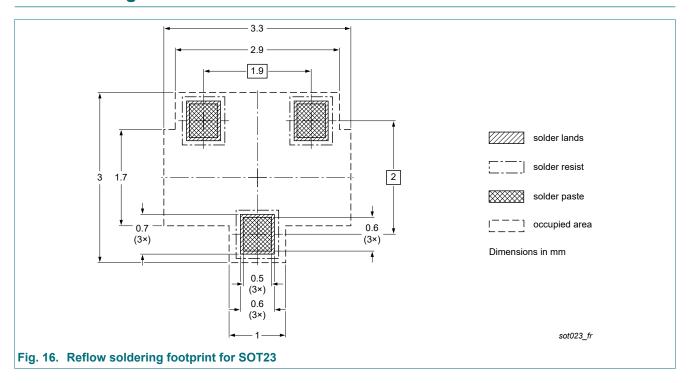
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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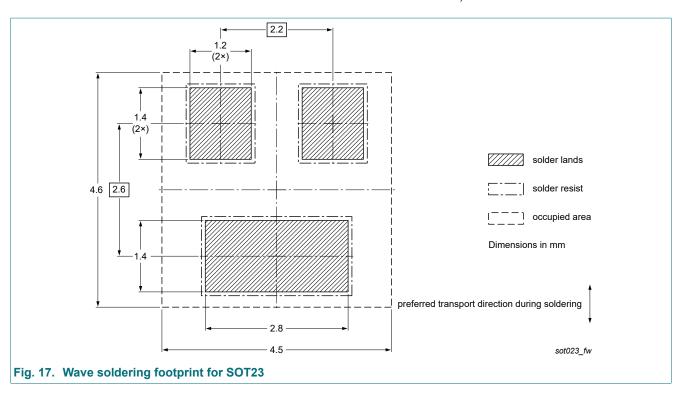
12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PBSS4041PT v.3	20230915	Product data sheet	-	PBSS4041PT_2		
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Section "Packing information" removed. 					
PBSS4041PT_2	20100131	Product data sheet	-	PBSS4041PT_1		
PBSS4041PT_1	20100131	Product data sheet	-	-		

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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For more information, please visit: http://www.nexperia.com For sales office addresses, please send an email to: salesaddresses@nexperia.com Date of release: 15 September 2023

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