

60 V, 3.8 A NPN low VCEsat transistor

15 September 2023

1. General description

NPN low V_{CEsat} transistor in a SOT23 (TO-236AB) small Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS4041PT

2. Features and benefits

- Very low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- High energy efficiency due to less heat generation
- Smaller required Printed-Circuit Board (PCB) area than for conventional transistors
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

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- Loadswitch
- Battery-driven devices
- Power management
- Charging circuits
- Power switches (e.g. motors, fans)

4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	60	V
I _C	collector current			-	-	3.8	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	8	А
R _{CEsat}	collector-emitter saturation resistance	I_C = 3 A; I_B = 300 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C		-	46	66	mΩ

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5. Pinning information

Table 2	2. Pinning info	ormation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	C
2	E	emitter		J
3	С	collector		B
			1 🛄 🛄 2 SOT23	sym021

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PBSS4041NT-Q		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	<u>SOT23</u>		

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PBSS4041NT-Q	%BK

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

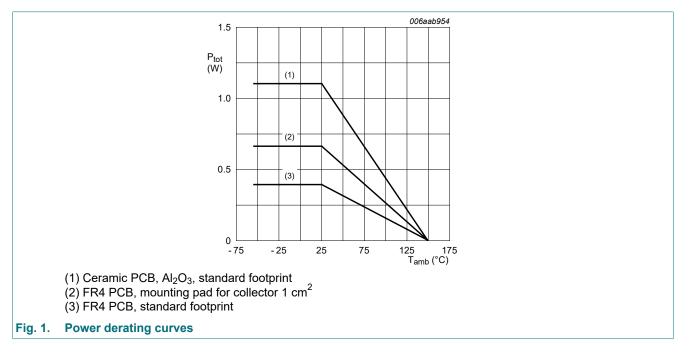
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	60	V
V _{CEO}	collector-emitter voltage	open base		-	60	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
l _C	collector current			-	3.8	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	8	А
I _B	base current			-	1	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	390	mW
			[2]	-	660	mW
			[3]	-	1.1	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

[3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.



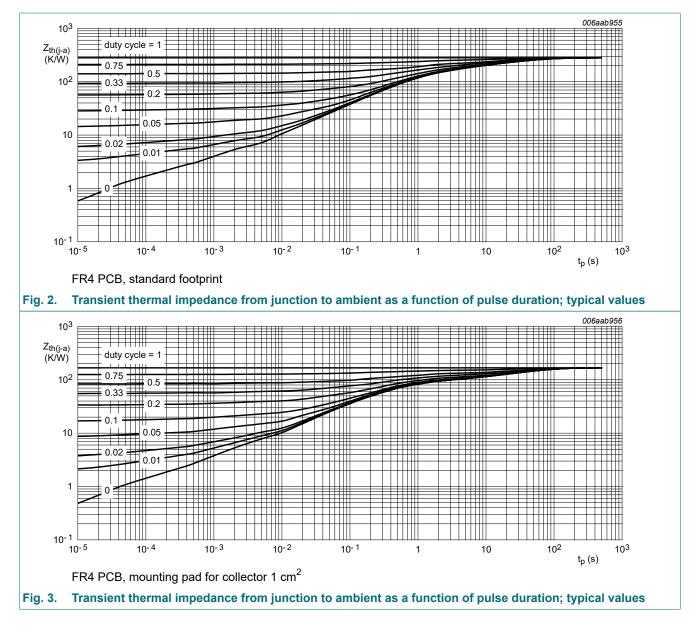
9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
-uiu-a)	thermal resistance from		[1]	-	-	320	K/W
	junction to ambient		[2]	-	-	190	K/W
			[3]	-	-	115	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	62	K/W

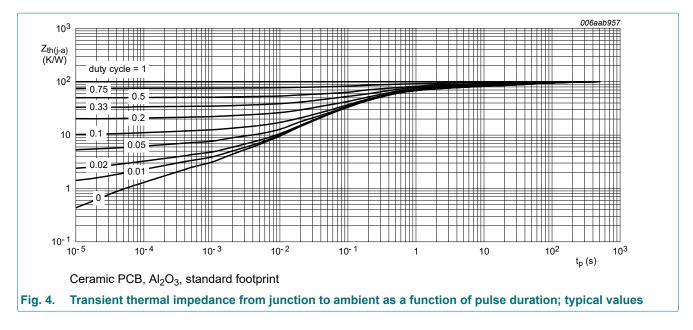
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

[3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.



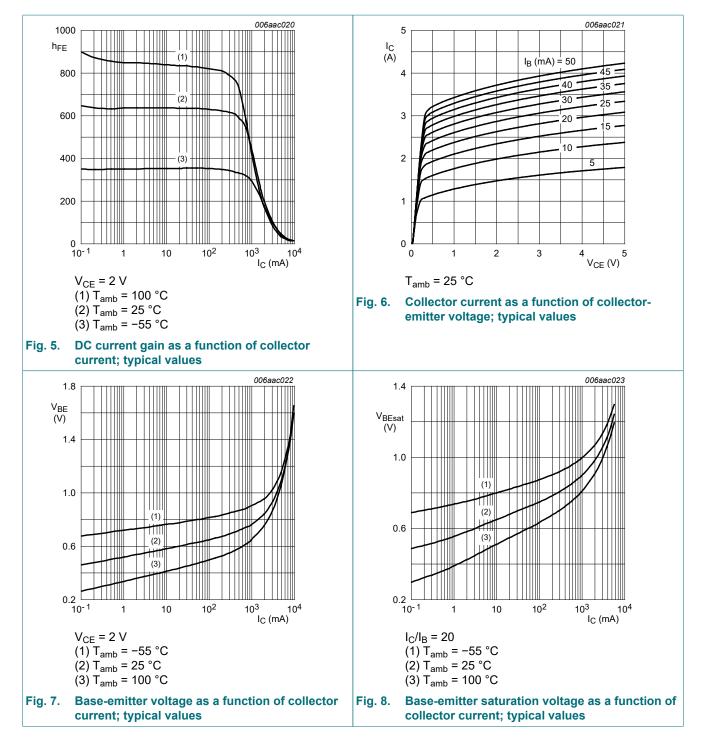
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10. Characteristics

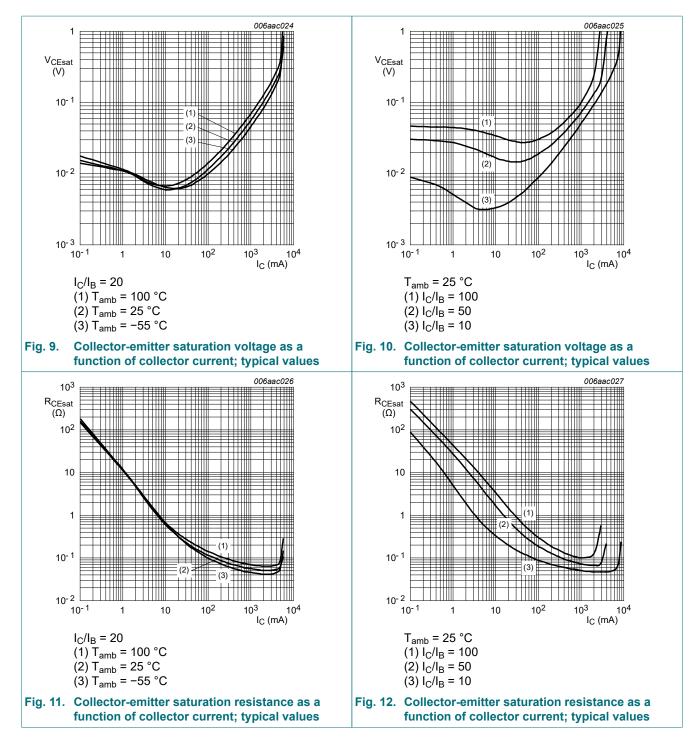
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
I _{CBO}	collector-base cut-off	V _{CB} = 60 V; I _E = 0 A; T _{amb} = 25 °C	-	-	100	nA
current		V _{CB} = 60 V; I _E = 0 A; T _j = 150 °C	-	-	50	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
I _{CES}	collector-emitter cut-off current	V_{CE} = 48 V; V_{BE} = 0 V; T_{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 2 V; I _C = 500 mA; T _{amb} = 25 °C	300	500	-	
		V_{CE} = 2 V; I _C = 1 A; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C	250	400	-	
		$ V_{CE} = 2 \text{ V}; I_C = 2 \text{ A}; \text{ pulsed}; t_p \leq 300 \mu\text{s}; \\ \delta \leq 0.02; T_{amb} = 25 ^\circ\text{C} $	120	170	-	
		$ V_{CE} = 2 \text{ V; } I_C = 4 \text{ A; pulsed; } t_p \le 300 \mu\text{s;} \\ \delta \le 0.02; T_{amb} = 25 ^\circ\text{C} $	30	50	-	
V _{CEsat}	collector-emitter	I_{C} = 500 mA; I_{B} = 50 mA; T_{amb} = 25 °C	-	29	40	mV
	saturation voltage	I_{C} = 1 A; I_{B} = 50 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	57	80	mV
		I_{C} = 1 A; I_{B} = 10 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	100	140	mV
		I_C = 2 A; I_B = 40 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	135	190	mV
		$I_C = 3$ A; $I_B = 300$ mA; pulsed; $t_p \le 300$ μs; $\delta \le 0.02$; $T_{amb} = 25$ °C	-	140	200	mV
		I_{C} = 4 A; I_{B} = 200 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	215	300	mV
R _{CEsat}	collector-emitter saturation resistance	I_C = 3 A; I_B = 300 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	46	66	mΩ
V _{BEsat}	base-emitter saturation voltage	I_{C} = 1 A; I_{B} = 100 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	0.94	1.05	V
		I_C = 3 A; I_B = 300 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	1.1	1.2	V
		I_{C} = 4 A; I_{B} = 400 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	1.2	1.3	V
V _{BEon}	base-emitter turn-on voltage	V _{CE} = 2 V; I _C = 2 A; T _{amb} = 25 °C	-	0.83	0.9	V
t _d	delay time	V _{CC} = 12.5 V; I _C = 1 A; I _{Bon} = 0.05 A;	-	13	-	ns
t _r	rise time	I _{Boff} = -0.05 A; T _{amb} = 25 °C	-	140	-	ns
t _{on}	turn-on time		-	153	-	ns
t _s	storage time		-	735	-	ns
t _f	fall time		-	320	-	ns
t _{off}	turn-off time		-	1055	-	ns
f _T	transition frequency	V _{CE} = 10 V; I _C = 100 mA; f = 100 MHz; T _{amb} = 25 °C	-	175	-	MHz
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	17	-	pF

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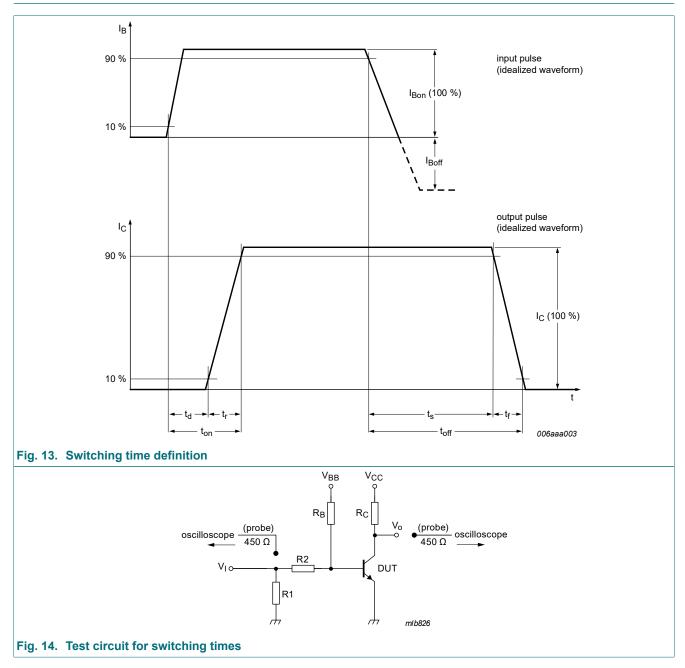


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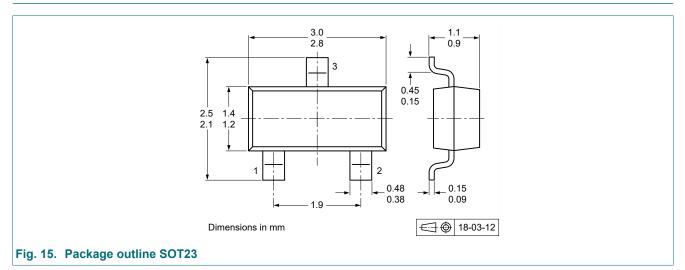
11. Test information



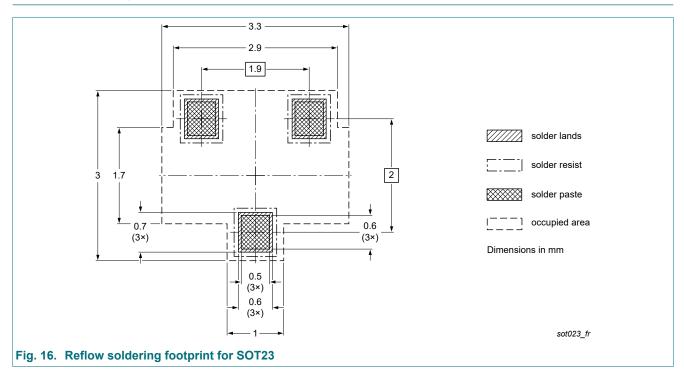
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

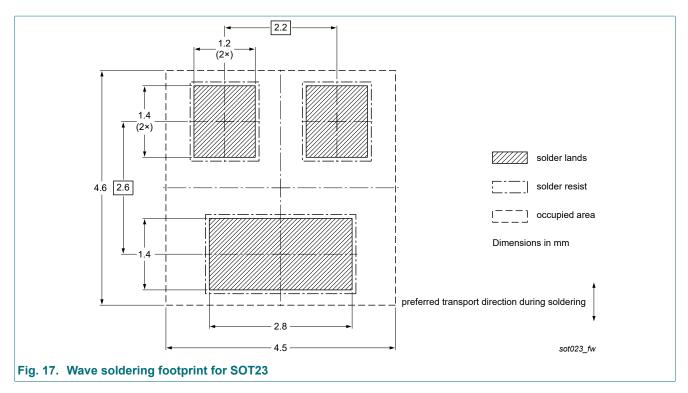
12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PBSS4041NT-Q v.1	20230915	Product data sheet	-	-			

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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