



PBSS4032PZ-Q

30 V, 4.4 A PNP low V_{CEsat} transistor

9 September 2024

Product data sheet

1. General description

PNP low V_{CEsat} transistor in a medium power SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS4032NZ-Q

2. Features and benefits

- Low collector-emitter saturation voltage V_{CEsat}
- Optimized switching time
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- High energy efficiency due to less heat generation
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- DC-to-DC conversion
- Battery-driven devices
- Power management
- Charging circuits

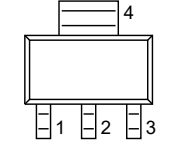
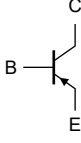
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-30	V
I _C	collector current		-	-	-4.4	A
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	-10	A
R _{CEsat}	collector-emitter saturation resistance	I _C = -4 A; I _B = -400 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	58	86	mΩ

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 SC-73 (SOT223)	 sym028
2	C	collector		
3	E	emitter		
4	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS4032PZ-Q	SC-73	plastic, surface-mounted package with increased heatsink; 4 leads; 2.3 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body	SOT223

7. Marking

Table 4. Marking codes

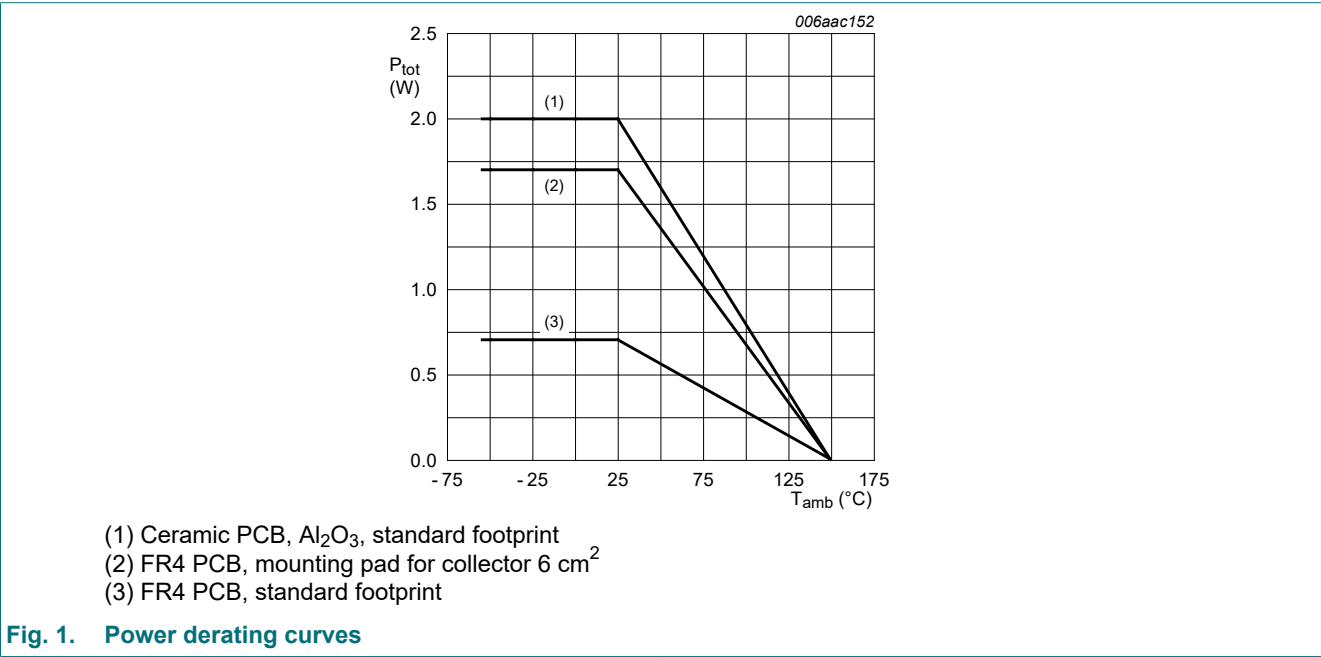
Type number	Marking code
PBSS4032PZ-Q	PB4032 PZ

8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-30	V
V _{CEO}	collector-emitter voltage	open base		-	-30	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-4.4	A
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-10	A
I _B	base current			-	-1	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	700	mW
			[2]	-	1700	mW
			[3]	-	2000	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².
[3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.



9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	180	K/W
			[2]	-	-	75	K/W
			[3]	-	-	65	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	15	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².
[3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

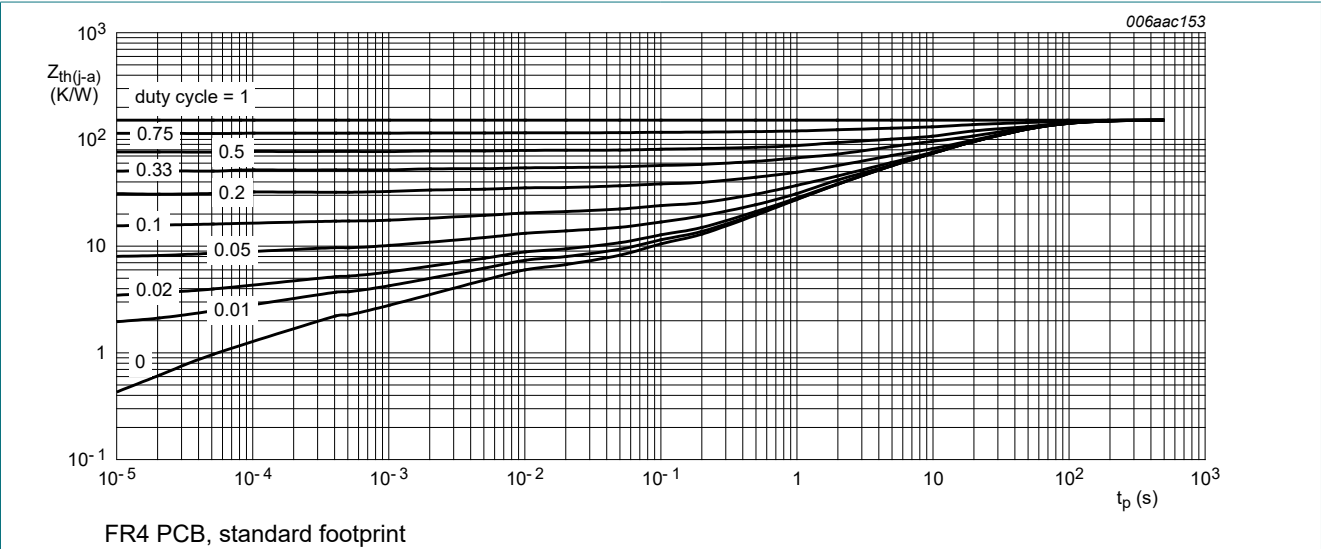


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

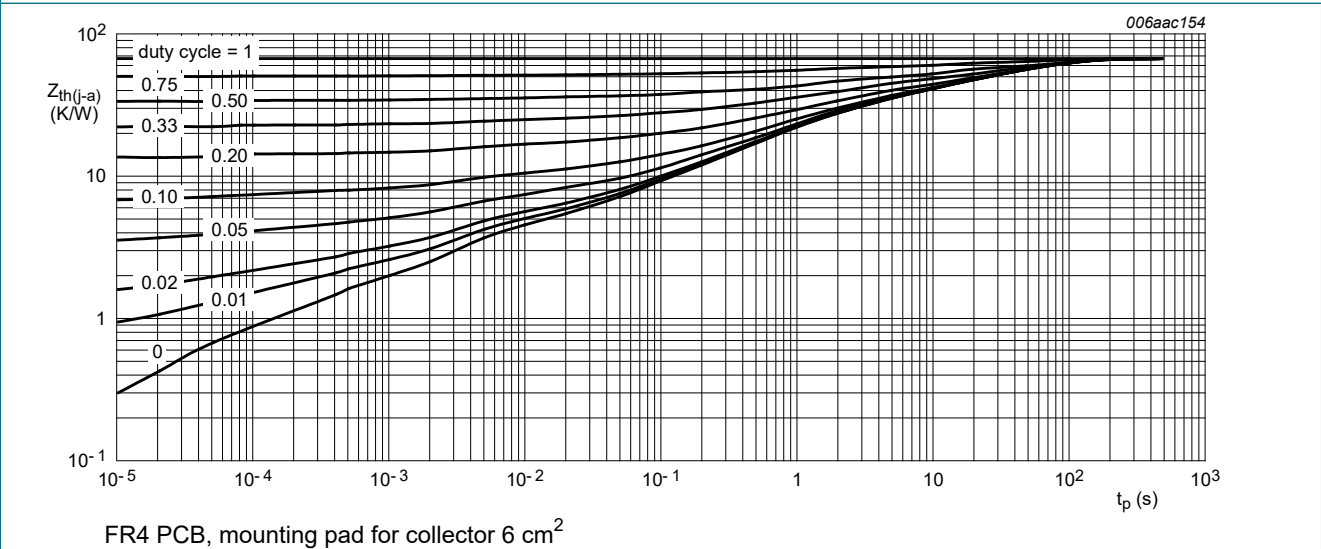
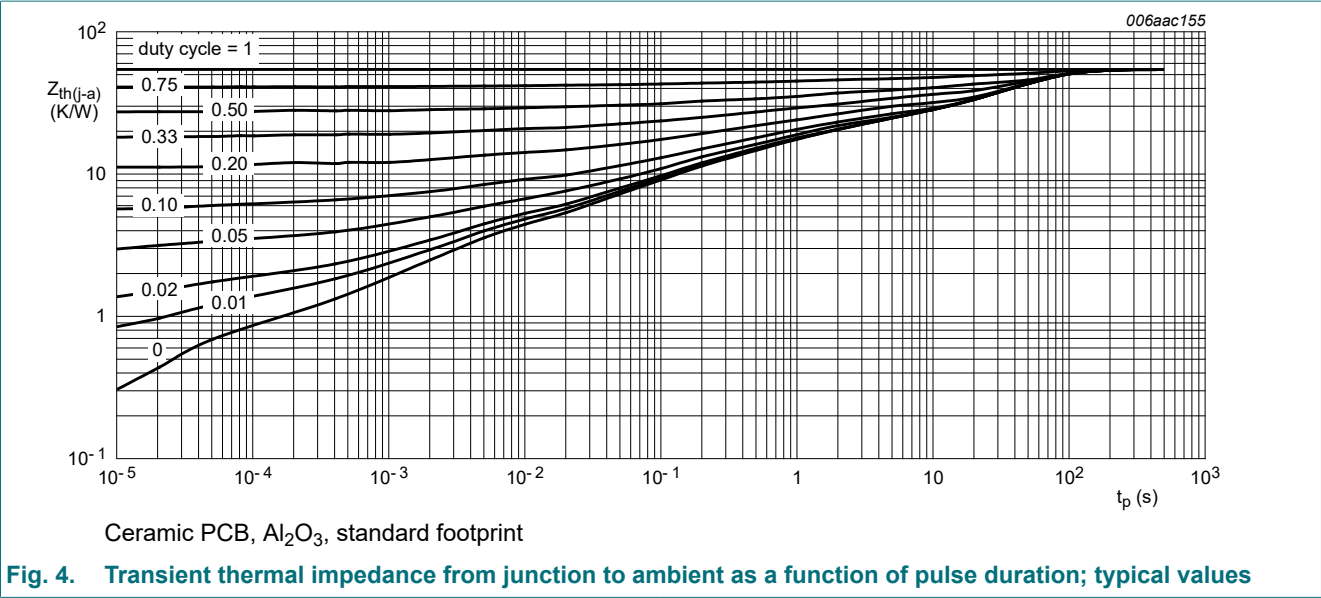


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = -30 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-100	nA
		V _{CB} = -30 V; I _E = 0 A; T _j = 150 °C	-	-	-55	μA
I _{CES}	collector-emitter cut-off current	V _{CE} = -24 V; V _{BE} = 0 V; T _{amb} = 25 °C	-	-	-100	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-100	nA
h _{FE}	DC current gain	V _{CE} = -2 V; I _C = -500 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	200	350	-	
		V _{CE} = -2 V; I _C = -1 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	200	320	-	
		V _{CE} = -2 V; I _C = -2 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	150	240	-	
		V _{CE} = -2 V; I _C = -4 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	60	100	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = -1 A; I _B = -50 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-110	-165	mV
		I _C = -1 A; I _B = -10 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-160	-240	mV
		I _C = -2 A; I _B = -40 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-200	-300	mV
		I _C = -4 A; I _B = -400 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-230	-345	mV
		I _C = -4 A; I _B = -200 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-270	-400	mV
R _{CEsat}	collector-emitter saturation resistance	I _C = -4 A; I _B = -400 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	58	86	mΩ
V _{BEsat}	base-emitter saturation voltage	I _C = -1 A; I _B = -50 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-0.79	-0.9	V
		I _C = -4 A; I _B = -400 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-0.99	-1.05	V
V _{BEon}	base-emitter turn-on voltage	V _{CE} = -2 V; I _C = -2 A; T _{amb} = 25 °C	-	-0.81	-0.9	V
t _d	delay time	V _{CC} = -12.5 V; I _C = -1 A; I _{Bon} = -0.05 A; I _{Boff} = 0.05 A; T _{amb} = 25 °C	-	30	-	ns
t _r	rise time		-	60	-	ns
t _{on}	turn-on time		-	90	-	ns
t _s	storage time		-	140	-	ns
t _f	fall time		-	80	-	ns
t _{off}	turn-off time		-	220	-	ns
f _T	transition frequency	V _{CE} = -10 V; I _C = -100 mA; f = 100 MHz; T _{amb} = 25 °C	-	130	-	MHz
C _c	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	65	-	pF

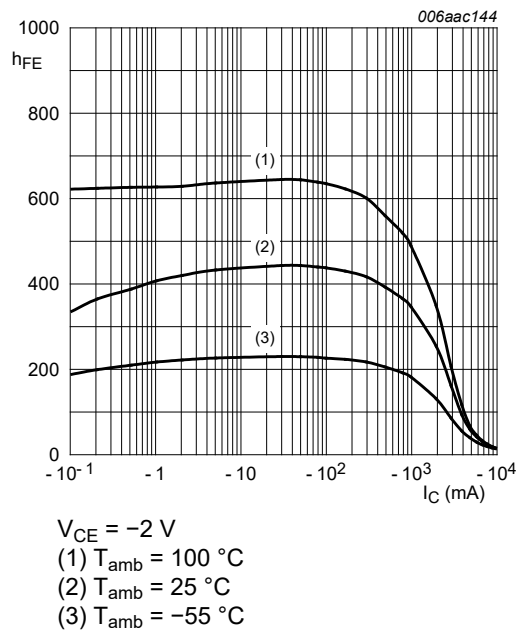


Fig. 5. DC current gain as a function of collector current; typical values

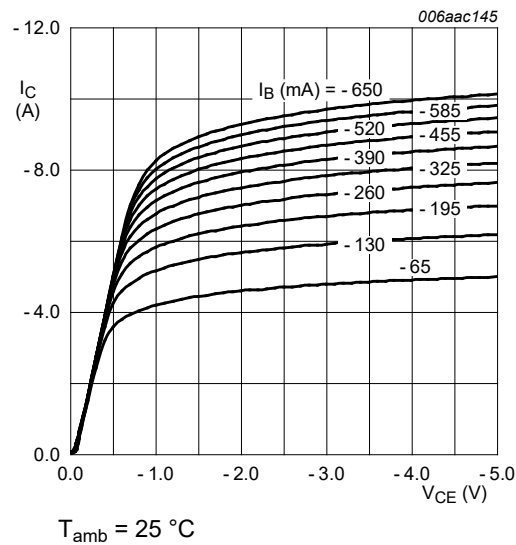


Fig. 6. Collector current as a function of collector-emitter voltage; typical values

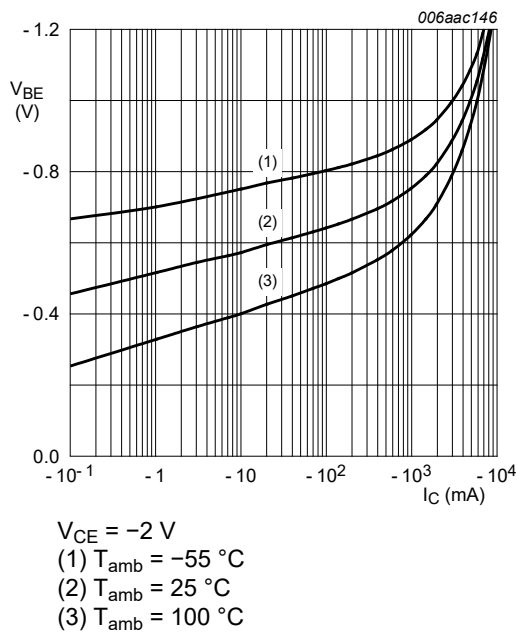


Fig. 7. Base-emitter voltage as a function of collector current; typical values

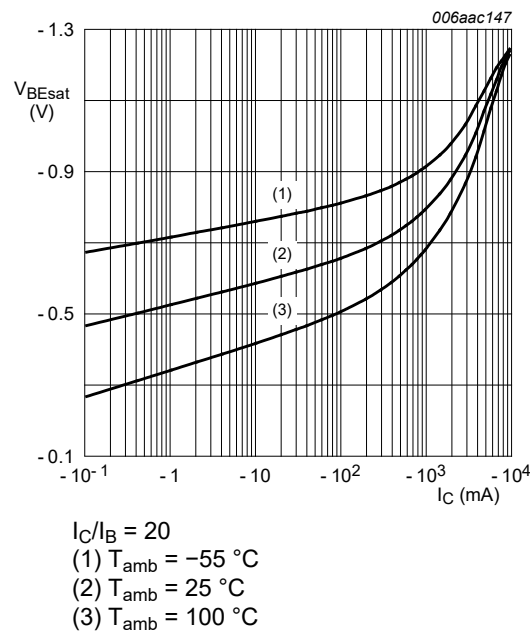


Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values

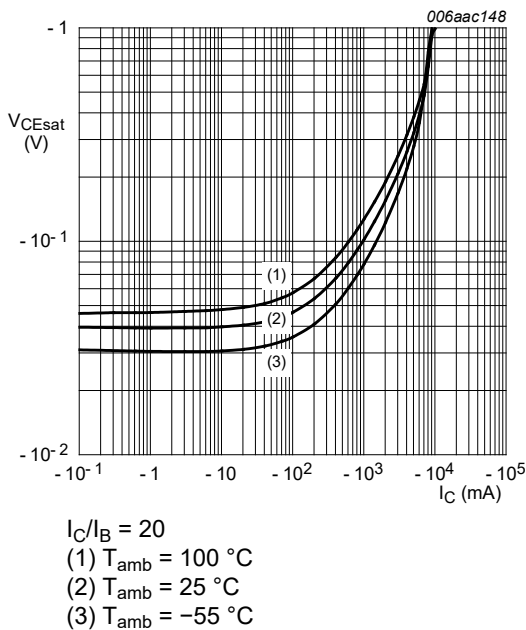


Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values

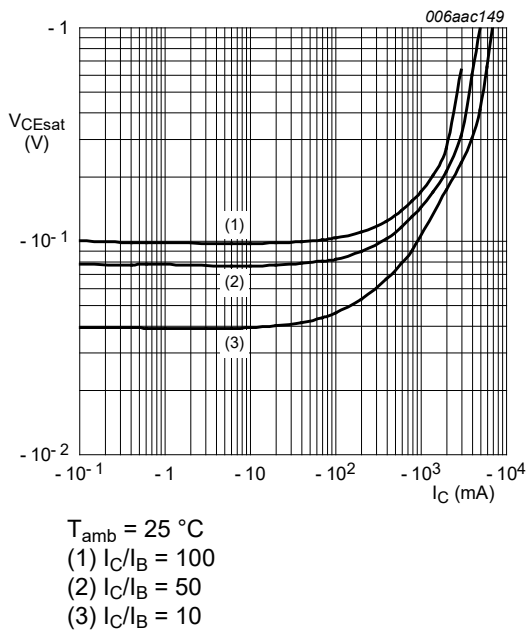


Fig. 10. Collector-emitter saturation voltage as a function of collector current; typical values

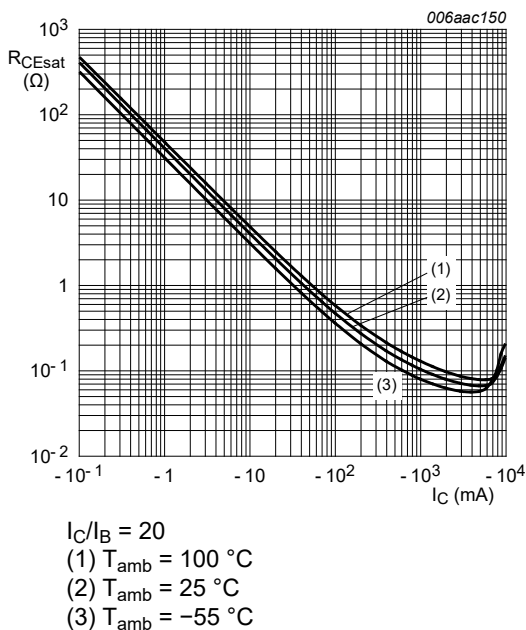


Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values

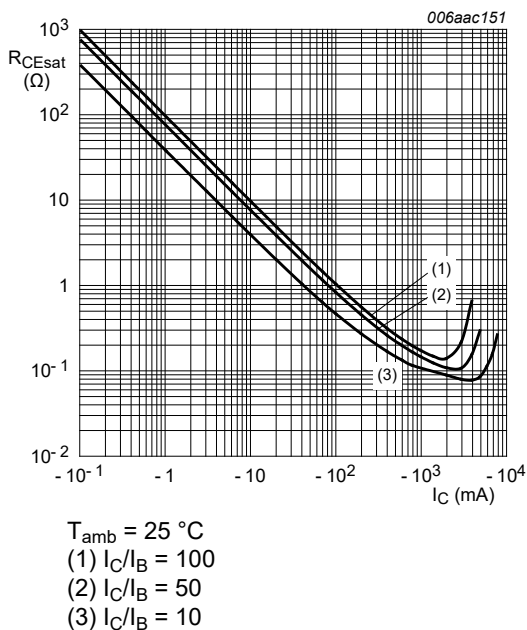


Fig. 12. Collector-emitter saturation resistance as a function of collector current; typical values

11. Test information

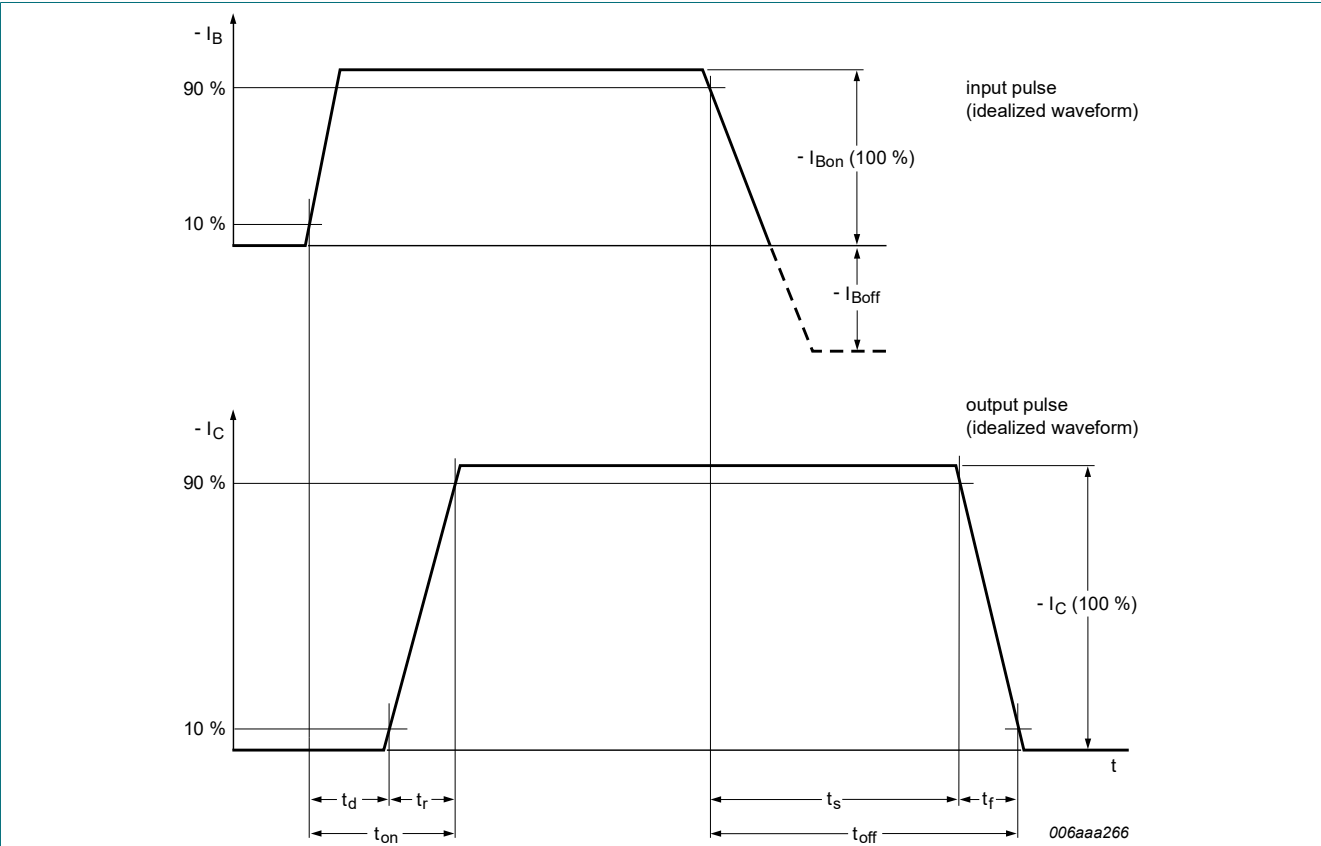


Fig. 13. Transistor switching time definition

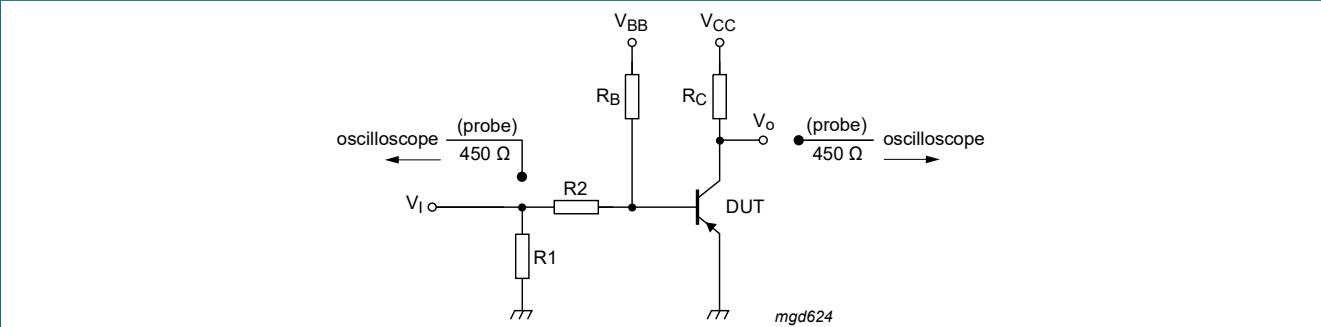
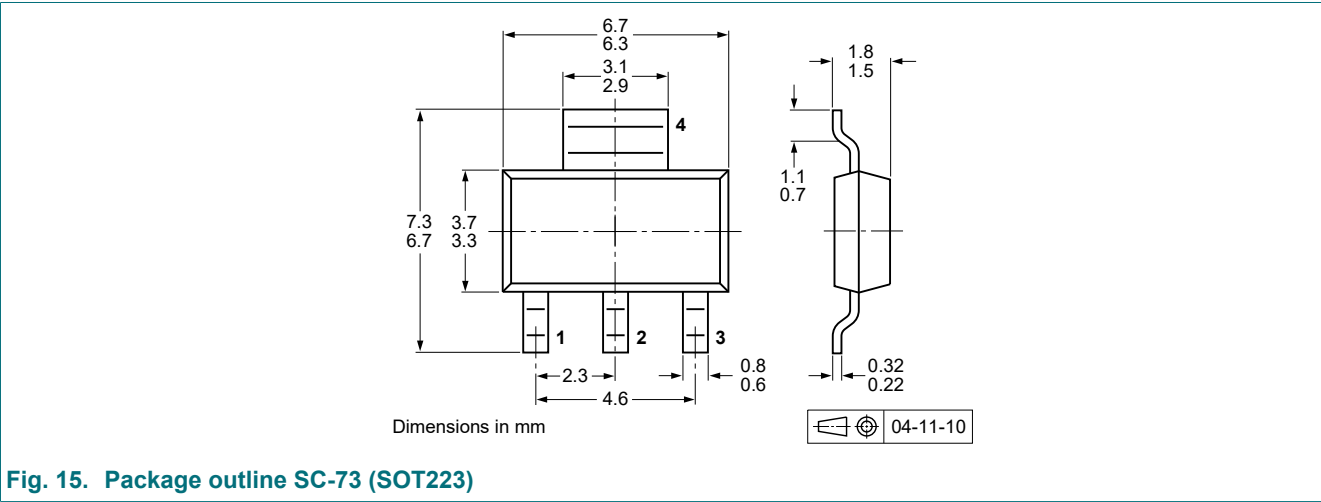


Fig. 14. Test circuit for switching times

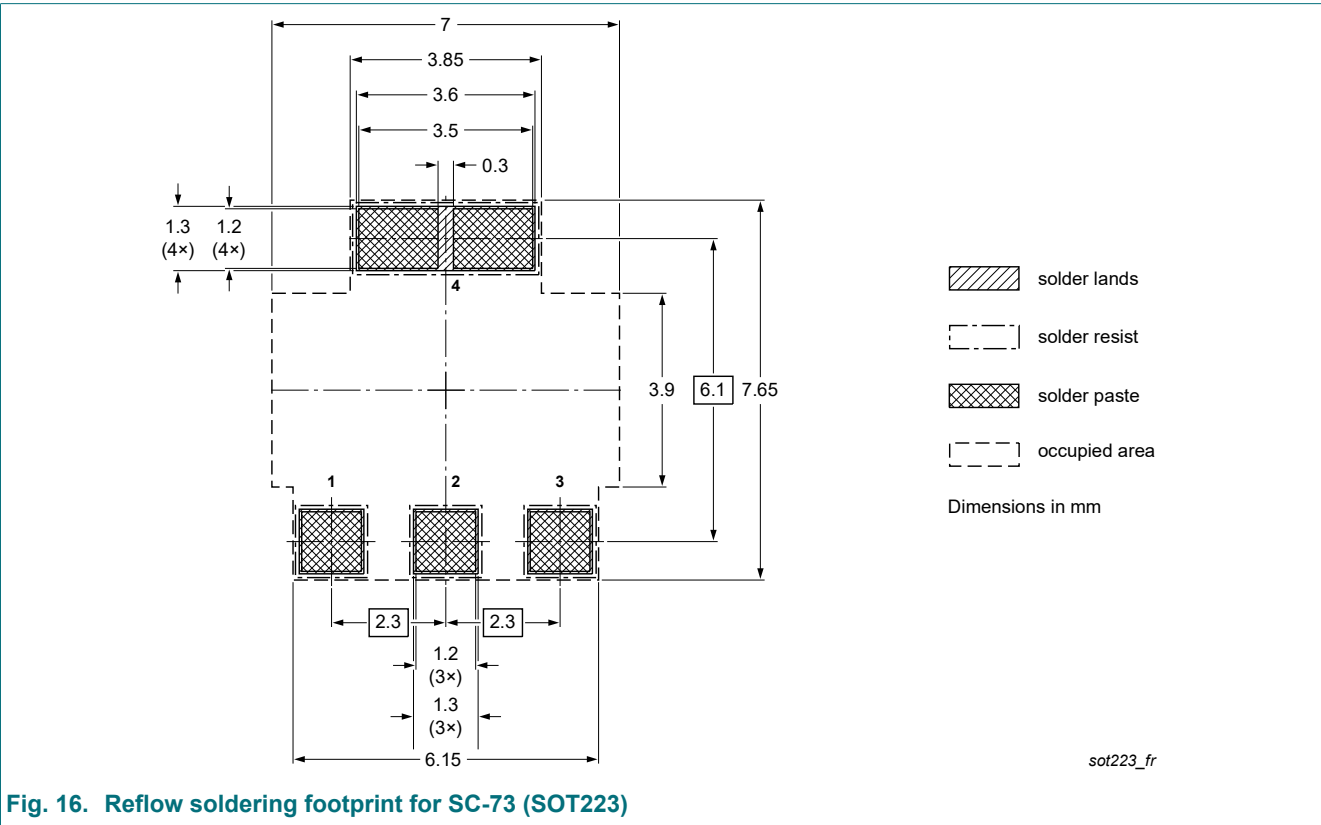
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



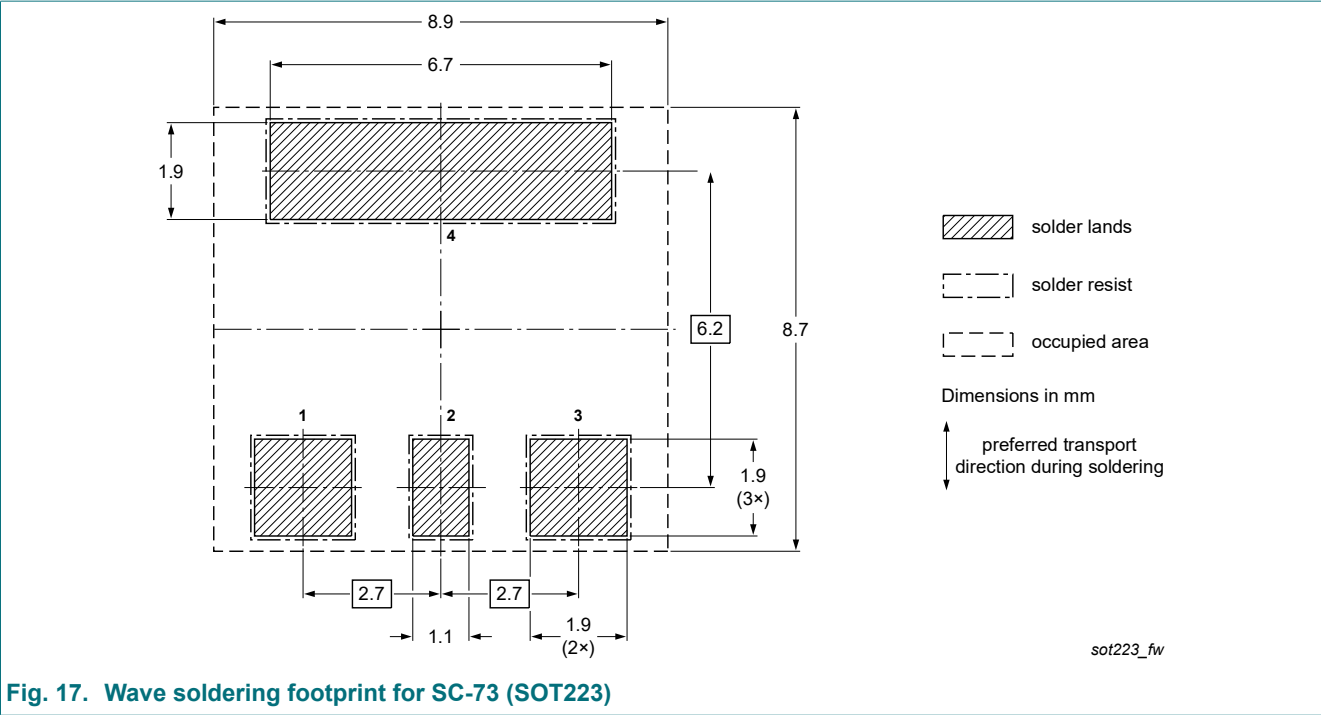


Fig. 17. Wave soldering footprint for SC-73 (SOT223)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4032PZ-Q v.1	20240909	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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